BANDARU PADMA SREE

bandarupadmasree2@gmail.com

6301871206

SUMMARY

An enthusiastic and career minded individual with great communication & interpersonal skills.

CAREER OBJECTIVE

To build a career in the IT industry and put all my efforts into growth of the organisation and have a good working environment

SKILLS

Programming language:	Java
Database:	SQL
Web technologies:	HTML, CSS

PROJECTS

Project Name: Concurrent error detectable carry select adder with easy testability

Role: Co leader	**Team Size:** 4	**Project Duration:** 14 Day

Project Detail

A concurrent error detectable carry select adder with easy testability is a very useful circuit in VLSI design. It is capable of detecting errors during its operation and correcting them in real-time. This feature makes it highly reliable for various applications such as arithmetic and digital signal processing. The carry select adder is composed of two parts: carry generation circuit and sum generation circuit. Carry generation circuit generates the carry signals for each bit position, while the sum generation circuit generates the sum output.

EDUCATION

Degree/Course	Percentage/CGPA	Year of Passing
Bachelor Of Technology	76.09 %	2023
Vaagdevi Institute of Technology and Science , JNTUA		
Intermediate	75.45 %	2019
Abhyas Junior College , Board of Intermediate		
SSC	93 %	2017
Sri Ravindra High School , Board of Secondary Education		

STRENGTHS

Ability to work under pressure, Self motivated, Punctual

HOBBIES

Painting, Photography

PERSONAL DETAILS

Address	D No 4/1238,super bazar road Proddatur, Andhra Pradesh, 516360
Date of Birth	07 February 2002
Gender	Female
Nationality	Indian
Marital Status	Single

Languages Known	English, Hindi, Telugu

DECLARATION

I hereby declare that all the above information is true to best of my knowledge

BANDARU PADMA SREE