Name: Lalitha

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<u>CARRER OBJECTIVE</u>: "To build a career where I can best utilize my knowledge, and skill to achieve organization goals" To work in a progressive and knowledgeable environment where hard work and true dedication serves as a catalyst of growth. And to develop among the individuals and teams a spirit of excellence and commitment and uphold the values and ethics by constantly renewing competencies and thus develop myself and the organization.

BACKGROUND:

Completed my **B.TECH** degree from **JOHNS COLLEGE OF ENGINEERING AND TECHNOLOGY** Affiliated to **JNTUA** with specialization in **(ECE)** with an aggregate of _68.75%

STRENGTHS:

- Ability to adapt to different sets of environment.
- Ability to develop and maintain Good Network.
- Ability to move from and failure to other success with no loss of enthusiasm.
- Straight forwardness.
- Decent academic background.
- Ability to grasp the new skills quickly.

EDUCATIONAL QUALIFICATION:

ACADEMIC QUALIFICATION	INSTITUTE	YEAR	% OF MARKS
B.Tech (ECE)	Johns College Of Engineering And Technology.	2018	68.75
Intermediate	Narayana Junior College	2014	89.5
X class	Nalanda High School	2012	7.7

TECHNICAL:

Language: Basic-C, C++, Java, Manual testing.

Operating systems: Ms-Dos, Windows XP. **Packages:** MS-office.

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PROFESSIONAL EXPEREIENCE

Confidential (Jan 2018- Till date)
Sr. Bench Sales Recruiter

Responsibilities:

- As a Bench Sales Recruiter was involved in selling the bench consultants, including searching, qualifying, scheduling interviews, rate negotiations, and closing.
- Searching the requirements for the allotted bench from portals, groups and with contacts. Sharing the requirements with the consultants by his/her comfort ability used to submit the resume to the recruiter.
- Speaking with the recruiters regarding the rate and getting the best as suggested by management.
- Working directly with end clients' requirements like Tier1 venders/Implementation partners Have Direct Contacts with the T1 Vendors or Implementation Partners in USA.
- Interacting, developing Tier-1 Vendor or Implementation Partners network daily basis to get the H1B/bench candidates placed in minimal turnaround time.
- Marketing H1B/Bench Consultants by posting/submitting their resumes on various job boards/requirements.
- Preparing the CV and suggest any modifications required broadcasting the consultants profile to Vendors on regular basis.
- Negotiate the rate and billing terms and also sort out the issues in NCA/NDA/MSA raised by the legal team with the vendor/s or Implementation Partners.
- Knowledge of Corp-to-Corp, 1099 & W2.
- Negotiating rate with vendors & managing the long-term relations with them.
- Set up interviews and closed candidates for various positions.
- Keeping track of the start and end dates of the bench consultants.
- Process candidates from initial contact through entire interview/placement process which includes interview scheduling and follow up.
- Maintain daily & weekly status lists of all activities through MS Excel.
- Experience on working with any job portals DICE, Monster, Corp to Corp, Career Builders etc.
- Maintaining friendly rapport with the consultants and making them aware of submissions, vendor/Implementation partner's calls and client interviews.
- Handling post interview aspects such as follow-up with the vendor for interview feedback, POs Joining and MSA.
- Keeping the management in loop on clients' interviews, project start dates etc.
- Coordinating with the consultant till he reports to the clients reporting manager.
- Aggressive, dynamic & result oriented with strong communication skills.
- Highly self-motivated and self-directed.

ACADEMIC PROJECT (B.TECH):

Title: Emerging Power-Gating Techniques for Ultra Low Power Full Adder.

Project Role: Most SOC design teams now regard power as one of their top design concerns since, due to high power dissipation battery lifetime is reduced especially for portable devices so for having longbattery lifetime we require low-power design.

Team size: 4

Tools: DSCH 2.0, MICROWIND 2

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Project Description: Miniaturization of ICS results in more leakage power which creates a critical problem in modern low power microprocessor. In this project, an industry-standard technique known as powergating is explored, whereby transistors are used to disconnect the power from idle portions of a chip. Present power-gating implementations suffer from limitations including non-zero off-state leakage, which can aggregate to a large amount of wasted energy during long idle periods, and high energy overhead, which limits its use to long-term system- wide sleep modes. In this project we use elective power gating which is hardware oriented and eliminates leakage power. This lowers static power consumption of microprocessors. Here we use SERF adder circuits to calculate power consumption using Power Gating Techniques.

<u>SHORT TERM PLANS</u>: To see myself in a responsible position in a corporate environment.

<u>MEDIUM TERM PLANS</u>: To see myself in an authoritative position involved in strategic decision making.

<u>LONG TERM PLANS</u>: To do work what I like to do and your company can provide me that opportunity.

Hobbies:

Gardening & Cooking, Listening MUSIC & Playing CHESS.

ACHIEVEMENT:

- Got several prizes in school and College level competitions.
- Represented our college in various National Level Technical Competitions.
- Done courses on DOM Certificate in Integrated Institute of Advanced Studies.
- Participated in 2 Day State Level Workshop on MATLAB.

DECLARATION:

I hereby declare that the details furnished above are true and correct to the best of my knowledge and belief.

PERSONAL PROFILE:

NAME : Lalitha

NATIONALITY : Indian

LANGUAGES KNOWN : Telugu, English and Kannada.

EMAIL ID : lalitham2288@gmail.com

PERMANENT ADDRES : Andhra Pradesh.