Planar_Controller

Design Description

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Planar_Controller: Design Description

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Publication date 14-Apr-2022 04:01:40
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Chapter 1. Model Version

Version: 1.2

Last modified: Thu Apr 14 08:41:33 2022

Checksum: 827792789 1137522301 3832176497 2264170400

Chapter 2. Root System

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Figure 2.1. Planar_Controller

EQUATIONS:
z" = u1/m - g
phi" = u2/Ixx
y" = g*phi
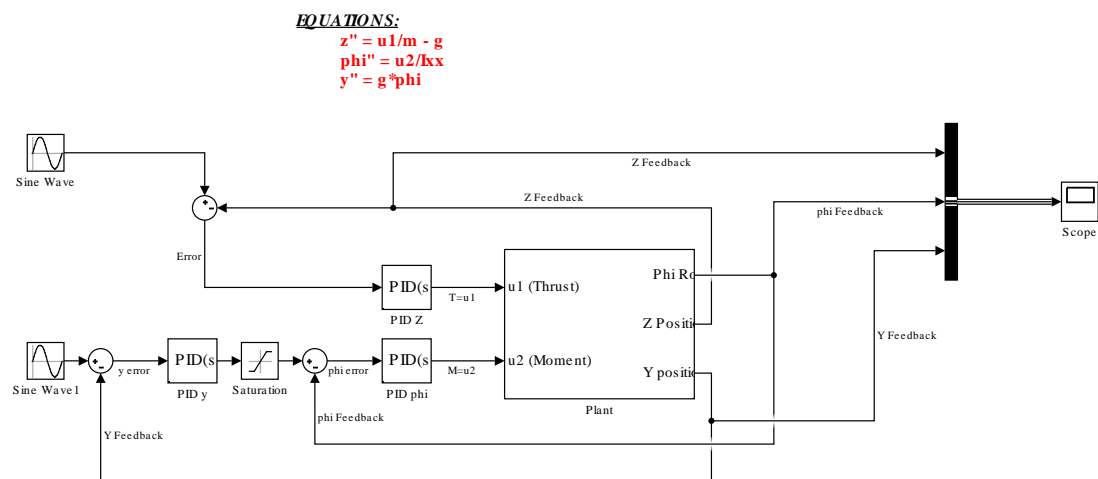
2.1. Blocks

2.1.1. Parameters

2.1.1.1. "Bus Creator" (BusCreator)

Table 2.1. "Bus Creator" Parameters

Parameter	Value
Number of inputs	3
Display option	bar
Data type	Inherit: auto
Output as nonvirtual bus	off
Override bus signal names from inputs	on

2.1.1.2. "Saturation" (Saturate)

Table 2.2. "Saturation" Parameters

Parameter	Value
Upper limit	0.8
Lower limit	-0.8
Treat as gain when linearizing	on
Enable zero-crossing detection	on
Sample time (-1 for inherited)	-1
Output minimum	[]
Output maximum	[]
Output data type	Inherit: Same as input
Lock output data type setting against changes by the fixed-point tools	off
Integer rounding mode	Floor

2.1.1.3. "Sine Wave" (Sin)

Table 2.3. "Sine Wave" Parameters

Parameter	Value
Sine type	Time based
Time (t)	Use simulation time
Amplitude	0.5
Bias	0
Frequency (rad/sec)	2
Phase (rad)	0
Samples per period	10
Number of offset samples	0
Sample time	0
Interpret vector parameters as 1-D	on

2.1.1.4. "Sine Wave1" (Sin)

Table 2.4. "Sine Wave1" Parameters

Parameter	Value
Sine type	Time based
Time (t)	Use simulation time
Amplitude	0.5
Bias	0
Frequency (rad/sec)	2
Phase (rad)	0
Samples per period	10
Number of offset samples	0
Sample time	0
Interpret vector parameters as 1-D	on

2.1.1.5. "Sum" (Sum)

Table 2.5. "Sum" Parameters

Parameter	Value
Icon shape	round
List of signs	|+-
Sum over	All dimensions
Dimension	1
Require all inputs to have the same data type	off
Accumulator data type	Inherit: Inherit via internal rule
Output minimum	[]
Output maximum	[]
Output data type	Inherit: Inherit via internal rule
Lock data type settings against changes by the fixed-point tools	off
Integer rounding mode	Floor
Saturate on integer overflow	off
Sample time (-1 for inherited)	-1

2.1.1.6. "Sum1" (Sum)

Table 2.6. "Sum1" Parameters

Parameter	Value
Icon shape	round
List of signs	|+-
Sum over	All dimensions
Dimension	1
Require all inputs to have the same data type	off
Accumulator data type	Inherit: Inherit via internal rule
Output minimum	[]
Output maximum	[]
Output data type	Inherit: Inherit via internal rule
Lock data type settings against changes by the fixed-point tools	off
Integer rounding mode	Floor
Saturate on integer overflow	off
Sample time (-1 for inherited)	-1

2.1.1.7. "Sum2" (Sum)

Table 2.7. "Sum2" Parameters

Parameter	Value
Icon shape	round
List of signs	|+-
Sum over	All dimensions
Dimension	1
Require all inputs to have the same data type	off
Accumulator data type	Inherit: Inherit via internal rule
Output minimum	[]
Output maximum	[]
Output data type	Inherit: Inherit via internal rule
Lock data type settings against changes by the fixed-point tools	off

Parameter	Value
Integer rounding mode	Floor
Saturate on integer overflow	off
Sample time (-1 for inherited)	-1

2.1.2. Block Execution Order

1. Z [21] (Integrator)
2. PHI [16] (Integrator)
3. Y [19] (Integrator)
4. Scope [3] (Scope)
5. Sine Wave [3] (Sin)
6. Sum [4] (Sum)
7. Derivative Gain [7] (Gain)
8. Filter [8] (Integrator)
9. SumD [11] (Sum)
10. Filter Coefficient [8] (Gain)
11. Integral Gain [9] (Gain)
12. Integrator [10] (Integrator)
13. Proportional Gain [10] (Gain)
14. Sum [11] (Sum)
15. Sine Wave1 [3] (Sin)
16. Sum2 [5] (Sum)
17. Proportional Gain (Gain)
18. Integrator (Integrator)
19. Derivative Gain (Gain)
20. Filter (Integrator)
21. SumD (Sum)
22. Filter Coefficient (Gain)
23. Sum (Sum)
24. Saturation [2] (Saturate)
25. Sum1 [4] (Sum)
26. Derivative Gain (Gain)
27. Filter (Integrator)
28. SumD (Sum)
29. Filter Coefficient (Gain)
30. Integral Gain (Gain)
31. Integrator (Integrator)
32. Proportional Gain (Gain)
33. Sum (Sum)
34. Integral Gain (Gain)
35. 1/Ixx [14] (Gain)
36. PHI˙ [17] (Integrator)
37. Y' [21] (Integrator)
38. g [15] (Gain)
39. 1/m [15] (Gain)
40. Gravity [16] (Constant)
41. Sum [18] (Sum)
42. Z˙ [22] (Integrator)

Chapter 3. Subsystems

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3.1. PID Z

Figure 3.1. Planar_Controller/PID Z
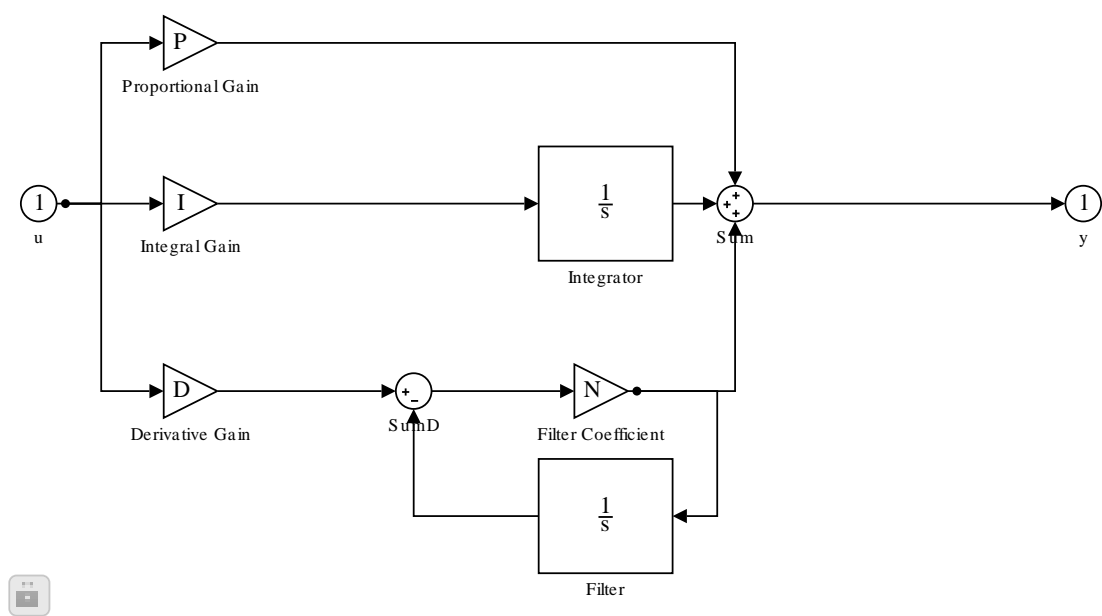
3.1.1. Blocks

3.1.1.1. Parameters

3.1.1.1.1. "Derivative Gain" (Gain)

Table 3.1. "Derivative Gain" Parameters

Parameter	Value
Gain	D
Multiplication	Element-wise(K.*u)
Parameter minimum	DParamMin

Parameter	Value
Parameter maximum	DParamMax
Parameter data type	Inherit: Inherit via internal rule
Output minimum	DOutMin
Output maximum	DOutMax
Output data type	Inherit: Inherit via internal rule
Lock output data type setting against changes by the fixed-point tools	off
Integer rounding mode	Floor
Saturate on integer overflow	off
Sample time (-1 for inherited)	SampleTime

3.1.1.1.2. "Filter" (Integrator)

Table 3.2. "Filter" Parameters

Parameter	Value
External reset	none
Initial condition source	internal
Initial condition	InitialConditionForFilter
Limit output	off
Upper saturation limit	inf
Lower saturation limit	-inf
Wrap state	off
Wrapped state upper value	pi
Wrapped state lower value	-pi
Show saturation port	off
Show state port	off
Ignore limit and reset when linearizing	off
Enable zero-crossing detection	on
State Name (e.g., 'position')	"

3.1.1.1.3. "Filter Coefficient" (Gain)

Table 3.3. "Filter Coefficient" Parameters

Parameter	Value
Gain	N
Multiplication	Element-wise(K.*u)
Parameter minimum	NParamMin
Parameter maximum	NParamMax
Parameter data type	Inherit: Inherit via internal rule
Output minimum	NOutMin
Output maximum	NOutMax
Output data type	Inherit: Inherit via internal rule
Lock output data type setting against changes by the fixed-point tools	off
Integer rounding mode	Floor
Saturate on integer overflow	off
Sample time (-1 for inherited)	SampleTime

3.1.1.1.4. "Integral Gain" (Gain)

Table 3.4. "Integral Gain" Parameters

Parameter	Value
Gain	I
Multiplication	Element-wise(K.*u)
Parameter minimum	IParamMin
Parameter maximum	IParamMax
Parameter data type	Inherit: Inherit via internal rule
Output minimum	IOutMin
Output maximum	IOutMax
Output data type	Inherit: Inherit via internal rule
Lock output data type setting against changes by the fixed-point tools	off
Integer rounding mode	Floor
Saturate on integer overflow	off
Sample time (-1 for inherited)	SampleTime

3.1.1.1.5. "Integrator" (Integrator)

Table 3.5. "Integrator" Parameters

Parameter	Value
External reset	none
Initial condition source	internal
Initial condition	InitialConditionForIntegrator
Limit output	off
Upper saturation limit	inf
Lower saturation limit	-inf
Wrap state	off
Wrapped state upper value	pi
Wrapped state lower value	-pi
Show saturation port	off
Show state port	off
Ignore limit and reset when linearizing	off
Enable zero-crossing detection	on
State Name (e.g., 'position')	"

3.1.1.1.6. "Proportional Gain" (Gain)

Table 3.6. "Proportional Gain" Parameters

Parameter	Value
Gain	P
Multiplication	Element-wise(K.*u)
Parameter minimum	PParamMin
Parameter maximum	PParamMax
Parameter data type	Inherit: Inherit via internal rule
Output minimum	POutMin
Output maximum	POutMax
Output data type	Inherit: Inherit via internal rule
Lock output data type setting against changes by the fixed-point tools	off
Integer rounding mode	Floor

Parameter	Value
Saturate on integer overflow	off
Sample time (-1 for inherited)	SampleTime

3.1.1.1.7. "Sum" (Sum)

Table 3.7. "Sum" Parameters

Parameter	Value
Icon shape	round
List of signs	+++
Sum over	All dimensions
Dimension	1
Require all inputs to have the same data type	off
Accumulator data type	Inherit: Inherit via internal rule
Output minimum	SumOutMin
Output maximum	SumOutMax
Output data type	Inherit: Inherit via internal rule
Lock data type settings against changes by the fixed-point tools	off
Integer rounding mode	Floor
Saturate on integer overflow	off
Sample time (-1 for inherited)	SampleTime

3.1.1.1.8. "SumD" (Sum)

Table 3.8. "SumD" Parameters

Parameter	Value
Icon shape	round
List of signs	|+-
Sum over	All dimensions
Dimension	1
Require all inputs to have the same data type	off
Accumulator data type	Inherit: Inherit via internal rule
Output minimum	SumDOutMin

Parameter	Value
Output maximum	SumDOutMax
Output data type	Inherit: Inherit via internal rule
Lock data type settings against changes by the fixed-point tools	off
Integer rounding mode	Floor
Saturate on integer overflow	off
Sample time (-1 for inherited)	SampleTime

3.1.1.1.9. "u" (Inport)

Table 3.9. "u" Parameters

Parameter	Value
Port number	1
Port dimensions (-1 for inherited)	-1
Sample time (-1 for inherited)	SampleTime
Minimum	[]
Maximum	[]
Data type	Inherit: auto

3.1.1.1.10. "y" (Outport)

Table 3.10. "y" Parameters

Parameter	Value
Port number	1
Icon display	Port number
Minimum	[]
Maximum	[]
Data type	Inherit: auto
Lock output data type setting against changes by the fixed-point tools	off
Output as nonvirtual bus in parent model	off
Unit (e.g., m, m/s^2, N*-m)	inherit

Parameter	Value
Port dimensions (-1 for inherited)	-1
Variable-size signal	Inherit
Sample time (-1 for inherited)	SampleTime
Source of initial output value	Dialog
Output when disabled	held
Initial output	0

3.2. Plant

Figure 3.2. Planar_Controller/Plant

z" = u1/m - g

phi" = u2/Ixx
y" = g*phi
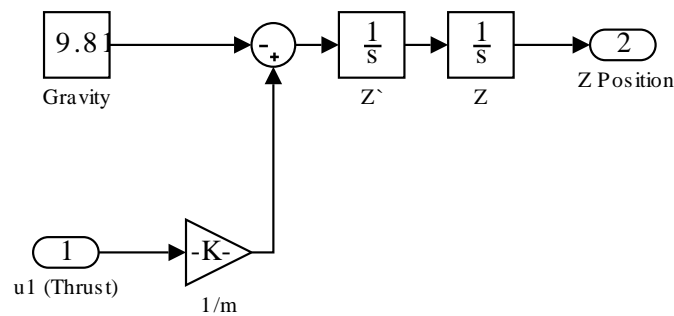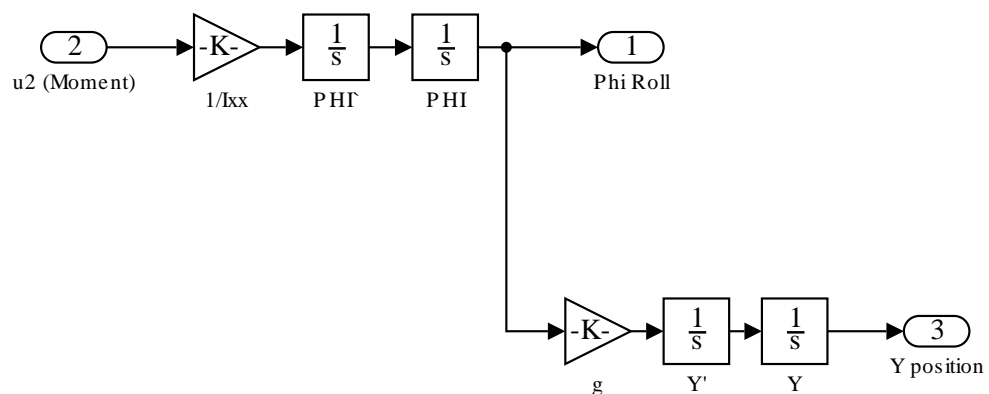
3.2.1. Blocks

3.2.1.1. Parameters

3.2.1.1.1. " Z Position" (Outport)

Table 3.11. " Z Position" Parameters

Parameter	Value
Port number	2
Icon display	Port number
Minimum	[]
Maximum	[]
Data type	Inherit: auto
Lock output data type setting against changes by the fixed-point tools	off
Output as nonvirtual bus in parent model	off
Unit (e.g., m, m/s^2, N*-m)	inherit
Port dimensions (-1 for inherited)	-1
Variable-size signal	Inherit
Sample time (-1 for inherited)	-1
Source of initial output value	Dialog
Output when disabled	held
Initial output	[]

3.2.1.1.2. "1/Ixx" (Gain)

Table 3.12. "1/Ixx" Parameters

Parameter	Value
Gain	1/0.1
Multiplication	Element-wise(K.*u)
Parameter minimum	[]
Parameter maximum	[]
Parameter data type	Inherit: Inherit via internal rule
Output minimum	[]

Parameter	Value
Output maximum	[]
Output data type	Inherit: Inherit via internal rule
Lock output data type setting against changes by the fixed-point tools	off
Integer rounding mode	Floor
Saturate on integer overflow	off
Sample time (-1 for inherited)	-1

3.2.1.1.3. "1/m" (Gain)

Table 3.13. "1/m" Parameters

Parameter	Value
Gain	1/0.2
Multiplication	Element-wise(K.*u)
Parameter minimum	[]
Parameter maximum	[]
Parameter data type	Inherit: Inherit via internal rule
Output minimum	[]
Output maximum	[]
Output data type	Inherit: Inherit via internal rule
Lock output data type setting against changes by the fixed-point tools	off
Integer rounding mode	Floor
Saturate on integer overflow	off
Sample time (-1 for inherited)	-1

3.2.1.1.4. "g" (Gain)

Table 3.14. "g" Parameters

Parameter	Value
Gain	9.81
Multiplication	Element-wise(K.*u)
Parameter minimum	[]

Parameter	Value
Parameter maximum	[]
Parameter data type	Inherit: Inherit via internal rule
Output minimum	[]
Output maximum	[]
Output data type	Inherit: Inherit via internal rule
Lock output data type setting against changes by the fixed-point tools	off
Integer rounding mode	Floor
Saturate on integer overflow	off
Sample time (-1 for inherited)	-1

3.2.1.1.5. "Gravity " (Constant)

Table 3.15. "Gravity " Parameters

Parameter	Value
Constant value	9.81
Interpret vector parameters as 1-D	on
Output minimum	[]
Output maximum	[]
Output data type	Inherit: Inherit from 'Constant value'
Lock output data type setting against changes by the fixed-point tools	off
Sample time	inf
Frame period	inf

3.2.1.1.6. "PHI" (Integrator)

Table 3.16. "PHI" Parameters

Parameter	Value
External reset	none
Initial condition source	internal
Initial condition	0
Limit output	off
Upper saturation limit	inf

Parameter	Value
Lower saturation limit	-inf
Wrap state	off
Wrapped state upper value	pi
Wrapped state lower value	-pi
Show saturation port	off
Show state port	off
Ignore limit and reset when linearizing	off
Enable zero-crossing detection	on
State Name (e.g., 'position')	''

3.2.1.1.7. "Phi Roll" (Outport)

Table 3.17. "Phi Roll" Parameters

Parameter	Value
Port number	1
Icon display	Port number
Minimum	[]
Maximum	[]
Data type	Inherit: auto
Lock output data type setting against changes by the fixed-point tools	off
Output as nonvirtual bus in parent model	off
Unit (e.g., m, m/s^2, N*-m)	inherit
Port dimensions (-1 for inherited)	-1
Variable-size signal	Inherit
Sample time (-1 for inherited)	-1
Source of initial output value	Dialog
Output when disabled	held
Initial output	[]

3.2.1.1.8. "PHI`" (Integrator)

Table 3.18. "PHI`" Parameters

Parameter	Value
External reset	none
Initial condition source	internal
Initial condition	0
Limit output	off
Upper saturation limit	inf
Lower saturation limit	-inf
Wrap state	off
Wrapped state upper value	pi
Wrapped state lower value	-pi
Show saturation port	off
Show state port	off
Ignore limit and reset when linearizing	off
Enable zero-crossing detection	on
State Name (e.g., 'position')	"

3.2.1.1.9. "Sum" (Sum)

Table 3.19. "Sum" Parameters

Parameter	Value
Icon shape	round
List of signs	|-+
Sum over	All dimensions
Dimension	1
Require all inputs to have the same data type	off
Accumulator data type	Inherit: Inherit via internal rule
Output minimum	[]
Output maximum	[]
Output data type	Inherit: Inherit via internal rule
Lock data type settings against changes by the fixed-point tools	off

Parameter	Value
Integer rounding mode	Floor
Saturate on integer overflow	off
Sample time (-1 for inherited)	-1

3.2.1.1.10. "u1 (Thrust)" (Inport)

Table 3.20. "u1 (Thrust)" Parameters

Parameter	Value
Port number	1
Port dimensions (-1 for inherited)	-1
Sample time (-1 for inherited)	-1
Minimum	[]
Maximum	[]
Data type	Inherit: auto

3.2.1.1.11. "u2 (Moment)" (Inport)

Table 3.21. "u2 (Moment)" Parameters

Parameter	Value
Port number	2
Port dimensions (-1 for inherited)	-1
Sample time (-1 for inherited)	-1
Minimum	[]
Maximum	[]
Data type	Inherit: auto

3.2.1.1.12. "Y" (Integrator)

Table 3.22. "Y" Parameters

Parameter	Value
External reset	none

Parameter	Value
Initial condition source	internal
Initial condition	0
Limit output	off
Upper saturation limit	inf
Lower saturation limit	-inf
Wrap state	off
Wrapped state upper value	pi
Wrapped state lower value	-pi
Show saturation port	off
Show state port	off
Ignore limit and reset when linearizing	off
Enable zero-crossing detection	on
State Name (e.g., 'position')	"

3.2.1.1.13. "Y position" (Outport)

Table 3.23. "Y position" Parameters

Parameter	Value
Port number	3
Icon display	Port number
Minimum	[]
Maximum	[]
Data type	Inherit: auto
Lock output data type setting against changes by the fixed-point tools	off
Output as nonvirtual bus in parent model	off
Unit (e.g., m, m/s^2, N*-m)	inherit
Port dimensions (-1 for inherited)	-1
Variable-size signal	Inherit
Sample time (-1 for inherited)	-1

Parameter	Value
Source of initial output value	Dialog
Output when disabled	held
Initial output	[]

3.2.1.1.14. "Y'" (Integrator)

Table 3.24. "Y'" Parameters

Parameter	Value
External reset	none
Initial condition source	internal
Initial condition	0
Limit output	off
Upper saturation limit	inf
Lower saturation limit	-inf
Wrap state	off
Wrapped state upper value	pi
Wrapped state lower value	-pi
Show saturation port	off
Show state port	off
Ignore limit and reset when linearizing	off
Enable zero-crossing detection	on
State Name (e.g., 'position')	''

3.2.1.1.15. "Z" (Integrator)

Table 3.25. "Z" Parameters

Parameter	Value
External reset	none
Initial condition source	internal
Initial condition	0
Limit output	off
Upper saturation limit	inf

Parameter	Value
Lower saturation limit	-inf
Wrap state	off
Wrapped state upper value	pi
Wrapped state lower value	-pi
Show saturation port	off
Show state port	off
Ignore limit and reset when linearizing	off
Enable zero-crossing detection	on
State Name (e.g., 'position')	"

3.2.1.1.16. "Z`" (Integrator)

Table 3.26. "Z`" Parameters

Parameter	Value
External reset	none
Initial condition source	internal
Initial condition	0
Limit output	off
Upper saturation limit	inf
Lower saturation limit	-inf
Wrap state	off
Wrapped state upper value	pi
Wrapped state lower value	-pi
Show saturation port	off
Show state port	off
Ignore limit and reset when linearizing	off
Enable zero-crossing detection	on
State Name (e.g., 'position')	"

Chapter 4. System Design Variables

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4.1. Design Variable Summary

Table 4.1. Functions used in Design Variable Expressions

Function Na-me	Parent Blocks	Calling string
pi	PHI [16]	pi
	PHI [16]	-pi
	PHIˋ [17]	pi
	PHIˋ [17]	-pi
	Y [19]	pi
	Y [19]	-pi
	Y' [21]	pi
	Y' [21]	-pi
	Z [21]	pi
	Z [21]	-pi
	Zˋ [22]	pi
	Zˋ [22]	-pi

Chapter 5. Requirements Traceability

Planar_Controller does not contain requirements traceability links.

Chapter 6. System Model Configuration

Source: Model

Source Name: Planar_Controller

Table 6.1. Planar_Controller Configuration Set

Property	Value
Description	
Components	[Planar_Controller Configuration Set.Components-(1) [25], Planar_Controller Configuration Set.-Components(2) [26], Planar_Controller Configuration Set.Components(3) [27], Planar_Controller Configuration Set.Components(4) [28], Planar_Controller Configuration Set.Components-(5) [31], Planar_Controller Configuration Set.-Components(6) [32], Planar_Controller Configuration Set.Components(7) [32], Planar_Controller Configuration Set.Components(8) [33], Planar_Controller Configuration Set.Components-(9) [35], Planar_Controller Configuration Set.-Components(10) [36]]
Name	Configuration
SimulationMode	normal
ConfigType	Model

Table 6.2. Planar_Controller Configuration Set.Components [25](1)

Property	Value
Name	Solver
Description	
Components	
StartTime	0.0
StopTime	10.0
AbsTol	auto
FixedStep	auto
InitialStep	auto
MaxNumMinSteps	-1
MaxOrder	5
ZcThreshold	auto
ConsecutiveZCsStepRelTol	10*128*eps
MaxConsecutiveZCs	1000
ExtrapolationOrder	4
NumberNewtonIterations	1

MaxStep	auto
MinStep	auto
MaxConsecutiveMinStep	1
RelTol	1e-3
SolverMode	Auto
EnableConcurrentExecution	off
ConcurrentTasks	off
Solver	VariableStepAuto
SolverName	VariableStepAuto
SolverType	Variable-step
SolverJacobianMethodControl	auto
ShapePreserveControl	DisableAll
ZeroCrossControl	UseLocalSettings
ZeroCrossAlgorithm	Nonadaptive
SolverResetMethod	Fast
PositivePriorityOrder	off
AutoInsertRateTranBlk	off
SampleTimeConstraint	Unconstrained
InsertRTBMode	Whenever possible
SampleTimeProperty	

Table 6.3. Planar_Controller Configuration Set.Components [25](2)

Property	Value
Name	Data Import/Export
Description	
Components	
Decimation	1
ExternalInput	[t, u]
FinalStateName	xFinal
InitialState	xInitial
LimitDataPoints	off
MaxDataPoints	1000
LoadExternalInput	off
LoadInitialState	off
SaveFinalState	off
SaveCompleteFinalSimState	off
SaveFormat	Dataset
SaveOutput	on
SaveState	off

SignalLogging	on
DSMLogging	on
InspectSignalLogs	off
VisualizeSimOutput	on
StreamToWorkspace	off
StreamVariableName	streamout
SaveTime	on
ReturnWorkspaceOutputs	off
StateSaveName	xout
TimeSaveName	tout
OutputSaveName	yout
SignalLoggingName	logsout
DSMLoggingName	dsmout
OutputOption	RefineOutputTimes
OutputTimes	[]
ReturnWorkspaceOutputsName	out
Refine	1
LoggingToFile	off
LoggingFileName	out.mat
LoggingIntervals	[-inf, inf]

Table 6.4. Planar_Controller Configuration Set.Components [25](3)

Property	Value
Name	Optimization
Description	
Components	
BlockReduction	on
BooleanDataType	on
ConditionallyExecuteInputs	on
DefaultParameterBehavior	Tunable
InlineParams	off
UseDivisionForNetSlopeComputation	off
UseFloatMulNetSlope	off
DefaultUnderspecifiedDataType	double
UseSpecifiedMinMax	off
InlineInvariantSignals	off
OptimizeBlockIOStorage	on
BufferReuse	on
GlobalBufferReuse	on

GlobalVariableUsage	None
StrengthReduction	off
AdvancedOptControl	
EnforceIntegerDowncast	on
ExpressionFolding	on
BooleansAsBitfields	off
BitfieldContainerType	uint_T
EnableMemcpy	on
MemcpyThreshold	64
PassReuseOutputArgsAs	Structure reference
PassReuseOutputArgsThreshold	12
FoldNonRolledExpr	on
LocalBlockOutputs	on
RollThreshold	5
StateBitsets	off
DataBitsets	off
ActiveStateOutputEnumStorageType	Native Integer
UseTempVars	off
ZeroExternalMemoryAtStartup	on
ZeroInternalMemoryAtStartup	on
InitFltsAndDblsToZero	off
NoFixptDivByZeroProtection	off
EfficientFloat2IntCast	off
EfficientMapNaN2IntZero	on
OptimizeModelRefInitCode	off
LifeSpan	inf
EvaledLifeSpan	Inf
MaxStackSize	Inherit from target
BufferReusableBoundary	on
SimCompilerOptimization	off
AccelVerboseBuild	off

Table 6.5. Planar_Controller Configuration Set.Components [25](4)

Property	Value
Name	Diagnostics
Description	
Components	
RTPrefix	error
ConsistencyChecking	none

ArrayBoundsChecking	none
SignalInfNanChecking	none
SignalRangeChecking	none
ReadBeforeWriteMsg	UseLocalSettings
WriteAfterWriteMsg	UseLocalSettings
WriteAfterReadMsg	UseLocalSettings
AlgebraicLoopMsg	warning
ArtificialAlgebraicLoopMsg	warning
SaveWithDisabledLinksMsg	warning
SaveWithParameterizedLinksMsg	warning
CheckSSInitialOutputMsg	on
UnderspecifiedInitializationDetection	Simplified
MergeDetectMultiDrivingBlocksExec	error
CheckExecutionContextRuntimeOutputMsg	off
SignalResolutionControl	UseLocalSettings
BlockPriorityViolationMsg	warning
MinStepSizeMsg	warning
TimeAdjustmentMsg	none
MaxConsecutiveZCsMsg	error
MaskedZcDiagnostic	warning
IgnoredZcDiagnostic	warning
SolverPrmCheckMsg	none
InheritedTsInSrcMsg	warning
MultiTaskDSMMsg	error
MultiTaskCondExecSysMsg	error
MultiTaskRateTransMsg	error
SingleTaskRateTransMsg	none
TasksWithSamePriorityMsg	warning
SigSpecEnsureSampleTimeMsg	warning
CheckMatrixSingularityMsg	none
IntegerOverflowMsg	warning
Int32ToFloatConvMsg	warning
ParameterDowncastMsg	error
ParameterOverflowMsg	error
ParameterUnderflowMsg	none
ParameterPrecisionLossMsg	warning
ParameterTunabilityLossMsg	warning
FixptConstUnderflowMsg	none
FixptConstOverflowMsg	none

FixptConstPrecisionLossMsg	none
UnderSpecifiedDataTypeMsg	none
UnnecessaryDatatypeConvMsg	none
VectorMatrixConversionMsg	none
InvalidFcnCallConnMsg	error
FcnCallInpInsideContextMsg	EnableAllAsError
SignalLabelMismatchMsg	none
UnconnectedInputMsg	warning
UnconnectedOutputMsg	warning
UnconnectedLineMsg	warning
SFcnCompatibilityMsg	none
FrameProcessingCompatibilityMsg	error
UniqueDataStoreMsg	none
BusObjectLabelMismatch	warning
RootOutportRequireBusObject	warning
AssertControl	UseLocalSettings
Echo	
EnableOverflowDetection	off
AllowSymbolicDim	on
ModelReferenceIOMsg	none
ModelReferenceVersionMismatchMessage	none
ModelReferenceIOMismatchMessage	none
ModelReferenceCSMismatchMessage	none
ModelReferenceSimTargetVerbose	off
UnknownTsInhSupMsg	warning
ModelReferenceDataLoggingMessage	warning
ModelReferenceSymbolNameMessage	warning
ModelReferenceExtraNoncontSigs	error
StateNameClashWarn	none
SimStateInterfaceChecksumMismatchMsg	warning
SimStateOlderReleaseMsg	error
InitInArrayFormatMsg	warning
StrictBusMsg	ErrorLevel1
BusNameAdapt	WarnAndRepair
NonBusSignalsTreatedAsBus	none
SFUnusedDataAndEventsDiag	warning
SFUnexpectedBacktrackingDiag	warning
SFInvalidInputDataAccessInChartInitDiag	warning
SFNoUnconditionalDefaultTransitionDiag	warning

SFTransitionOutsideNaturalParentDiag	warning
SFUnconditionalTransitionShadowingDiag	warning
SFUndirectedBroadcastEventsDiag	warning
SFTransitionActionBeforeConditionDiag	warning
SFOutputUsedAsStateInMooreChartDiag	error
IntegerSaturationMsg	warning
AllowedUnitSystems	all
UnitsInconsistencyMsg	warning
AllowAutomaticUnitConversions	on

Table 6.6. Planar_Controller Configuration Set.Components [25](5)

Property	Value
Name	Hardware Implementation
Description	
Components	
ProdBitPerChar	8
ProdBitPerShort	16
ProdBitPerInt	32
ProdBitPerLong	32
ProdBitPerLongLong	64
ProdBitPerFloat	32
ProdBitPerDouble	64
ProdBitPerPointer	64
ProdLargestAtomicInteger	Char
ProdLargestAtomicFloat	Float
ProdIntDivRoundTo	Zero
ProdEndianess	LittleEndian
ProdWordSize	64
ProdShiftRightIntArith	on
ProdLongLongMode	off
ProdHWDeviceType	Intel->x86-64 (Windows64)
TargetBitPerChar	8
TargetBitPerShort	16
TargetBitPerInt	32
TargetBitPerLong	32
TargetBitPerLongLong	64
TargetBitPerFloat	32
TargetBitPerDouble	64
TargetBitPerPointer	32

TargetLargestAtomicInteger	Char
TargetLargestAtomicFloat	None
TargetShiftRightIntArith	on
TargetLongLongMode	off
TargetIntDivRoundTo	Undefined
TargetEndianess	Unspecified
TargetWordSize	32
TargetTypeEmulationWarnSuppressLevel	0
TargetPreprocMaxBitsSint	32
TargetPreprocMaxBitsUint	32
TargetHWDeviceType	Specified
TargetUnknown	off
ProdEqTarget	on
UseEmbeddedCoderFeatures	on
UseSimulinkCoderFeatures	on

Table 6.7. Planar_Controller Configuration Set.Components [25](6)

Property	Value
Name	Model Referencing
Description	
Components	
UpdateModelReferenceTargets	IfOutOfDateOrStructuralChange
SkipRefExpFcnMdlSchedulingOrderCheck	off
EnableRefExpFcnMdlSchedulingChecks	on
CheckModelReferenceTargetMessage	error
EnableParallelModelReferenceBuilds	off
ParallelModelReferenceErrorOnInvalidPool	on
ParallelModelReferenceMATLABWorkerInit	None
ModelReferenceNumInstancesAllowed	Multi
PropagateVarSize	Infer from blocks in model
ModelDependencies	
ModelReferencePassRootInputsByReference	on
ModelReferenceMinAlgLoopOccurrences	off
PropagateSignalLabelsOutOfModel	on
SupportModelReferenceSimTargetCustomCode	off

Table 6.8. Planar_Controller Configuration Set.Components [25](7)

Property	Value
Name	Simulation Target

Description	
Components	
SimCustomSourceCode	
SimCustomHeaderCode	
SimCustomInitializer	
SimCustomTerminator	
SimReservedNameArray	
SimUserSources	
SimUserIncludeDirs	
SimUserLibraries	
SimUserDefines	
SFSimEnableDebug	off
SFSimOverflowDetection	on
SFSimEcho	on
SimBlas	on
SimCtrlC	on
SimExtrinsic	on
SimIntegrity	on
SimUseLocalCustomCode	off
SimParseCustomCode	on
SimBuildMode	sf_incremental_build
SimDataInitializer	
SimGenImportedTypeDefs	off

Table 6.9. Planar_Controller Configuration Set.Components [25](8)

Property	Value
Name	Code Generation
SystemTargetFile	grt.tlc
HardwareBoard	None
TLCOptions	
CodeGenDirectory	
GenCodeOnly	off
MakeCommand	make_rtw
GenerateMakefile	on
PackageGeneratedCodeAndArtifacts	off
PackageName	
TemplateMakefile	grt_default_tmf
PostCodeGenCommand	
Description	

GenerateReport	off
SaveLog	off
RTWVerbose	on
RetainRTWFile	off
ProfileTLC	off
TLCDebug	off
TLCCoverage	off
TLCAssert	off
ProcessScriptMode	Default
ConfigurationMode	Optimized
ProcessScript	
ConfigurationScript	
ConfigAtBuild	off
RTWUseLocalCustomCode	off
RTWUseSimCustomCode	off
CustomSourceCode	
CustomHeaderCode	
CustomInclude	
CustomSource	
CustomLibrary	
CustomDefine	
CustomLAPACKCallback	
CustomInitializer	
CustomTerminator	
Toolchain	Automatically locate an installed toolchain
BuildConfiguration	Faster Builds
CustomToolchainOptions	
IncludeHyperlinkInReport	off
LaunchReport	off
PortableWordSizes	off
GenerateErtSFunction	off
CreateSILPILBlock	None
CodeExecutionProfiling	off
CodeExecutionProfileVariable	executionProfile
CodeProfilingSaveOptions	SummaryOnly
CodeProfilingInstrumentation	off
SILDebugging	off
TargetLang	C
IncludeERTFirstTime	off

GenerateTraceInfo	off
GenerateTraceReport	off
GenerateTraceReportSl	off
GenerateTraceReportSf	off
GenerateTraceReportEml	off
GenerateCodeInfo	off
GenerateWebview	off
GenerateCodeMetricsReport	off
GenerateCodeReplacementReport	off
RTWCompilerOptimization	off
ObjectivePriorities	
RTWCustomCompilerOptimizations	
CheckMdlBeforeBuild	Off
CustomRebuildMode	OnUpdate
DataInitializer	
Components	[Planar_Controller Configuration Set.Components(8).Components(1) [36], Planar_Controller Configuration Set.Components(8).Components(2)- [37]]

Table 6.10. Planar_Controller Configuration Set.Components [25](9)

Property	Value
Description	Simulink Coverage Configuration Component
Components	
Name	Simulink Coverage
RecordCoverage	off
CovPath	/
CovSaveName	covdata
CovCompData	
CovMetricSettings	dw
CovFilter	
CovHTMLOptions	
CovNameIncrementing	off
CovHtmlReporting	on
CovForceBlockReductionOff	on
CovEnableCumulative	on
CovSaveCumulativeToWorkspaceVar	on
CovSaveSingleToWorkspaceVar	on
CovCumulativeVarName	covCumulativeData
CovCumulativeReport	off

CovSaveOutputData	on
CovOutputDir	slcov_output/$ModelName$
CovDataFileName	$ModelName$_cvdata
CovReportOnPause	on
CovModelRefEnable	off
CovModelRefExcluded	
CovExternalEMLEnable	off
CovSFcnEnable	on
CovBoundaryAbsTol	1.0000e-05
CovBoundaryRelTol	0.0100
CovUseTimeInterval	off
CovStartTime	0
CovStopTime	0

Table 6.11. Planar_Controller Configuration Set.Components [25](10)

Property	Value
Description	HDL Coder custom configuration component
Components	
Name	HDL Coder

Table 6.12. Planar_Controller Configuration Set.Components(8).Components [35](1)

Property	Value
Name	Code Appearance
Description	
Components	
ForceParamTrailComments	off
GenerateComments	on
CommentStyle	Auto
IgnoreCustomStorageClasses	on
IgnoreTestpoints	off
IncHierarchyInIds	off
MaxIdLength	31
PreserveName	off
PreserveNameWithParent	off
ShowEliminatedStatement	off
OperatorAnnotations	off
IncAutoGenComments	off
SimulinkDataObjDesc	off

SFDataObjDesc	off
MATLABFcnDesc	off
IncDataTypeInIds	off
PrefixModelToSubsysFcnNames	on
MangleLength	1
CustomSymbolStr	RN$M
CustomSymbolStrGlobalVar	RN$M
CustomSymbolStrType	NR$M_T
CustomSymbolStrField	NM
CustomSymbolStrFcn	RNMF
CustomSymbolStrFcnArg	rt$I$N$M
CustomSymbolStrBlkIO	rtb_NM
CustomSymbolStrTmpVar	NM
CustomSymbolStrMacro	RN$M
CustomSymbolStrUtil	NC
CustomCommentsFcn	
DefineNamingRule	None
DefineNamingFcn	
ParamNamingRule	None
ParamNamingFcn	
SignalNamingRule	None
SignalNamingFcn	
InsertBlockDesc	off
InsertPolySpaceComments	off
SimulinkBlockComments	on
MATLABSourceComments	off
EnableCustomComments	off
InternalIdentifier	Shortened
InlinedPrmAccess	Literals
ReqsInCode	off
UseSimReservedNames	off
ReservedNameArray	

Table 6.13. Planar_Controller Configuration Set.Components(8).Components [35](2)

Property	Value
Name	Target
Description	
Components	

IsERTTarget	off
TargetFcnLib	ansi_tfl_table_tmw.mat
TargetLibSuffix	
TargetPreCompLibLocation	
GenFloatMathFcnCalls	NOT IN USE
TargetLangStandard	C89/C90 (ANSI)
TargetFunctionLibrary	NOT IN USE
CodeReplacementLibrary	None
UtilityFuncGeneration	Auto
ERTMultiwordTypeDef	System defined
ERTMultiwordLength	256
MultiwordLength	2048
GenerateFullHeader	on
InferredTypesCompatibility	off
GenerateSampleERTMain	off
GenerateTestInterfaces	off
ModelReferenceCompliant	on
ParMdlRefBuildCompliant	on
CompOptLevelCompliant	on
ConcurrentExecutionCompliant	on
IncludeMdlTerminateFcn	on
CombineOutputUpdateFcns	on
CombineSignalStateStructs	off
SuppressErrorStatus	off
ERTFirstTimeCompliant	off
IncludeFileDelimiter	Auto
ERTCustomFileBanners	off
SupportAbsoluteTime	on
LogVarNameModifier	rt_
MatFileLogging	on
MultiInstanceERTCode	off
CodeInterfacePackaging	Nonreusable function
SupportNonFinite	on
SupportComplex	on
PurelyIntegerCode	off
SupportContinuousTime	on
SupportNonInlinedSFcns	on
SupportVariableSizeSignals	off
ParenthesesLevel	Nominal

CastingMode	Nominal
GenerateClassInterface	off
ModelStepFunctionPrototypeControlCompliant	off
CPPClassGenCompliant	on
GRTInterface	off
GenerateAllocFcn	off
UseToolchainInfoCompliant	on
GenerateSharedConstants	on
ExtMode	off
ExtModeStaticAlloc	off
ExtModeTesting	off
ExtModeStaticAllocSize	1000000
ExtModeTransport	0
ExtModeMexFile	ext_comm
ExtModeMexArgs	
ExtModeIntrfLevel	Level1
RTWCAPISignals	off
RTWCAPIParams	off
RTWCAPIStates	off
RTWCAPIRootIO	off
GenerateASAP2	off
MultiInstanceErrorCode	Error

Chapter 7. Glossary

Atomic Subsystem.   A subsystem treated as a unit by an implementation of the design documented in this report. The implementation computes the outputs of all the blocks in the atomic subsystem before computing the next block in the parent system's block execution order (sorted list).

Block Diagram.   A Simulink block diagram represents a set of simultaneous equations that relate a system or subsystem's inputs to its outputs as a function of time. Each block in the diagram represents an equation of the form $y = f(t, x, u)$ where t is the current time, u is a block input, y is a block output, and x is a system state (see the Simulink documentation for information on the functions represented by the various types of blocks that make up the diagram). Lines connecting the blocks represent dependencies among the blocks, i.e., inputs whose current values are the outputs of other blocks. An implementation of a design described in this document computes a root or atomic system's outputs at each time step by computing the outputs of the blocks in an order determined by block input/output dependencies.

Block Parameter.   A variable that determines the output of a block along with its inputs, for example, the gain parameter of a Gain block.

Block Execution Order.   The order in which Simulink evaluates blocks during simulation of a model. The block execution order determined by Simulink ensures that a block executes only after all blocks on whose outputs it depends are executed.

Checksum.   A number that indicates whether different versions of a model or atomic subsystem differ functionally or only cosmetically. Different checksums for different versions of the same model or subsystem indicate that the versions differ functionally.

Design Variable.   A symbolic (MATLAB) variable or expression used as the value of a block parameter. Design variables allow the behavior of the model to be altered by altering the value of the design variable.

Signal.   A block output, so-called because block outputs typically vary with time.

Virtual Subsystem.   A subsystem that is purely graphical, i.e., is intended to reduce the visual complexity of the block diagram of which it is a subsystem. An implementation of the design treats the blocks in the subsystem as part of the first nonvirtual ancestor of the virtual subsystem (see Atomic Subsystem).

Chapter 8. About this Report

Table of Contents

8.1. Report Overview

This report describes the design of the Planar_Controller system. The report was generated automatically from a Simulink model used to validate the design. It contains the following sections:

Model Version. Specifies information about the version of the model from which this design description was generated. Includes the model checksum, a number that indicates whether different versions of the model differ functionally or only cosmetically. Different checksums for different versions indicate that the versions differ functionally.

Root System. Describes the design's root system.

Subsystems. Describes each of the design's subsystems.

Design Variables. Describes system design variables, i.e., MATLAB variables and expressions used as block parameter values.

System Model Configuration. Lists the configuration parameters, e.g., start and stop time, of the model used to simulate the system described by this report.

Requirements Traceability. Shows design requirements associated with elements of the design model. This section appears only if the design model contains requirements links.

Glossary. Defines Simulink terms used in this report.

8.2. Root System Description

This section describes a design's root system. It contains the following sections:

Diagram. Simulink block diagram that represents the algorithm used to compute the root system's outputs.

Description. Description of the root system. This section appears only if the model's root system has a Documentation property or a Doc block.

Interface. Name, data type, width, and other properties of the root system's input and output signals. The number of the block port that outputs the signal appears in angle brackets appended to the signal name. This section appears only if the root system has input or output ports.

Blocks. This section has two subsections:

- **Parameters.** Describes key parameters of blocks in the root system. This section also includes graphical and/or tabular representations of lookup table data used by lookup table blocks, i.e., blocks that use lookup tables to compute their outputs.

- **Block Execution Order.** Order in which blocks must be executed at each time step in order to ensure that each block's inputs are available when it executes.

State Charts. Describes state charts used in the root system. This section appears only if the root system contains Stateflow blocks.

8.3. Subsystem Descriptions

This section describes a design's subsystems. Each subsystem description contains the following sections:

Checksum. This section appears only if the subsystem is an atomic subsystem. The checksum indicates whether the version of the model subsystem used to generate this report differs functionally from other versions of the model subsystem. If two model checksums differ, the corresponding versions of the model differ functionally.

Diagram. Simulink block diagram that graphically represents the algorithm used to compute the subsystem's outputs.

Description. Description of the subsystem. This section appears only if the subsystem has a Documentation property or contains a Doc block.

Interface. Name, data type, width, and other properties of the subsystem's input and output signals. The number of the block port that outputs the signal appears in angle brackets appended to the signal name. This section appears only if the subsystem is atomic and has input or output ports.

Blocks. Blocks that this subsystem contains. This section has two subsections:

- **Parameters.** Key parameters of blocks in the subsystem. This section also includes graphical and/or tabular representations of lookup table data used by lookup table blocks, blocks that use lookup tables to compute their outputs.
- **Block Execution Order.** Order in which the subsystem's blocks must be executed at each time step in order to ensure that each block's inputs are available when the block executes .This section appears only if the subsystem is atomic. Note: in Acrobat(PDF) reports, the number in square brackets next to the block name is a hyperlink to the block parameter table. The number has no model significance.

State Charts. Describes state charts used in the subsystem. This section appears only if the root system contains Stateflow blocks.

8.4. State Chart Descriptions

This section describes the state machines used by Stateflow blocks to compute their outputs, i.e., Stateflow blocks. Each state machine description contains the following sections:

Chart. Diagram representing the state machine.

States. Describes the state machine's states. Each state description includes the state's diagram and diagrams and/or descriptions of graphical functions, Simulink functions, truth tables, and MATLAB functions parented by the state.

Transitions. Transitions between the state machine's states. Each transition description specifies the values of key transition properties. Appears only if a transition has properties that do not appear on the chart.

Junctions. Transition junctions. Each junction description specifies the values of key junction properties. Appears only if a junction has properties that do not appear on the chart.

Events.   Events that trigger state transitions. Each event description specifies the values of key event properties.

Data.   Data types and other properties of the Stateflow block's inputs, outputs, and other state machine data.

Targets.   Executable implementations of the state machine used to compute the outputs of the corresponding Stateflow block.

MATLAB Supporting Functions.   List of functions invoked by MATLAB functions defined in the chart.