

# Logic: Representation

---

Virendra Singh

Professor

Computer Architecture and Dependable Systems Lab

Department of Computer Science & Engineering, and

Department of Electrical Engineering

Indian Institute of Technology Bombay

<http://www.cse.iitb.ac.in/~viren/>

E-mail: viren@{cse, ee}.iitb.ac.in

*CS-230: Digital Logic Design & Computer Architecture*

---



Lecture 7 (18 January 2022)

**CADSL**

Components

$$X \leftarrow \text{NOT } Y$$

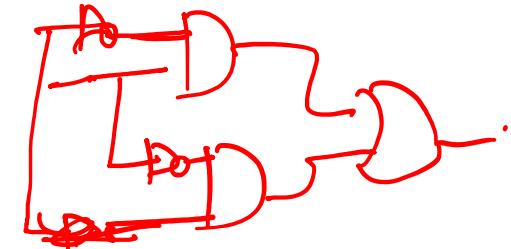
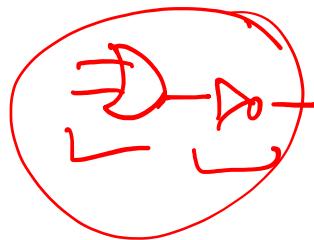
$$X \leftarrow X \text{ AND } Z$$

$$X \leftarrow Y \text{ OR } Z$$

NOT, OR, AND

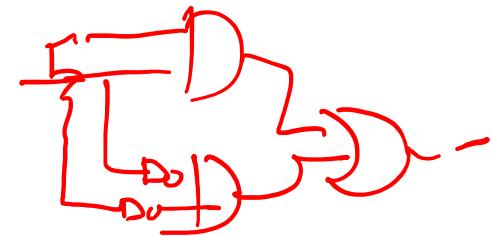
NAND, NOR, XOR, XNOR

Components



(VHDL file)

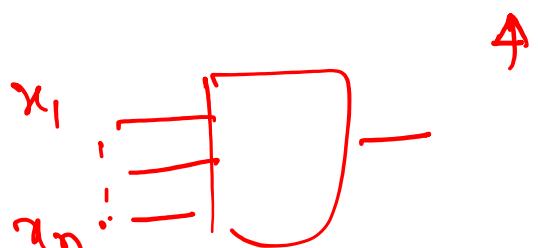
~~Project~~



# Truth Table/ Min Term/Max Term

# Truth Table

	X	Y	Z	F
0 →	0	0	0	0 ✓
1	0	0	1	1
2	0	1	0	0
3	0	1	1	0
4	1	0	0	1
5	1	0	1	1
6	1	1	0	1
7	1	1	1	1



$$\left\{ \begin{array}{c} \text{SOP} \\ x \cdot y + y \cdot z \\ \hline \text{POS} \\ (x+y) \cdot (y+z) \end{array} \right\} \text{Not Canonical.}$$

← Canonical

$$x \cdot y + 3 \cdot (x+y)$$

5      1, 4, 5, 6, 7, { true }.

3

(6, 2, 3} 2



# Truth Table/ Min Term/Max Term

Truth Table

X Y Z	F
0 0 0	0
0 0 1	1
0 1 0	0
0 1 1	0
1 0 0	1
1 0 1	1
1 1 0	1
1 1 1	1

Canonical.

$$\bar{x} \cdot \bar{y} \cdot z$$

$$F(x, y, z)$$

$$\bar{x} \cdot \bar{y} \cdot z \rightarrow F.$$

Implicant

$$F = m_1 + m_4 + m_5 + m_6 + m_7$$

$$x \bar{y} \bar{z}$$



# Truth Table/ Min Term/Max Term

Truth Table

X	Y	Z	F
0	0	0	0 ✓
0	0	1	1
0	1	0	0 ✓
0	1	1	0 ✓
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	1

$$\bar{F} = (\bar{x} \cdot \bar{y} \cdot \bar{z}) + (\bar{x} \cdot y \cdot \bar{z}) + (\bar{x} \cdot y \cdot z)$$

$$F = \bar{F} = ( )$$

Clause. ↴

$$(x+y+z) \cdot (x+\bar{y}+z) \cdot (x+y+\bar{z})$$

↑

M<sub>0</sub>

↑

M<sub>2</sub>

↑

M<sub>3</sub>

$$F = M_0 \cdot M_2 \cdot M_3$$

(POS)



# Logic Expression

# Truth Table

X Y Z	F
0 0 0	0
0 0 1	1
0 1 0	0
0 1 1	0
1 0 0	1
1 0 1	1
1 1 0	1
1 1 1	1

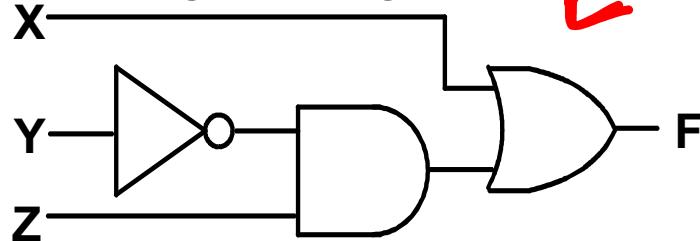
# Logic Expression

$$F = \overline{X}.\overline{Y}.Z + X.\overline{Y}.\overline{Z} + X.\overline{Y}.Z$$

$\overline{X}.\overline{Y}.\overline{Z}$  +  $X.\overline{Y}.Z$

Logic Expression Book on  
Algebra }  
 $F = X + \bar{Y} Z$

# Logic Diagram



6 SOP  
AND - OR  
NAND - NAND POS OR-And. NOR-nor. 2N

# What to Minimize?

## Logic Expression

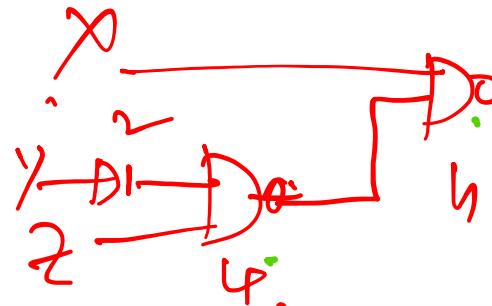
$$F = \overline{\underline{\overline{X}} \cdot \overline{Y} \cdot Z} + \overline{X} \cdot \overline{\underline{Y}} \cdot \overline{Z} + \overline{X} \cdot \overline{Y} \cdot \overline{Z}$$

$$+ \overline{X} \cdot Y \cdot \overline{Z} + X \cdot \overline{Y} \cdot Z$$

$\Downarrow$  Boolean Algebra

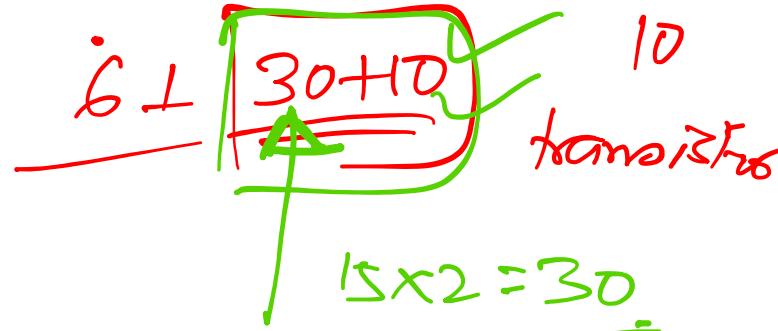
## Logic Expression

$$F = \overline{X} + \overline{Y} \cdot Z$$



AND - OR .

NAND - NAND



Min { # literals +  
#.prod. terms }

10

8



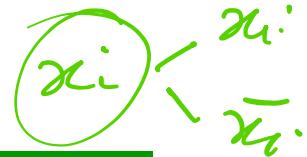
# Graphical Method: Binary Decision Diagram



$$y = f(x_1, x_2, \dots, x_n)$$



# Binary Decision Diagram

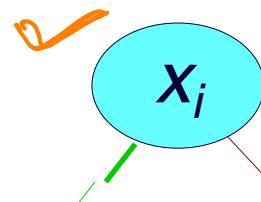


- ❖ BDD is canonical form of representation

- ❖ Shanon's expansion theorem

$$f(x_1, x_2, \dots, x_i, \dots, x_n) = x_i \cdot f(x_1, x_2, \dots, x_i=1, \dots, x_n) + x_i' \cdot f(x_1, x_2, \dots, x_i=0, \dots, x_n)$$

$f_{xi} \uparrow$  Cofactor



✓  $f(x_1, x_2, \dots, x_i=1, \dots, x_n)$

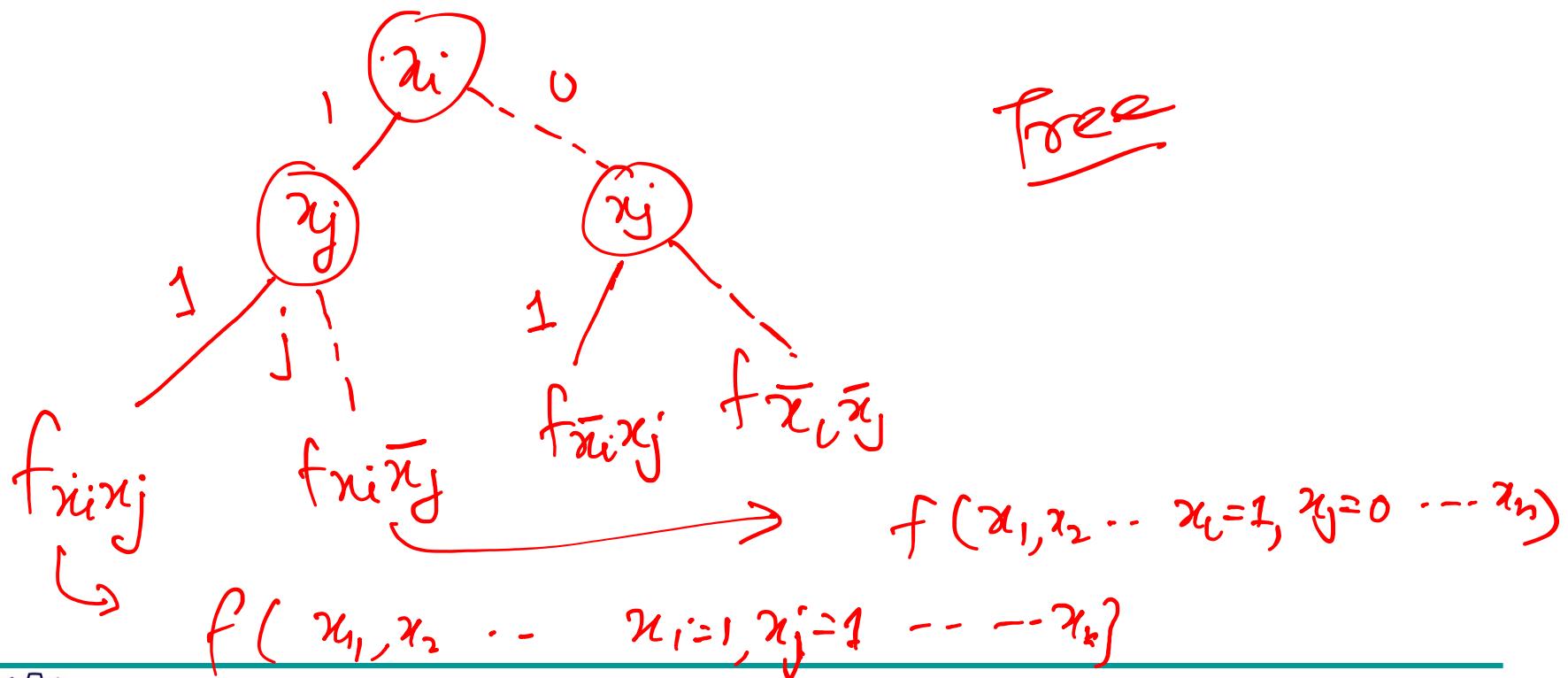
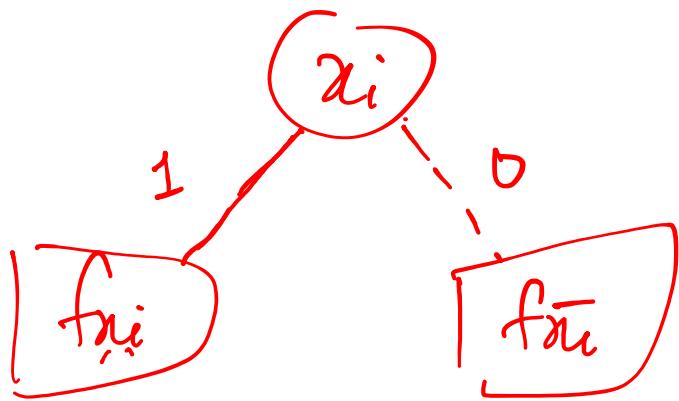
✓  $f(x_1, x_2, \dots, x_i=1, \dots, x_n)$

$f_{x̄_i}$

$$\begin{aligned} & f(x_1, x_2, \dots, x_n) = x_1 \cdot f_{x_1} + x_2 \cdot f_{x_2} + \dots + x_n \cdot f_{x_n} \\ & \text{if } x_1 = 1, f_{x_1} = x_2 + x_3 \\ & \text{if } x_1 = 0, f_{x_1} = 0 \end{aligned}$$

$$x_1 \cdot (x_2 + x_3) + \bar{x}_1 \cdot 0 = x_1 x_2 + x_1 x_3$$





# Decision Structures

$x_1, x_2, x_3$

ordered,  
 $x_1 < x_2 < x_3$ .

Truth Table

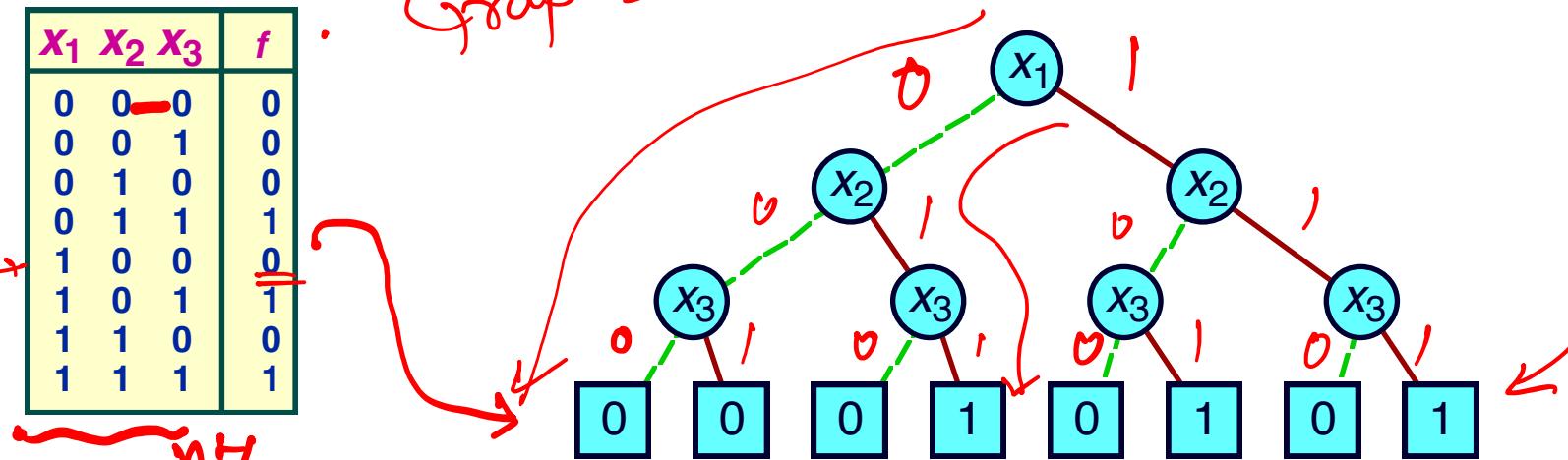
$x_1$	$x_2$	$x_3$	$f$
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1

$2^n$

$n=3$

Graphical

Decision Tree



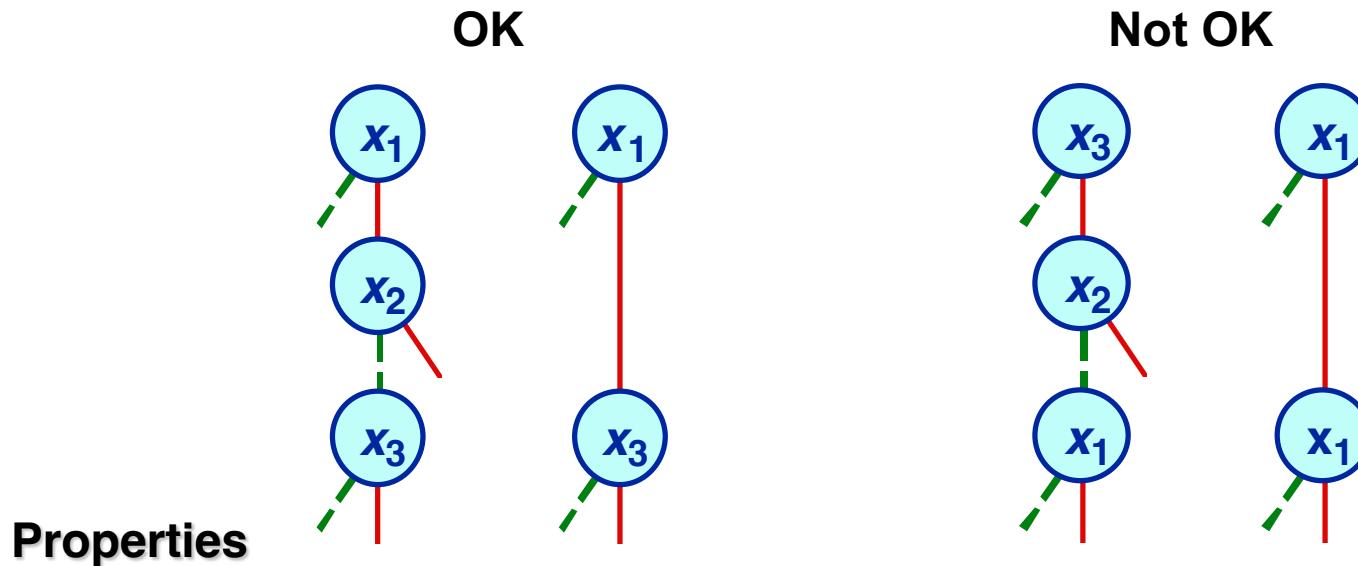
- Vertex represents decision
- Follow green (dashed) line for value 0
- Follow red (solid) line for value 1
- Function value determined by leaf value.

$2^n$   
leaf  
evaluat.



# Variable Ordering

- ❖ Assign arbitrary total ordering to variables Go
- e.g.,  $x_1 < x_2 < x_3$
- ❖ Variables must appear in ascending order along all paths



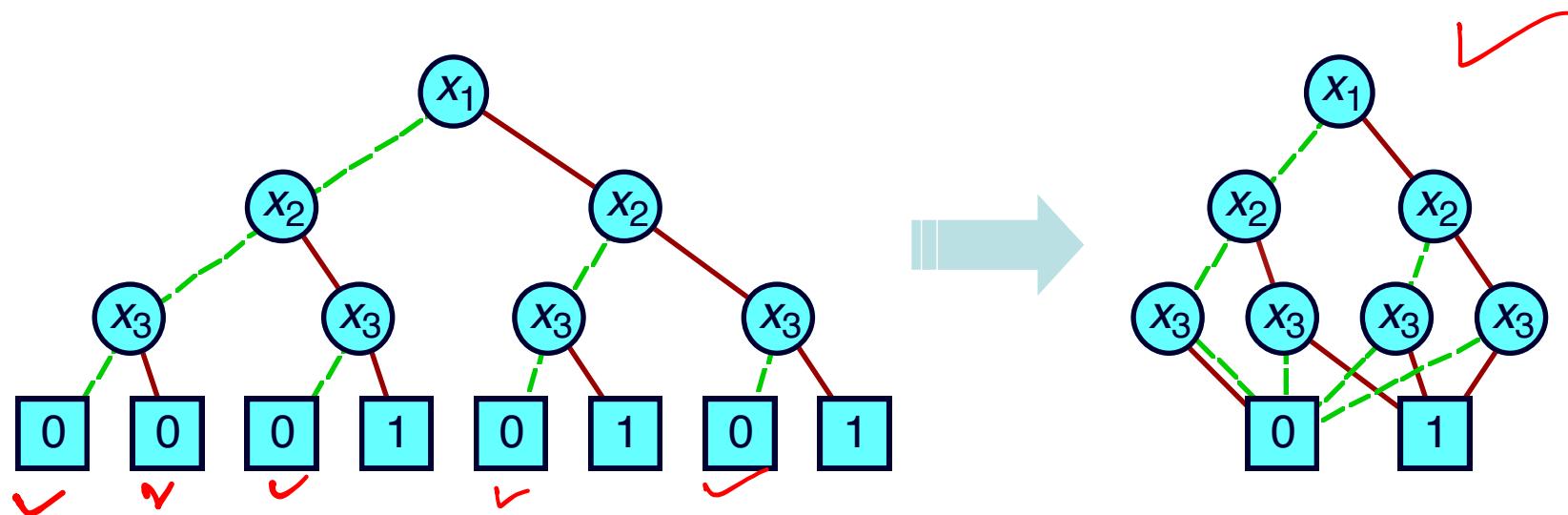
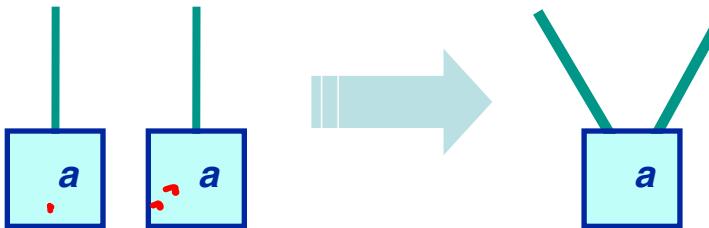
- No conflicting variable assignments along path
- Simplifies manipulation



# Reduction Rule #1

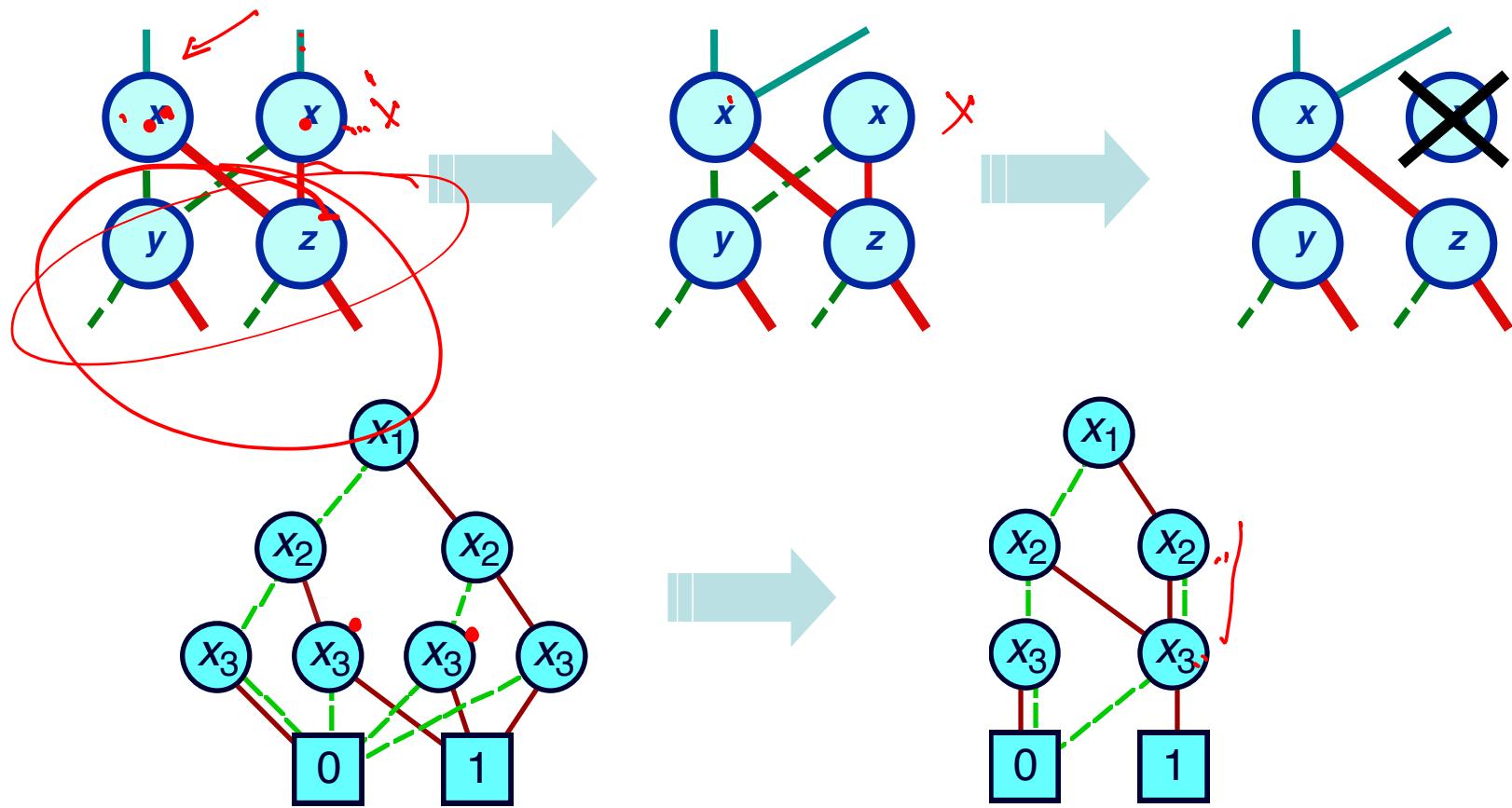
---

Merge equivalent leaves



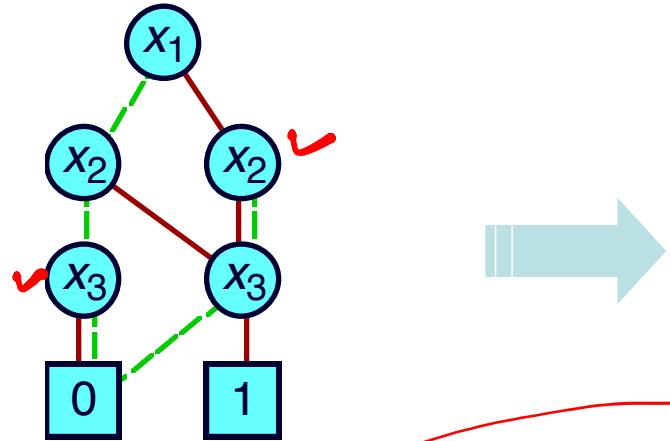
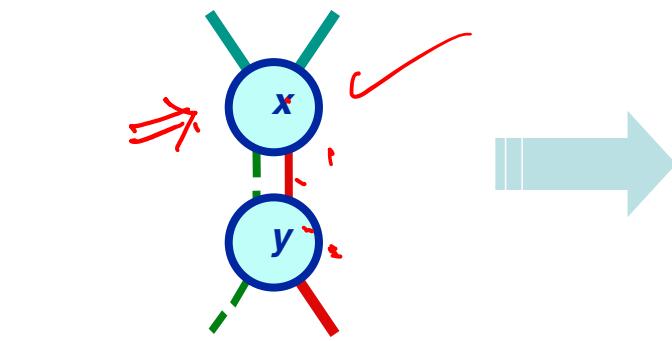
# Reduction Rule #2

Merge isomorphic nodes



# Reduction Rule #3

Eliminate Redundant Tests



Binary  
Ordered Decision Tree Graph.  
Reduced, ordered, binary decision diagram

(ROBDD)  
Compact  
Canonical



$x_1, x_2 + x_2 \cdot x_3$   
18 Jan 2022

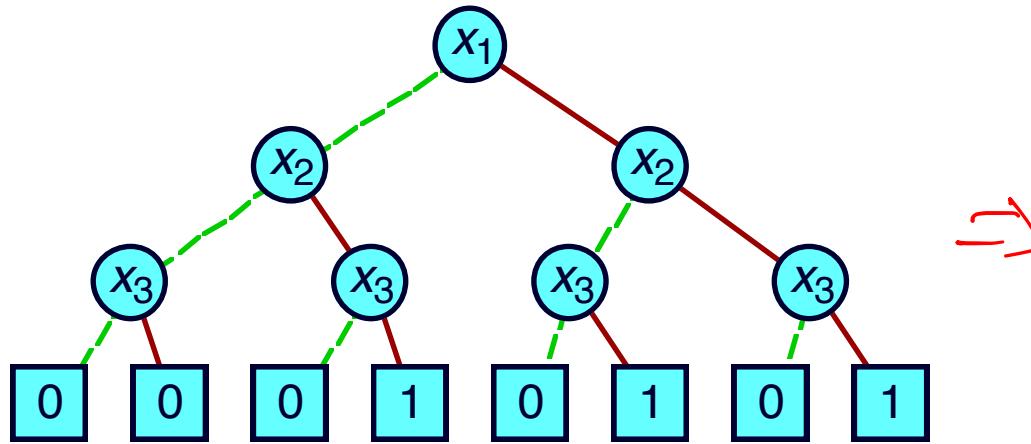
$\bar{x}_1 \cdot \bar{x}_2$   
CS-230@IITB

$x_1, x_3 + \bar{x}_1 \cdot x_2 \cdot x_3$   
15

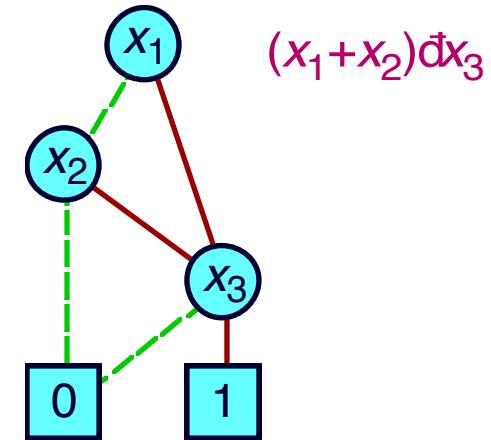
Unique CADSL

# Example OBDD

Initial Graph



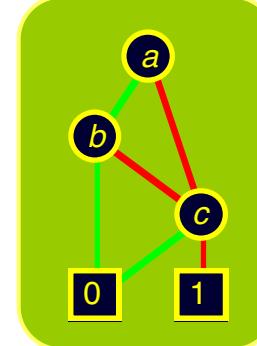
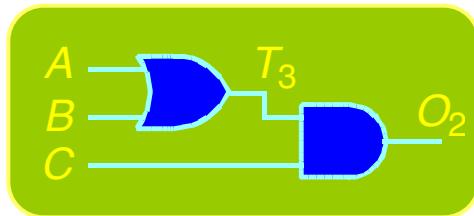
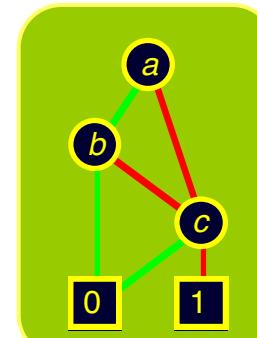
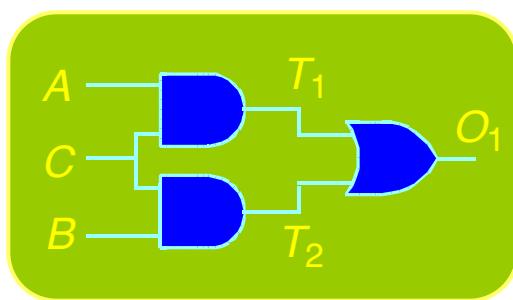
Reduced Graph



- Canonical representation of Boolean function
  - ❖ For given variable ordering
  - Two functions equivalent if and only if graphs isomorphic
    - o Can be tested in linear time
  - Desirable property: *simplest form is canonical.*

# Binary Decision Diagram

- Generate Complete Representation of Circuit Function
  - Compact, canonical form



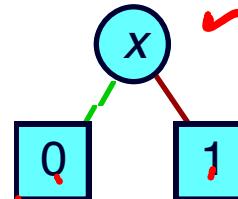
- Functions equal if and only if representations identical
- Never enumerate explicit function values
- Exploit structure & regularity of circuit functions

# Example Functions

## Constants

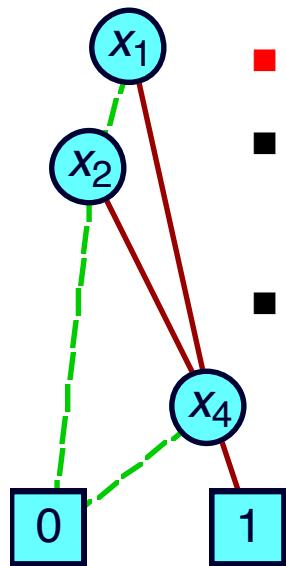
- ✓  Unique unsatisfiable function
- ✓  Unique tautology

## Variable



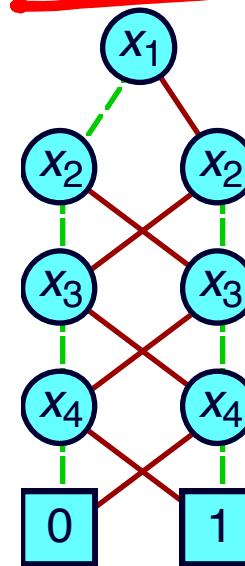
Treat variable  
as function

## Typical Function



- $(x_1 + x_2) \cdot x_4$
- No vertex labeled  $x_3$ 
  - ◆ independent of  $x_3$
- Many subgraphs shared

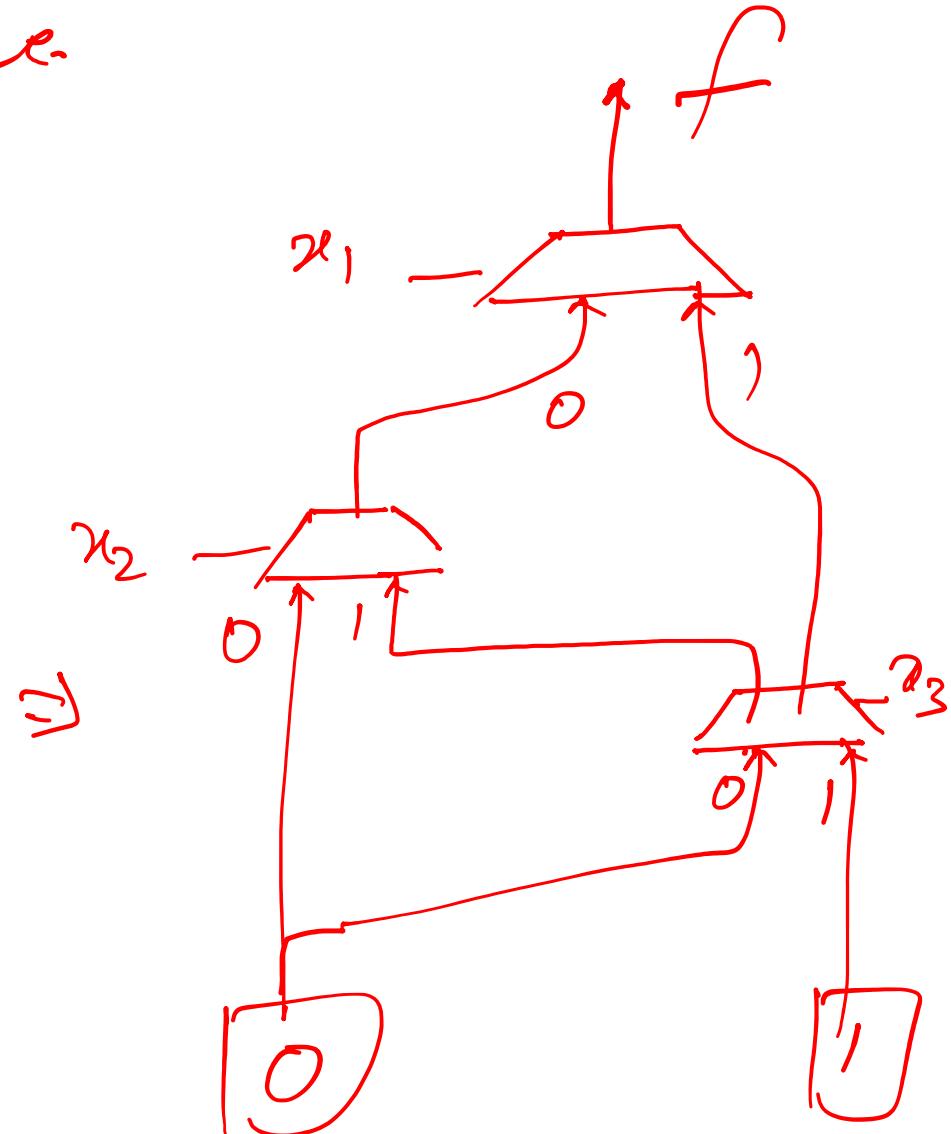
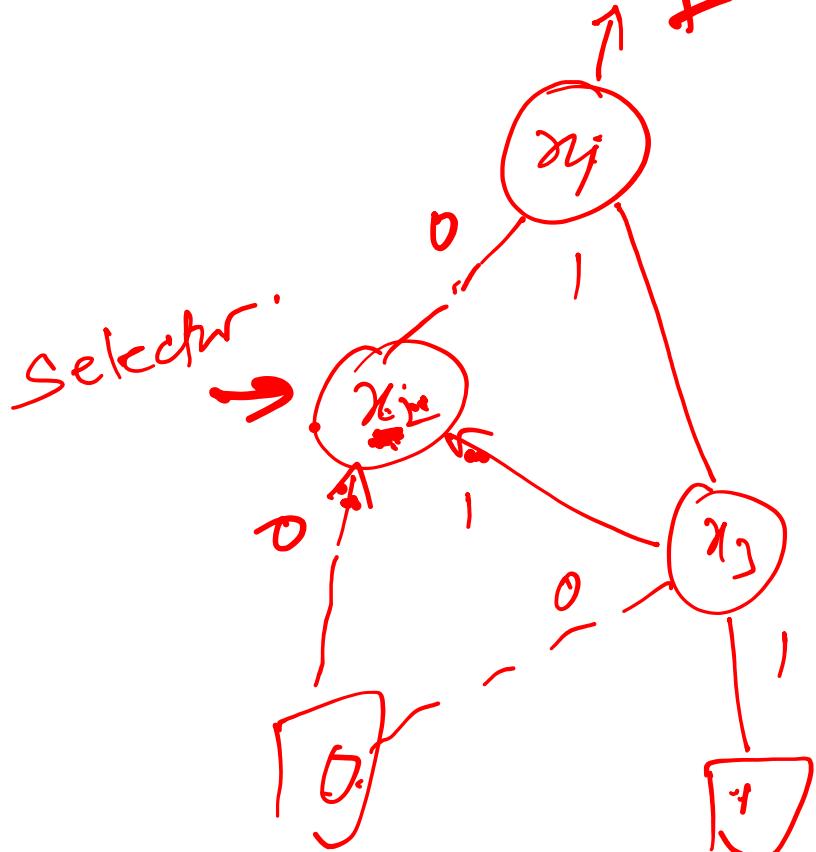
## Odd Parity



$$\overbrace{x_1 \oplus x_2 \oplus x_3 \oplus x_4}^{\text{sym.}}$$

Linear  
representation

Synthesize  
 $f$



# Thank You

