



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, TELANGANA STATE, INDIA.

CONSOLIDATED MEMO OF MARKS/GRADES AND CREDITS



B.Tech. ELECTRONICS & COMMUNICATION ENGINEERING

CMM. No. **C 0977987**

Serial No. **21334031270**

Name : **POLLAPALLI SHRAVIKA REDDY**

Hall Ticket No. : **15RA1A0413** Year of Admission : **2015-2016**



Name of the College : **RA-KPRIT FDI II ARAN**

Month & Year of Final Exam : **May, 2019**

Class Awarded : **FIRST CLASS**

S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
	Maximum Marks in Theory	25	75	100			Maximum Marks in Lab	25	50	75	
I YEAR											
1	ENGLISH	12	32	44	4	2	COMPUTER PROGRAMMING	14	32	46	6
3	ENGINEERING CHEMISTRY	17	26	43	6	4	MATHEMATICAL METHODS	17	30	47	6
5	MATHEMATICS - I	17	49	66	6	6	ENGINEERING PHYSICS	19	26	45	6
7	ENGINEERING DRAWING	19	26	45	6	8	IT WORKSHOP / ENGINEERING WORKSHOP	20	39	59	4
9	ENGINEERING PHYSICS & ENGINEERING CHEMISTRY LA	22	42	64	4	10	COMPUTER PROGRAMMING LAB	23	45	68	4
11	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	23	46	69	4						
I SEMESTER											
II YEAR											
1	SIGNALS AND SYSTEMS	15	26	41*	4	1	ENVIRONMENTAL STUDIES	15	37	52	4
2	PROBABILITY THEORY AND STOCHASTIC PROCESSES	18	36	54	4	2	DIGITAL DESIGN USING VERILOG HDL	16	29	45	4
3	MATHEMATICS - III	18	37	55	4	3	ELECTRONIC CIRCUIT ANALYSIS	18	44	62	4
4	SWITCHING THEORY AND LOGIC DESIGN	19	31	50	4	4	ELECTROMAGNETIC THEORY AND TRANSMISSION LIN	19	26	45	4
5	ELECTRICAL CIRCUITS	20	26	46	4	5	PULSE AND DIGITAL CIRCUITS	20	44	64	4
6	ELECTRONIC DEVICES AND CIRCUITS	20	31	51	4	6	PRINCIPLES OF ELECTRICAL ENGINEERING	21	32	53	4
7	ELECTRONIC DEVICES AND CIRCUITS LAB	20	42	62	2	7	GENDER SENSITIZATION* ^	21	45	66	2
8	BASIC SIMULATION LAB	23	42	65	2	8	ELECTRICAL TECHNOLOGY LAB	23	42	65	2
						9	ELECTRONIC CIRCUITS AND PULSE CIRCUITS LAB	23	44	67	2
I SEMESTER											
III YEAR											
1	ANTENNAS AND WAVE PROPAGATION	16	47	63	4	1	MICROPROCESSORS AND MICROCONTROLLERS	17	26	43	4
2	ANALOG COMMUNICATIONS	19	40	59	4	2	DIGITAL SIGNAL PROCESSING	18	30	48	4
3	LINEAR AND DIGITAL IC APPLICATIONS	19	45	64	4	3	DIGITAL COMMUNICATIONS	19	26	45	4
4	ELECTRONIC MEASUREMENTS AND INSTRUMENTATION	20	26	46	4	4	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	20	26	46	4
5	CONTROL SYSTEMS ENGINEERING	20	32	52	4	5	VLSI DESIGN	20	31	51	4
6	IC APPLICATIONS AND HDL SIMULATION LAB	21	46	67	2	6	DISASTER MANAGEMENT	21	32	53	4
7	COMPUTER ORGANIZATION AND OPERATING SYSTEMS	22	43	65	4	7	MICROPROCESSORS AND MICROCONTROLLERS LAB	21	42	63	2
8	ANALOG COMMUNICATIONS LAB	23	41	64	2	8	DIGITAL SIGNAL PROCESSING LAB	23	45	68	2
I SEMESTER											
IV YEAR											
1	COMPUTER NETWORKS	17	38	55	4	1	INDUSTRY ORIENTED MINI PROJECT	-	42	42	2
2	MICROWAVE ENGINEERING	18	28	46	4	2	COMPREHENSIVE VIVA	-	89	89	2
3	OBJECT ORIENTED PROGRAMMING THROUGH JAVA	20	31	51	4	3	RADAR SYSTEMS	22	22	44*	0
4	CELLULAR AND MOBILE COMMUNICATIONS	20	32	52	4	4	WIRELESS COMMUNICATIONS AND NETWORKS	22	41	63	4
5	EMBEDDED SYSTEMS DESIGN	20	37	57	4	5	SATELLITE COMMUNICATIONS	25	35	60	4
6	ADVANCED COMMUNICATION SKILLS LAB	21	44	65	2	6	SEMINAR	41	-	41	2
7	MANAGEMENT SCIENCE	22	26	48	4	7	MAJOR PROJECT	44	138	182	10
8	MICROWAVE ENGINEERING AND DIGITAL COMMUNICATI	22	47	69	2						

(# Project Internal=50, External=150)

Number of Credits registered for : **226** Aggregate Marks Secured for best: **218**

Aggregate Marks/CGPA Secured :

3249 OUT OF 5250 (61.89%)

Date of Issue :

August 5, 2019

(see overleaf for Rules concerned to award of class)

A indicates 'ABSENT'

(Credits considered for award of degree)

(Courses registered but not counted for calculation of aggregate)



S. Tara Kalyani
CONTROLLER OF EXAMINATIONS