

## F. Y. B. Tech Academic Year 2020-21

1 rimester: 1 S	<b>Subject:</b> Basics of Electrical and Electronics Engineering		
Name		Divis	ion
Roll No		Batch	1
	Experime	nt No:7	
Name of the Experiment: voltage law (KVL)	Verification of Kirc	hhoff's current law (KO	CL) and Kirchhoff's
Performed on:			
Submitted on:			

# Aim: Verification of Kirchhoff's current law (KCL) and Kirchhoff's voltage law (KVL)

## **Objectives**

- To get the idea of analysing the circuits
- To solve the electrical circuit theoretically
- To verify practical and theoretical value

## Components and equipment required

Components	Specification
DC Ammeters	0 – 1 A (3 No.)
DC V-14	` ′
DC Voltmeter	0 - 20 V (1 No.)
Circuit board	Same as shown in Fig.3
DC Power supply	0 <u>-15 V (2 No.)</u>

## Theory

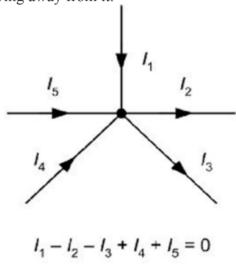
A German physicist Gustav Kirchhoff developed two laws enabling easy analysis of an interconnection of any number of circuit elements. The first law deals with the flow of current and is popularly known as **Kirchhoff's Current Law (KCL)** 



while the second one deals with the voltage drop in a closed network and is known as **Kirchhoff's Voltage Law** (KVL).

#### A. Kirchhoff's current law

It states that, at any moment, the algebraic sum of currents flowing through a point (or junction) in a network is Zero or in any electrical network, the algebraic sum of the currents meeting at a point (or junction) is Zero. This law is also known as Point Law. In other words, the sum of the currents flowing towards a point is equal to the sum of those flowing away from it.



Convention:

Current flowing towards the junction is positive (+)
Current flowing away from the junction is negative (-)

Fig. 1: Kirchoff's current law

In Fig.1, currents  $I_1$ ,  $I_4$  and  $I_5$  are flowing towards the node, are considered as positive. At the same time, currents  $I_2$  and  $I_3$  are leaving the junction, are considered as negative. As per KCL, sum of all the currents is zero. That means sum of currents entering the node and sum of currents leaving the node is equal.



# B. Kirchhoff's voltage law

It states that, in any closed loop (mesh), the algebraic sum of the EMFs applied is equal to the algebraic sum of the voltage drops in the elements. It is also known as Mesh law.

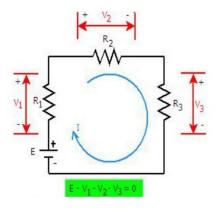
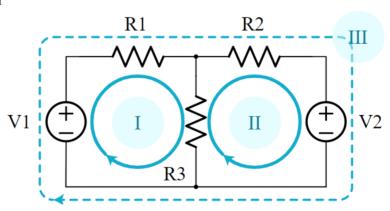


Fig. 2: Kirchoff's voltage law

**Component Values used:** 

**Voltage source E=** 

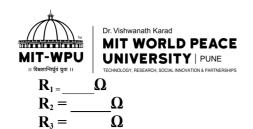
# Circuit diagram



# Components used in the circuit

$$V_1 = \underline{\qquad} V$$

$$V_2 = \underline{\qquad} V$$



#### Procedure:

### A. KCL

- 1) Connect the circuit as shown in the diagram
- 2) Measure currents in every branch and note it in observation table as practical value.
- 3) Calculate theoretical current values using mesh equations

#### B. KVL

- 1) Connect the circuit as shown in the diagram
- 2) Using a voltmeter, measure the source voltage E and the voltage drop across the resistances R<sub>1</sub>, R<sub>2</sub> and R<sub>3</sub>.
- 3) Find the current I = E / R1 + R2 + R3
- 4) Calculate theoretical values as V1=IR1, V2=IR2, V3=IR3

#### **Observation Tables:**

1) Verification of KCL-

Current (A)	Theoretical Value	Practical value

Calculate and compare it with. These two values should be equal.

2) Verification of KVL-

Voltage (V)	Theoretical Value	Practical value

and are the voltage drops across resistances  $R_1 R_2$  and  $R_3$ . Calculate and compare it with E. These two values should be equal.

#### **Conclusion**



**Post-Lab Questions** 

- 1. Theoretically calculate the voltages and currents for each element in the circuit and compare them to the measured values.
- 2. Calculate the percentage error in the two measurements and provide a brief explanation for the error.

Note: Students are instructed to do all necessary calculations and answer the questions on separate sheets and attach them.



