

Medium T.L.

Surge Impedance

Surge Impedance (Z_0) of a line is defined as the square root of Z/Y

$$\text{ie } Z_0 = \sqrt{\frac{Z}{Y}}$$

where $Z = R + jX \rightarrow$ Series impedance

$Y = G + jB \rightarrow$ shunt admittance

For a line having negligible resistance (ie when the conductors are of large c.s.) and having no shunt leakage (ie when value of G will be zero), $Z = \sqrt{L/C}$ which is a pure resistance. It has a value of 400 to 600 Ω for an overhead line & 40 to 60 Ω for an underground cable.

The surge impedance of a line may be measured in terms of Z_{oc} & Z_{sc} where these are impedances measured at the sending end with receiving end open-circuited & short circuited resp.

We know, for a T.L.,

$$V_s = A \cdot V_R + B \cdot I_R$$

$$I_s = C \cdot V_R + D \cdot I_R$$

\therefore when there is o.c. at R.E,

$$I_R = 0$$

$$\therefore V_s = A \cdot V_R$$

$$I_s = C \cdot V_R$$

$$\therefore Z_{oc} = \frac{V_s}{I_s} = \frac{A}{C} \quad \text{--- (1)}$$

When R.E is short circuited,

$$V_R = 0$$

$$\therefore V_s = B \cdot I_R$$

$$I_s = D \cdot I_R$$

$$\therefore Z_{sc} = \frac{V_s}{I_s} = B/D. \quad \text{--- (2)}$$

* Multiply ① & ②,

$$\therefore Z_{oc} \cdot Z_{sc} = \frac{A}{C} \cdot \frac{B}{D}$$

As $A = D$ for a T.L.,

$$Z_{oc} \cdot Z_{sc} = B/C$$

$$\left. \begin{aligned} B &= \sqrt{Z/Y} \cdot \sinh \sqrt{YZ} \\ C &= \sqrt{Y/Z} \sinh \sqrt{YZ} \end{aligned} \right\} \text{ for a long T.L.}$$

$$\therefore \frac{B}{C} = Z/Y$$

But from definition of surge impedance,

$$\frac{Z}{Y} = Z_c^2$$

$$\therefore Z_{oc} \cdot Z_{sc} = Z_c^2$$

$$\therefore \boxed{Z_0 = \sqrt{Z_{oc} \cdot Z_{sc}}}$$

Surge impedance Loading (SIL)

SIL is a very imp. parameter in the study of power system as it is used in the prediction of max. loading capacity of T.L.s.

Before understanding SIL, we must know what is surge impedance (Z_c) of

We know that a long T.L. have distributed inductance & capacitance as its inherent property. When the line is charged, the capacitance component feeds reactive power to the line, while the inductance component absorbs the reactive power. If we take the balance of the two reactive powers, we get,

Capacitive VAR = inductive VAR

$$\text{Capacitive VARs} = V \cdot I_c = V \cdot \frac{V}{X_c} = \frac{V^2}{X_c}$$

$$\text{Inductive VARs} = V \cdot I_L = I_L \cdot X_L \cdot I_L = I_L^2 X_L$$

$$\therefore \frac{V^2}{X_c} = I_L^2 X_L$$

$$\therefore \frac{V}{I} = \sqrt{X_L X_c} = \sqrt{\frac{2\pi f L}{2\pi f C}}$$

$$\frac{V}{I} = \sqrt{\frac{L}{C}} = Z_c$$

This quantity having the dimensions of resistance (Ω) is called as surge impedance.

It can be considered as a purely resistive load which when connected at the receiving end of the line, the reactive power generated by capacitive reactance will be completely absorbed by inductive reactance. It is nothing but the characteristic impedance (Z_c) of a lossless line.

The surge impedance of a line may be measured in terms of Z_{oc} & Z_{sc} where these are impedances measured at the sending end with receiving end open-circuited & short circuited resp.

We know that,

$$V_s = A \cdot V_R + B I_R$$

$$I_s = C \cdot V_R + D \cdot I_R$$

When there is O.C. at R.E, $I_R = 0$

$$\therefore V_s = A \cdot V_R$$

$$I_s = C \cdot V_R$$

$$\therefore Z_{oc} = \frac{V_s}{I_s} = A/C \quad \text{--- (1)}$$

When there is S.C. at R.E, $V_R = 0$

$$\therefore V_s = B \cdot I_R$$

$$I_s = D \cdot I_R$$

$$\therefore Z_{sc} = \frac{V_s}{I_s} = B/D \quad \text{--- (2)}$$

Multiply A ① & ②;

$$Z_{oc} \cdot Z_{sc} = \frac{A}{C} \cdot \frac{B}{D}$$

$$= \frac{B}{C} \quad \dots \text{ as } A = D \text{ for T.L.}$$

$$\therefore Z_{oc} \cdot Z_{sc} = B/C$$

~~B~~ For a long T.L.,

$$B = \sqrt{Z/Y} \cdot \sinh \sqrt{YZ}$$

$$C = \sqrt{Y/Z} \sinh \sqrt{YZ}$$

$$\therefore \frac{B}{C} = Z/Y$$

But from definition of surge impedance,

$$\frac{Z}{Y} = Z_c^2$$

$$\therefore Z_{oc} \cdot Z_{sc} = Z_c^2$$

$$\therefore \boxed{Z_c = \sqrt{Z_{oc} \cdot Z_{sc}}}$$

→ Surge
Impedance.

Now let's define SIL.

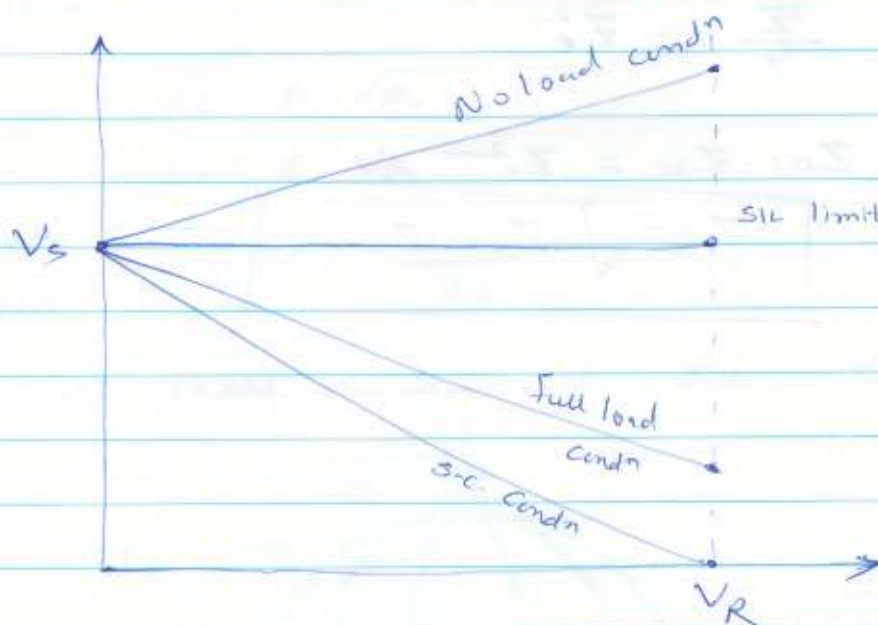
SIL is defined as the power delivered by a line to a purely resistive load equal in value to a surge impedance of that line.

The unit of SIL is watt.

When the line is terminated by surge impedance, the R.E. voltage is equal to S.E. voltage and this case is called as flat voltage profile.

* imp \rightarrow surge impedance & hence SIL is independent of the length of the line. The value of surge impedance will be the same at all the points on the line & hence the voltage.

Voltage behavior of a long T.L. (without shunt reactor installed)



The power transmitted under SIL condⁿ is

$$P_R = \frac{V_R^2}{Z_0}$$

$Z_0 \rightarrow$ surge impedance.

$P_R \rightarrow$ surge impedance loading.

\rightarrow is also called as natural power of the line.

Above eqⁿ gives a limit to the max. power that can be delivered and is useful in the design of the T.L.

SIL can be used for the comparison of loads that can be carried on the lines at diff. voltages.

In order to increase the power transmitted through a long T.L., either value of RE voltage is to be increased or more than one T.L. can be run in parallel. But the 2nd method is costly.

\therefore From above eqⁿ, in order to increase P_R , either V_R is to be increased or Z_0 is to be decreased.

Increase in V_R - now a days trend is for higher & higher voltages, so this is the most widely adopted method to increase the power limit.

Decrease in Z_0 - Since spacing betⁿ cond^rs cannot be decreased much, it being dependent on the line voltages & corona, etc, the value of

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Increase in V_R - now a days trend is for higher & higher voltages, so this is the most widely adopted method to increase the power limit.

Decrease in Z_c - Since spacing betⁿ cond^rs cannot be decreased much, it being dependent on the line voltages & corona, etc, the value of

Z_0 cannot be ~~var~~ varied much.

$Z_0 = \sqrt{L/C}$ for a lossless T-L. To decrease Z_0 , either L is decreased using series capacitors or C is increased using shunt capacitors.

Surge Impedance Loading (SIL)

Surge impedance loading is defined as the load that can be delivered by the line having no resistance, the load being at unity p.f.

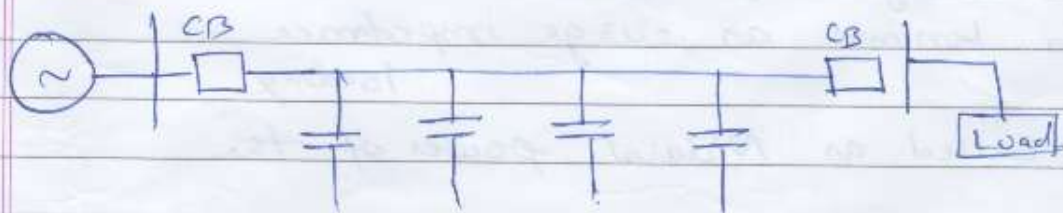
Surge impedance continued \rightarrow

A long T.L. have distributed inductance & capacitance as its property. When the line is charged, the capacitance component feeds reactive power to the line while the inductance absorbs the reactive power.

At the balance of two reactive powers,
 Capacitive VAR = Inductive VAR

The load at which the inductive & capacitive VARs are equal & opposite, such load is called surge impedance load. In SIL, the voltage & current are in the same phase at all the points of the line.

Shunt capacitance charges the T.L. when the circuit breaker at the S.E. is closed as shown below.

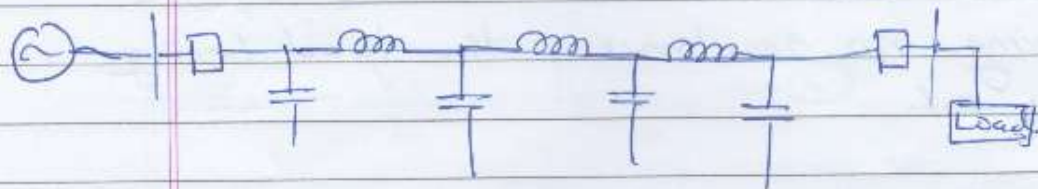


\therefore Capacitive VARs generated in the line are,

$$= \frac{V^2}{X_c} = V^2 \omega C \text{ per ph.}$$

$$\text{Wing} \left[V \cdot I = V \cdot \frac{V}{X_c} \right]$$

The series inductance of the line consumes the electrical energy when the S.E. & R.E. terminals are closed



$$\therefore \text{Inductive VARs absorbed by the line,} \\ = I^2 X_L = I^2 \omega L. \quad \left[P = V \cdot I = I^2 R \right]$$

\therefore Capacitive VARs = Inductive VARs

$$\frac{V^2}{X_C} = I^2 X_L$$

$$\therefore \frac{V}{I} = \sqrt{X_L \cdot X_C} \\ = \sqrt{\frac{L}{C}} = Z_0$$

The power transmitted under these conditions is,

$$P_R = \frac{V_R^2}{Z_0} \quad \left(\text{using } P = \frac{V^2}{R} \right)$$

Z_0 is surge impedance

P_R is known as surge impedance loading

also called as Natural power of the line.

Above eqn gives a limit to the max. power that can be delivered & is useful in the design of T.Ls.

SIL can be used for the comparison

of loads that can be carried on the lines at different voltages.

In order to increase the power transmitted through a long T.L., either value of R.E. Vltg. is to be increased or more than one T.L. can be run in parallel. But the 2nd method is costly. From the above eqn, in order to increase P_R , either V_R is to be increased or Z_0 is to be decreased.

Increase in V_R - Now a days the trend is for higher & higher voltages, so this is the most widely adopted method to increase the power limit.

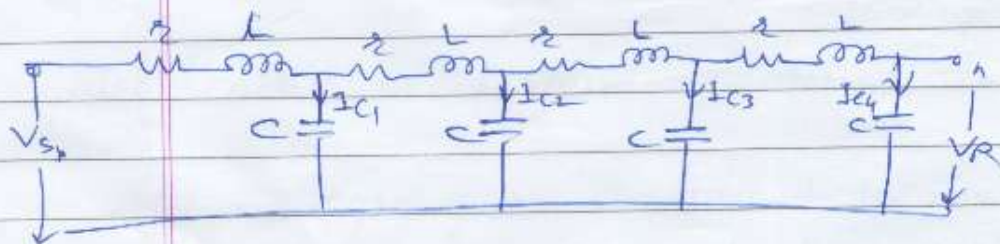
Decrease in Z_0 - Since spacing betn conductors cannot be decreased much, it being dependent on the line voltages & corona, etc., the value of Z_0 cannot be varied much. $Z_0 = \sqrt{L/C}$ for a lossless T.L. To decrease Z_0 either L is decreased using series capacitors or C is increased using shunt capacitors.

Ferranti Effect

In general, we know that for all electrical systems, current flows from region of higher potential to the region of lower potential. In all practical cases, the S.E. voltage is higher than the R.E. voltage due to line losses, so current flows from supply end to the load.

But Sir S.Z. Ferranti, in the year 1890, came up with an astonishing theory about medium & long T.L. suggesting that in case of light loading or no load operation, the R.E. Vtg increases beyond S.E. voltage. This phenomenon is called as Ferranti effect in P.S.

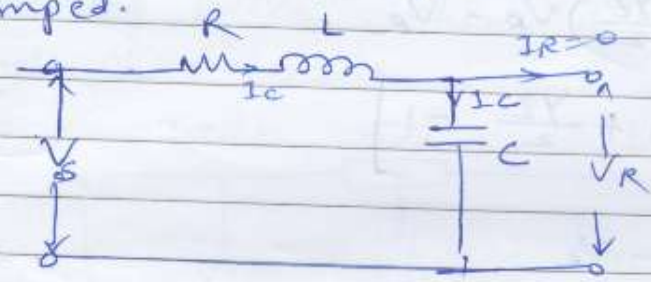
A long T.L. can be considered to be composed of a considerably high amount of capacitance & inductance distributed across the entire length of the line. Ferranti effect occurs when the current drawn by the distributed capacitance of the line itself is greater than the current associated with the load at the R.E. of the line (during light load or no load).



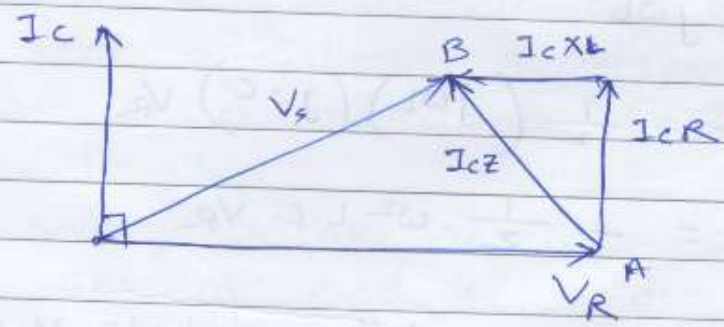
This capacitor charging current leads to a Vtg drop across the inductor

which is in phase with the S-E vltg. This vltg. drop keeps on increasing additively as we move towards the load end of the line and subsequently, the R.E. vltg. tends to get larger than applied vltg. leading to phenomena of Ferranti effect.

The above fig. can be replaced by the new one shown below (for approximate analysis) where, distributed parameters are shown lumped.



the V_R is drawn as ref phasor.



Charging current I_c is 90° ahead of V_R . This current causes vltg. drop AB which is equal to $I_c(R + j\omega L)$ in the line. It can be seen that V_s is lower than V_R .

Ferranti effect can also be explained with the help of nominal π ckt.

In nominal π model,

$$V_s = \left(1 + \frac{YZ}{2}\right) V_R + Z I_R$$

at no load, $I_R = 0$

$$\therefore V_s = \left(1 + \frac{YZ}{2}\right) V_R$$

$$\begin{aligned} \text{Now, } V_s - V_R &= \left(1 + \frac{YZ}{2}\right) V_R - V_R \\ &= V_R \left[1 + \frac{YZ}{2} - 1\right] \end{aligned}$$

$$V_s - V_R = \frac{YZ}{2} \cdot V_R$$

$$Z = R + j\omega L$$

$$Y = j\omega C$$

if R is neglected,

$$Z = j\omega L$$

$$\begin{aligned} \therefore V_s - V_R &= \frac{1}{2} (j\omega L)(j\omega C) V_R \\ &= -\frac{1}{2} \omega^2 L \cdot C \cdot V_R \end{aligned}$$

It shows that difference betⁿ V_s & V_R is -ve, i.e. V_R is bigger than V_s .

To reduce Ferranti effect-

1. To reduce R.E Vltg, shunt reactors can be connected at receiving end of the line. It will compensate the capacitive current

$$\cosh j\theta = \cos \theta$$

$$\sinh j\theta = j \sin \theta$$

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Tuned Power Lines

When receiving end voltage & current are numerically equal to the corresponding sending end values, so that there is no voltage drop on load. Such a line is called a tuned line.

The equation,

$$\begin{bmatrix} V_s \\ I_s \end{bmatrix} = \begin{bmatrix} \cosh r l & Z_0 \sinh r l \\ \frac{1}{Z_0} \sinh r l & \cosh r l \end{bmatrix} \begin{bmatrix} V_R \\ I_R \end{bmatrix}$$

characterises the performance of long T.L.

For a overhead line shunt conductance G is always negligible & it is sufficiently accurate to neglect the resistance R as well. With this assumption,

$$\cancel{r} = j\omega L$$

$$r = \sqrt{yZ} = j\omega \sqrt{LC}$$

$$\therefore \cosh r l = \cosh j\omega l \sqrt{LC} = \cos \omega l \sqrt{LC}$$

$$\sinh r l = \sinh j\omega l \sqrt{LC} = j \sin \omega l \sqrt{LC}$$

Hence above eqn is now,

$$\begin{bmatrix} V_s \\ I_s \end{bmatrix} = \begin{bmatrix} \cos \omega l \sqrt{LC} & j Z_0 \sin \omega l \sqrt{LC} \\ \frac{j}{Z_0} \sin \omega l \sqrt{LC} & \cos \omega l \sqrt{LC} \end{bmatrix} \begin{bmatrix} V_R \\ I_R \end{bmatrix}$$

Now if $\omega L\sqrt{LC} = n\pi$
 where $n=1, 2, 3, \dots$

$$\frac{V_s}{I_s} = \frac{V_R}{I_R}$$

$$|V_s| = |V_R|$$

$$|I_s| = |I_R|$$

ie the R.E voltage & current is numerically equal to corresponding S.E values.

For 50Hz, the length of the line for tuning is,

$$l = \frac{n\pi}{\omega\sqrt{LC}} = \frac{n\pi}{2\pi f\sqrt{LC}}$$

Since $\frac{1}{\sqrt{LC}} \approx v$, the velocity of light
 (300,000 km/sec)

$$l = \frac{1}{2} (n\lambda)$$

$$= \frac{1}{2} \lambda, \lambda, \frac{3}{2} \lambda, \dots$$

(Putting $n = 1, 2, 3, \dots$)

$$= 3000 \text{ km}, 6000 \text{ km}, \dots$$

It is a too long a distance of transmission from the point of view of cost & efficiency. (here resistance was neglected).

For a given line length, & frequency, tuning can be achieved by increasing L or C ie, by adding series inductances or shunt capacitances at several places along the line length. But this method is impractical & uneconomical for power frequency lines & is adopted, for telephony (communication) where

The method presently experimented for tuning of lines is, using series capacitance to cancel the effect of line inductance & using shunt inductors to neutralise line capacitance. A long line is divided into several sections which are individually tuned.

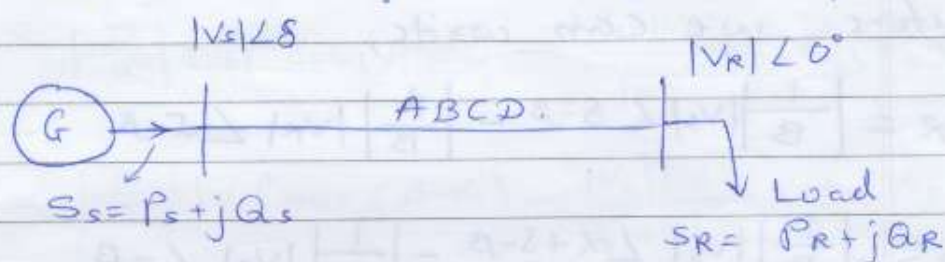
So far, the practical method of improving line regulation & power transfer capacity is to add series capacitors to reduce line inductance, shunt capacitors under heavy load conditions & shunt inductors under light or no-load conditions.

Power Flow through a T.L.

Till now T.L. performance eqns were in the form of vltg & current relationships betⁿ S.E. & R.E.

But since, loads are often expressed in terms of real & reactive powers, it is convenient to deal with T.L. eqns in the form of S.E. and R.E. complex power & voltages.

Consider a single T.L. (2 node / 2 bus sys.)



Consider a R.E. vltg as reference phasor ($V_R = |V_R| \angle 0^\circ$) and let S.E. vltg lead by an angle δ ($V_S = |V_S| \angle \delta$).

The angle δ is known as torque angle (torque angle is the angle by which rotating field lags the stator field in case of syn. motor).

The complex powers at S.E. & R.E. are,

$$S_R = P_R + jQ_R = V_R \cdot I_R^* \quad \text{--- (1)}$$

$$S_S = P_S + jQ_S = V_S \cdot I_S^* \quad \text{--- (2)}$$

From basic eqns of V_S & I_S i.e.

$$V_S = A V_R + B I_R \rightarrow B I_R = V_S - A V_R, \therefore I_R = \frac{1}{B} V_S - \frac{A}{B} V_R$$

$$I_S = C V_R + D I_R \quad I_S = C V_R + D \left[\frac{1}{B} V_S - \frac{A}{B} V_R \right]$$

I_S & I_R can be written as,

$$= C V_R + \frac{D}{B} V_S - \frac{DA}{B} V_R$$

$$= \frac{D}{B} V_S + V_R \left(C - \frac{DA}{B} \right)$$

$$I_R = \frac{1}{B} V_s - \frac{A}{B} V_R \quad \text{--- (3)}$$

$$I_s = \frac{D}{B} V_s - \frac{1}{B} V_R \quad \text{--- (4)}$$

$$\therefore I_s = \frac{D}{B} V_s + V_R \left(\frac{BC - AD}{B} \right)$$

$$= \frac{D}{B} V_s - \frac{1}{B} V_R$$

$$\therefore (BC - AD = -1)$$

Where, A, B, D are T-L constants as

$$A = |A| \angle \alpha$$

$$B = |B| \angle \beta$$

$$D = |D| \angle \delta \quad \text{--- (as } A = D)$$

Therefore, we can write,

$$I_R = \left| \frac{1}{B} \right| |V_s| \angle \delta - \beta - \left| \frac{A}{B} \right| |V_R| \angle \alpha - \beta \quad \text{--- (5)}$$

$$I_s = \left| \frac{D}{B} \right| |V_s| \angle \delta + \delta - \beta - \left| \frac{1}{B} \right| |V_R| \angle -\beta \quad \text{--- (6)}$$

Substituting for I_R in eqn (1) & I_s in eqn (2)

$$\begin{aligned} S_R &= |V_R| \angle 0 \left[\left| \frac{1}{B} \right| |V_s| \angle \delta - \beta - \left| \frac{A}{B} \right| |V_R| \angle \alpha - \beta \right] \\ &= \frac{|V_R| |V_s|}{|B|} \angle (\delta - \beta) - \frac{|A| |V_R|^2}{|B|} \angle (\beta - \alpha) \quad \text{--- (7)} \end{aligned}$$

Similarly,

$$S_s = \left| \frac{D}{B} \right| |V_s|^2 \angle (\delta + \delta - \beta) - \frac{|V_s| |V_R|}{|B|} \angle (\beta + \delta) \quad \text{--- (8)}$$

Eqns (7) & (8) gives 3 ph. MVA if V_s & V_R are expressed in kv line.

If eqns (7) & (8) expressed in real & imaginary parts, we can write ~~real~~ real & reactive powers at R.E & S.E. as,

$$P_R = \frac{|V_s||V_R|}{|B|} \cos(\beta - \delta) - \left| \frac{A}{B} \right| |V_R|^2 \cos(\beta - \alpha) \quad \text{--- (9)}$$

$$Q_R = \frac{|V_s||V_R|}{|B|} \sin(\beta - \delta) - \left| \frac{A}{B} \right| |V_R|^2 \sin(\beta - \alpha) \quad \text{--- (10)}$$

Similarly,

$$P_S = \left| \frac{D}{B} \right| |V_s|^2 \cos(\beta - \alpha) - \frac{|V_s||V_R|}{|B|} \cos(\beta + \delta) \quad \text{--- (11)}$$

$$Q_S = \left| \frac{D}{B} \right| |V_s|^2 \sin(\beta - \alpha) - \frac{|V_s||V_R|}{|B|} \sin(\beta + \delta) \quad \text{--- (12)}$$

From eqn (9),

P_R will be max. when $\delta = \beta$

$$\therefore P_{R(\max)} = \frac{|V_s||V_R|}{B} - \left| \frac{A}{B} \right| |V_R|^2 \cos(\beta - \alpha) \quad \text{--- (13)}$$

and corresponding Q_R will be,

$$Q_R = - \left| \frac{A}{B} \right| |V_R|^2 \sin(\beta - \alpha) \quad \text{--- (14)}$$

Thus load must draw this much leading reactive power (MVAR) in order to receive maximum real power.

Consider now a special case of short T-L with series impedance Z , so

$$A = D = 1 \angle 0^\circ$$

$$B = Z = |Z| \angle \theta$$

Substituting these values in eqs (9), (10), (11) & (12) we get simplified results for short line as,

$$P_R = \frac{|V_s||V_R|}{|Z|} \cos(\theta - \delta) - \frac{|V_R|^2}{|Z|} \cos \theta \quad \text{--- (15)}$$

$$Q_R = \frac{|V_s||V_R|}{|Z|} \sin(\theta - \delta) - \frac{|V_R|^2}{|Z|} \sin \theta \quad \text{--- (16)}$$

$$P_S = \frac{|V_s|^2}{|Z|} \cos \theta - \frac{|V_s||V_R|}{|Z|} \cos(\theta + \delta) \quad \text{--- (17)}$$

$$Q_S = \frac{|V_s|^2}{|Z|} \sin \theta - \frac{|V_s||V_R|}{|Z|} \sin(\theta + \delta) \quad \text{--- (18)}$$

• Above short line eqn will also be applicable for a long line when the line is replaced by its equivalent π or nominal π . This technique is always used to treat the load flow problem.

From eqn (15), max. R-E power is when $\delta = 0$, so that

$$\therefore P_R(\max) = \frac{|V_s||V_R|}{|Z|} - \frac{|V_R|^2}{|Z|} \cos \theta$$

$$\cos \theta = R/|Z|$$

$$\therefore P_R(\max) = \frac{|V_s||V_R|}{|Z|} - \frac{|V_R|^2}{|Z|^2} \cdot R \quad \text{--- (19)}$$

Normally the resis. of T.L is small as compared to its reactance, so that,

$$\theta = \tan^{-1} \frac{x}{R} \approx 90^\circ \quad \text{where } Z = R + jx$$

\therefore Eqn (15) & (16) at R.E can be approximated as,

$$P_R = \frac{|V_s||V_R|}{x} \sin \delta \quad \text{--- (20)}$$

$$Q_R = \frac{|V_s||V_R|}{x} \cos \delta - \frac{|V_R|^2}{x} \quad \text{--- (21)}$$

Eqn (21) can be further simplified by assuming $\cos \delta \approx 1$ since δ is normally small,

$$\therefore Q_R = \frac{|V_R|}{x} (|V_s| - |V_R|) \quad \text{--- (22)}$$

Let $|V_s| - |V_R| = |\Delta V|$ drop in Vtg. along the T.L,

$$Q_R = \frac{|V_R|}{x} |\Delta V| \quad \text{--- (23)}$$

Conclusions from eqn (20) to (23) -

1. For a valid approximation of $R \approx 0$, the real power transferred to R.E. is proportional to $\sin \delta$ while reactive power is proportional to magnitude of Vtg. drop across the line.
2. Real power received is max. for $\delta = 90^\circ$ & the value $\frac{|V_s||V_R|}{x}$

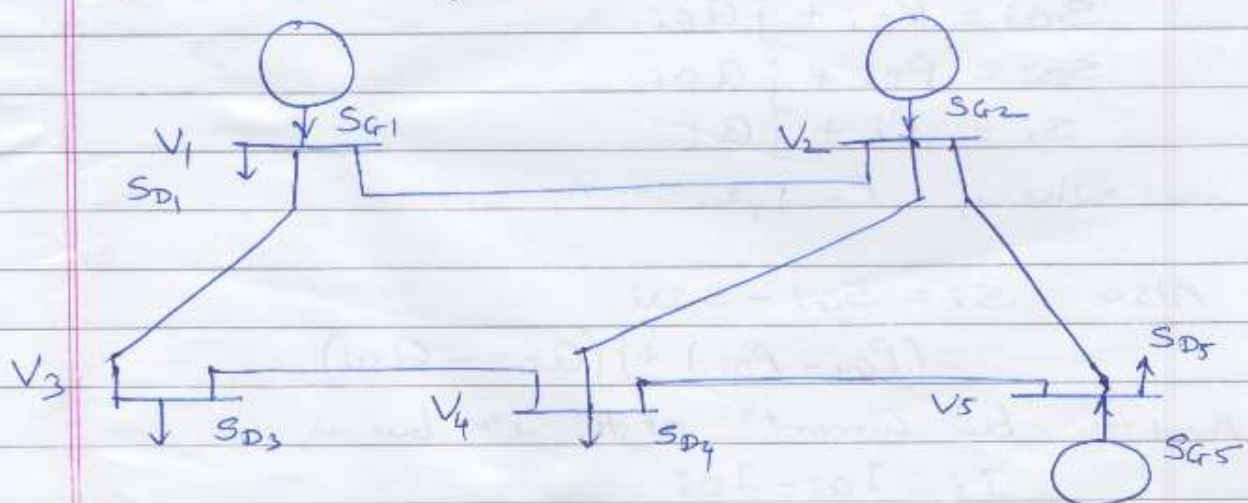
3. The VARs (lagging) delivered by a line is proportional to the line voltage drop & is independent of S .

Power system stability

↳ Bus Admittance Matrix

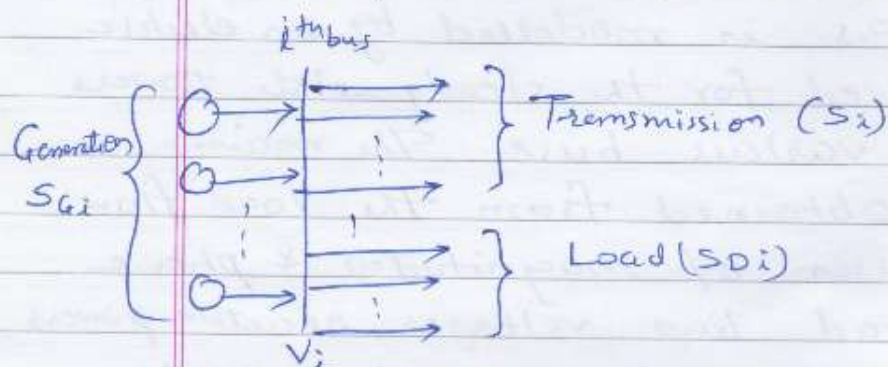
Load flow study in power system is the steady state solution of the power system network. The P.S. is modelled by an electric network & solved for the steady state powers & voltages at various buses. The main information obtained from the load flow study comprises of magnitudes & phase angles of load bus voltages, reactive powers and voltage phase angles at generator buses, real & reactive power flow on T.L. together with powers at reference buses, etc. This information is essential for continuous monitoring of the current state of the sys. and for analysing the effectiveness of alternate plans for the future such as adding new generator sites, meeting increased load demand & locating new transmission sites.

Fig. below shows one line diagram of P.S. having five buses.



S_{Gi} & S_{Di} represent the complex power injected by the generators & complex power drawn by the loads. V_i represents complex voltages at various buses.

In practical sys. there can be thousands of buses & transmission links.



Consider an i^{th} bus of an ' n ' bus power system

It is convenient to work with power at each bus injected into the transmission sys., called the "bus power".

The i^{th} bus power is,

$$S_i = S_{Gi} - S_{Di}$$

writing the complex powers in terms of real & reactive powers,

$$S_{Gi} = P_{Gi} + jQ_{Gi}$$

$$S_{Di} = P_{Di} + jQ_{Di}$$

$$S_i = P_i + jQ_i$$

where, $i = 1, 2, \dots, n$

$$\text{Also } S_i = S_{Gi} - S_{Di}$$

$$= (P_{Gi} - P_{Di}) + j(Q_{Gi} - Q_{Di})$$

And the "bus current" at the i^{th} bus is,

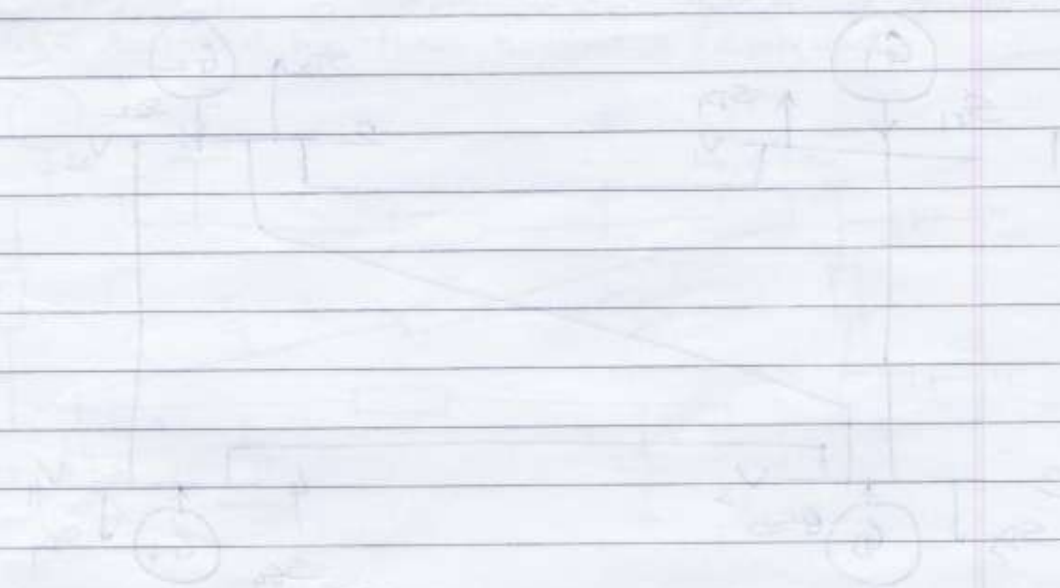
$$I_i = I_{Gi} - I_{Di}$$

Next step is to develop relation between bus currents & bus voltages with following assumptions,

- i) there is no coupling betn the T.L.s &
- ii) there is an absence of regulating transformers.

Let y_{ik} ($i \neq k$) be the total admittance between the i^{th} & k^{th} buses.

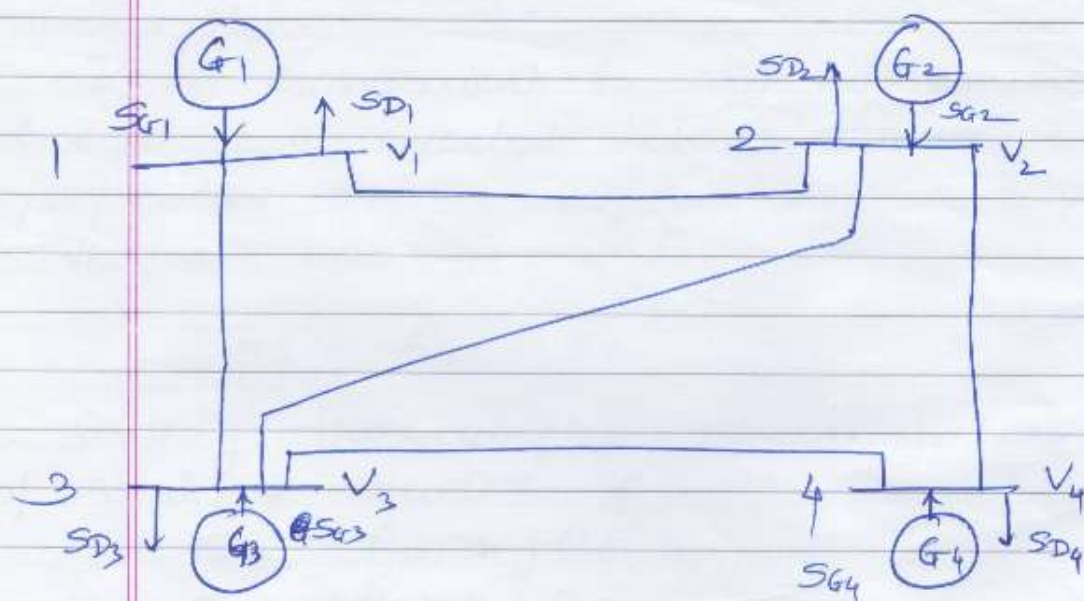
y_{i0} be the admittance between i^{th} bus & the ground



In a power system, power is injected into a bus from generators, while the loads are tapped from it. There may be some buses with only generators & there may be other only with loads. Some buses may have both, while some other may have static capacitors for reactive power compensation.

The surplus power at some of the buses is transported through T.L. to the bus deficient in power.

Single line diag. of a 4-bus sys. with generators & load at each bus is shown below.



Let $S_{Gi} \rightarrow$ 3 ph. complex power generated

$S_{Di} \rightarrow$ 3 ph. complex power demand

These are represented as,

$$S_{Gi} = P_{Gi} + jQ_{Gi}$$

$$S_{Di} = P_{Di} + jQ_{Di}$$

∴ Net complex power injected into the bus is difference of S_{Gi} & S_{Di}

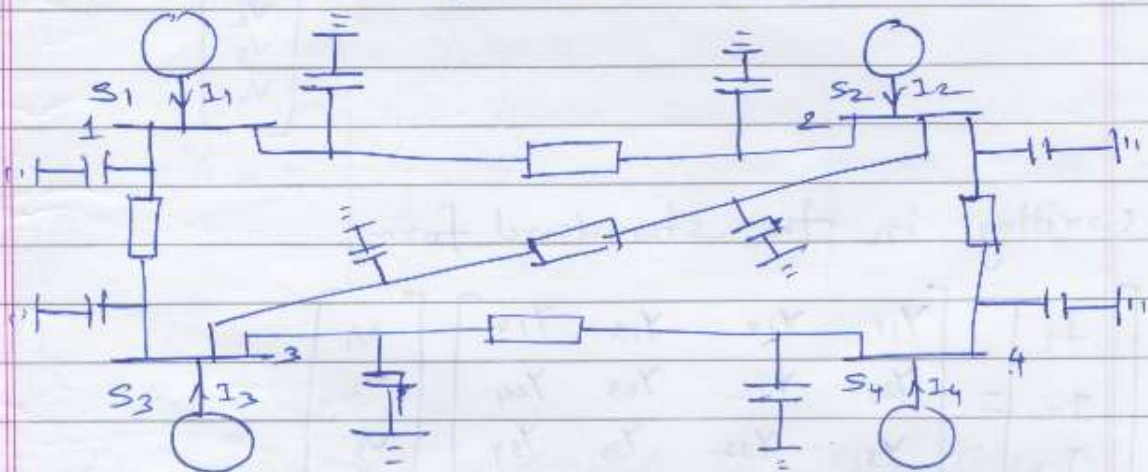
$$S_i = P_i + jQ_i = (P_{Gi} - P_{Di}) + j(Q_{Gi} - Q_{Di})$$

where, $P_i = P_{Gi} - P_{Di}$

$Q_i = Q_{Gi} - Q_{Di}$

where $i = 1, 2, 3, \dots, n$.

The one line diagram above is replaced by its equivalent circuit & shown below, where, the equivalent power source at i th bus injects current I_i into the bus. The structure is such that all the sources are always connected to a common ground node. T.L. are replaced by their nominal π equivalent.



The line admittance betⁿ nodes i & k is represented by $Y_{ik} = Y_{ki}$. Further the mutual admittances betⁿ the lines is assumed to be zero.

In above figure, there are five nodes i.e., 4 nodes corresponding to 4 buses and one ground node.

Applying KCL to the four nodes gives the following eq^{ns}.

$$I_1 = V_1 \cdot Y_{10} + (V_1 - V_2) Y_{12} + (V_1 - V_3) Y_{13}$$

$$I_2 = V_2 \cdot Y_{20} + (V_2 - V_1) Y_{21} + (V_2 - V_3) Y_{23} + (V_2 - V_4) Y_{24}$$

$$I_3 = V_3 \cdot Y_{30} + (V_3 - V_1) Y_{31} + (V_3 - V_2) Y_{23} + (V_3 - V_4) Y_{34}$$

$$I_4 = V_4 \cdot Y_{40} + (V_4 - V_2) Y_{24} + (V_4 - V_3) Y_{34}$$

Rearranging above eq^{ns}, we get

$$\begin{bmatrix} I_1 \\ I_2 \\ I_3 \\ I_4 \end{bmatrix} = \begin{bmatrix} Y_{10} + Y_{12} + Y_{13} & -Y_{12} & -Y_{13} & 0 \\ -Y_{12} & Y_{20} + Y_{21} + Y_{23} + Y_{24} & -Y_{23} & -Y_{24} \\ -Y_{13} & -Y_{23} & Y_{30} + Y_{31} + Y_{23} + Y_{34} & -Y_{34} \\ 0 & -Y_{24} & -Y_{34} & Y_{40} + Y_{24} + Y_{34} \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \\ V_3 \\ V_4 \end{bmatrix}$$

Writing in the standard form,

$$\begin{bmatrix} I_1 \\ I_2 \\ I_3 \\ I_4 \end{bmatrix} = \begin{bmatrix} Y_{11} & Y_{12} & Y_{13} & Y_{14} \\ Y_{21} & Y_{22} & Y_{23} & Y_{24} \\ Y_{31} & Y_{32} & Y_{33} & Y_{34} \\ Y_{41} & Y_{42} & Y_{43} & Y_{44} \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \\ V_3 \\ V_4 \end{bmatrix}$$

$$\text{OR. } I_{\text{bus}} = Y_{\text{bus}} \cdot V_{\text{bus}}$$

As it can be observed that, the diagonal elements of the Y_{bus} are self admittances and are given by,

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$$Y_{11} = Y_{10} + Y_{12} + Y_{13}$$

$$Y_{22} = Y_{20} + Y_{21} + Y_{23} + Y_{24}$$

$$Y_{33} = Y_{30} + Y_{31} + Y_{32} + Y_{34}$$

$$Y_{44} = Y_{40} + Y_{42} + Y_{43}$$

The off-diagonal elements are the transfer (or mutual admittances) and are given by

$$Y_{12} = Y_{21} = -Y_{12}$$

$$Y_{13} = Y_{31} = -Y_{13}$$

$$Y_{14} = Y_{41} = 0$$

$$Y_{23} = Y_{32} = -Y_{23}$$

$$Y_{24} = Y_{42} = -Y_{24}$$

$$Y_{34} = Y_{43} = -Y_{34}$$

Similarly, general eqn for n-bus network based on KCL & admittance form is,

$$I = Y_{bus} \cdot V$$

$$Y_{bus} = \begin{bmatrix} Y_{11} & Y_{12} & \dots & Y_{1n} \\ Y_{21} & Y_{22} & \dots & Y_{2n} \\ \vdots & \vdots & \ddots & \vdots \\ Y_{n1} & Y_{n2} & \dots & Y_{nn} \end{bmatrix}$$

Properties:-

1. The diagonal elements of Y_{bus} are the self admittances
2. Off-diagonal elements are the transfer admittances
3. Y_{bus} is $n \times n$ matrix where n is no. of buses.
4. Y_{bus} is a symmetric matrix
5. $Y_{ik} = 0$ if i^{th} & k^{th} buses are not connected.

Corona

In overhead T.L., when the electric gradient is set up betn two parallel conductors, electrons & ions are set in motion by the electric field resulting in a small current betn the conductors.

This phenomenon undergoes a radical change when the electric field intensity reaches a value of 30 kV/cm .

At this stage, the moving ions attain a high velocity and strike the adjacent molecule dislodging electron from it. This dislodged electron further collide with other molecules producing more ions. This ultimately results in a complete electric breakdown of the dielectric medium betn the conductors establishing an arc between these two conductors.

This phenomenon is further prominent as the electric field intensity goes up. It is maximum on the surface of the conductors and decreases in inverse proportion to the distance from centre of the conductors. The ionisation is generally accompanied by a violet luminous glow around the conductor. The glow is brightest near the rough surface of the conductor. It is also associated with a hissing sound.

This phenomenon is designated as Corona & is very much evident in power lines of 100 kV & above.

Corona also produces power loss

and depends on weather conditions. During humid & moist climate corona loss is higher.

In EHV AC transmission lines the visible glow of corona is mostly uniform about both the conductors. Whereas in HVDC sys. the glow is uniform about the positive wire but spotty about the negative wire.

Factor affecting corona loss

1. Frequency - Corona loss is directly prop. to supply freq.
2. sys. voltage - As electric field increases with greater potential difference, corona loss increases.
3. Conductivity of air - During rain & thunderstorms, ion content in air increases & thus corona loss becomes high.
4. Conductor diameter - With greater conductor diameter, electric field intensity reduces resulting in low corona loss.
5. Load current - Flow of load current increases temperature of conductor. Thus it prevents deposition of dew or ~~snow~~ snow on conductor surface & thus reduces corona loss.
6. Conductor surface - Roughness of conductor surface results in field distortion & gives rise to high potential gradient causing higher corona loss.

Wireless signals are adversely affected by corona discharges. Corona discharge may create interference to communication line within even few kilometers.

Methods of reducing corona-

Intense corona effects are observed at a working voltage of 33 kV or above. Therefore, careful design should be made to avoid corona on substations or bus-bars rated for 33 kV & higher voltage.

Corona effects can be reduced by following methods:-

i) By increasing conductor size - By this method, the voltage at which corona occurs is raised & hence corona effects are considerably reduced. For this reason only, ACSR conductors are used in T.L.s & have large c.s. area.

ii) By increasing conductor spacing - because of this, the voltage at which corona occurs is raised & hence corona effects can be eliminated.

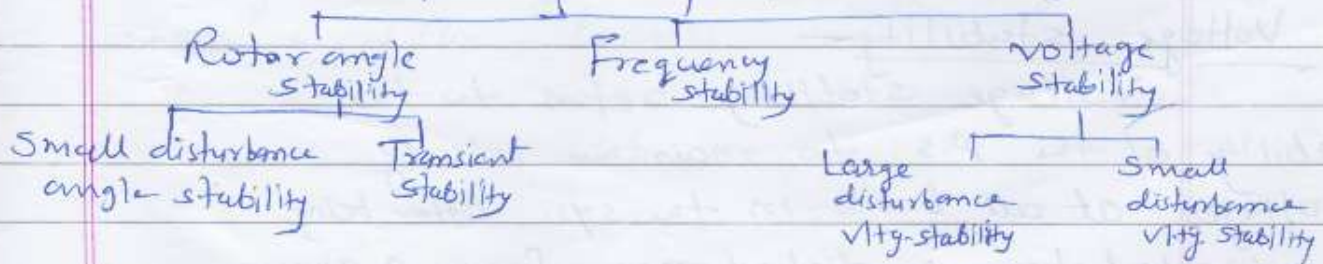
Power system Stability

Society today need an ever-increasing supply of electric power & the demand is increasing every year. Successful operation of P.S. depends largely on the ability to provide reliable & uninterrupted service to the loads. Ideally, the loads must be fed at constant voltage & frequency at all times. In any case, all interconnected synchronous machines should remain in synchronism if the sys. is stable, i.e., they should all remain operating in parallel & at the same speed.

If the Oscillatory response of a P.S. during the transient period following a disturbance is damped & the sys. settles in a finite time to a new steady operating condition, ~~it is~~ the sys. is said to be stable.

P.S. stability is the ability of an electric power system, for a given initial operating condition, to regain a state of operating equilibrium after being subjected to a physical disturbance, with most system variables bounded so that practically the entire sys. remains intact.

Classification of P.S. stability



Rotor angle stability -

Rotor angle stability refers to the ability of syn. machines of an interconnected p.s. to remain in synchronism after being subjected to disturbance. It depends on the ability to restore equilibrium betn electromagnetic torque & mechanical torque of each syn. machine in the sys. Instability that may result occurs in the form of increasing angular swings of some generators leading to their loss of synchronism with other generators.

Small disturbance rotor angle stability is concerned with the ability of the p.s. to maintain synchronism under small disturbances.

Transient stability is concerned with the ability of the p.s. to maintain synchronism when subjected to a severe disturbance, such as a s.c. on a T.L. Transient stability depends on both the initial operating state of the sys. & severity of disturbance. Instability is usually in the form of periodic angular separation due to insufficient synchronising torque, first swing instability, etc.

Voltage stability -

Voltage stability refers to the ability of the p.s. to maintain steady voltages at all buses in the sys. after being subjected to a disturbance from a given initial operating condition. It depends on

the ability to restore equilibrium betⁿ load demand & load supply from the P.S.

Instability that may result occurs in the form of a progressive fall or rise of voltages of some buses. A possible outcome of voltage instability is loss of load in an area, or tripping of T.L.s and other elements by their protective systems leading to cascading outages. Loss of synchronism of some generators may result from these outages.

Large disturbance vltg. stability refers to the system's ability to maintain steady voltages following large disturbances such as system faults, loss of generation, etc.

Small disturbance voltage stability is sys.'s ability to maintain steady vltg. when subjected to small disturbances such as incremental changes in sys. load.

Frequency stability.

It refers to ability of the P.S. to maintain steady freq. following a severe system problem resulting in significant imbalance betⁿ generation & load. The instability occurs in the form of sustained frequency swings leading to tripping of generating units and/or loads.