

## Experiment No-6

~~Q 34. For 122.65~~ obtain the IEEE 754 standards of Single precision and Double Precision format for  $(85.125)_{10}$  ?

The IEEE Standard for Floating-Point Arithmetic (IEEE 754) is a technical standard for floating-point computation which was established in 1985 by the Institute of Electrical and

**Electronics Engineers (IEEE).** The standard addressed many problems found in the diverse floating point implementations that made them difficult to use reliably and reduced their portability. IEEE Standard 754 floating point is the most common representation today for real numbers on computers, including Intel-based PC's, Macs, and most Unix platforms. There are several ways to represent floating point number but IEEE 754 is the most efficient in most cases. IEEE 754 has 3 basic components:

1. **The Sign of Mantissa –**

This is as simple as the name. 0 represents a positive number while 1 represents a negative number.

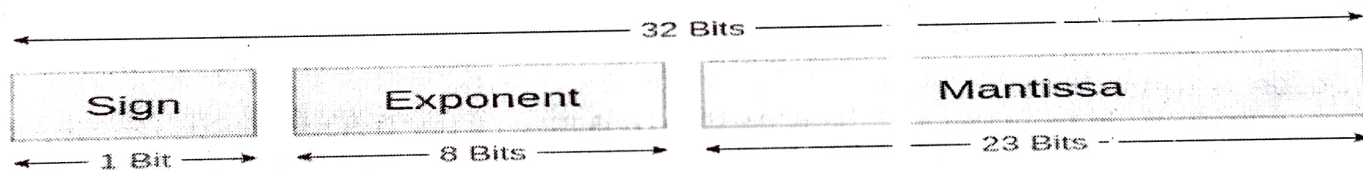
2. **The Biased exponent –**

The exponent field needs to represent both positive and negative exponents. A bias is added to the actual exponent in order to get the stored exponent.

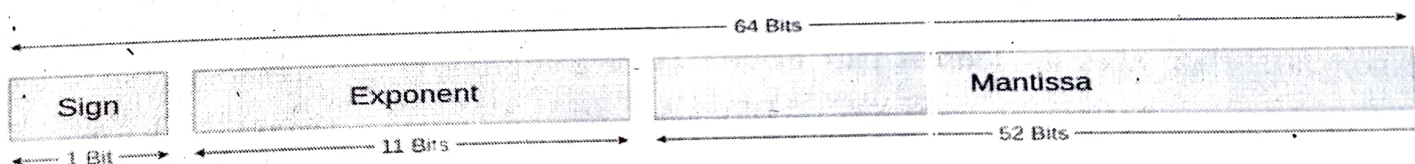
3. **The Normalised Mantissa –**

The mantissa is part of a number in scientific notation or a floating-point number, consisting of its significant digits. Here we have only 2 digits, i.e. 0 and 1. So a normalised mantissa is one with only one 1 to the left of the decimal.

**IEEE 754 numbers are divided into two based on the above three components: single precision and double precision.**



**Single Precision  
IEEE 754 Floating-Point Standard**



**Double Precision  
IEEE 754 Floating-Point Standard**



Click reset to clear and re-enter the values

## Simulations

DECIMAL  
NUMBER

85.125

BITS FOR  
EXPONENT

8

Submit

Reset

## RESULTS

8-bit binary

010000101

Binary Representation Of  
Integral Part

1010101

Binary Representation Of  
Fractional Part

001

Binary Representation of  
the Number

1010101.001

Normalised  
Representation of the  
Number

1. X 2 power6

Sign

0

Bias

127

Mantiss

Expone

133



Click reset to clear and re-enter the values

## Simulations

DECIMAL  
NUMBER

85.125

BITS FOR  
EXPONENT

11

Submit

Reset

## RESULTS

8-bit binary

0 1 0 0 0 0 0 0 1 0 1

Binary Representation Of  
Integral Part

1010101

Binary Representation Of  
Fractional Part

001

Binary Representation of  
the Number

1010101.001

Normalised  
Representation of the  
Number

1. X 2 power6

Sign

0

Bias

1023

Mantiss

Expone

1029

