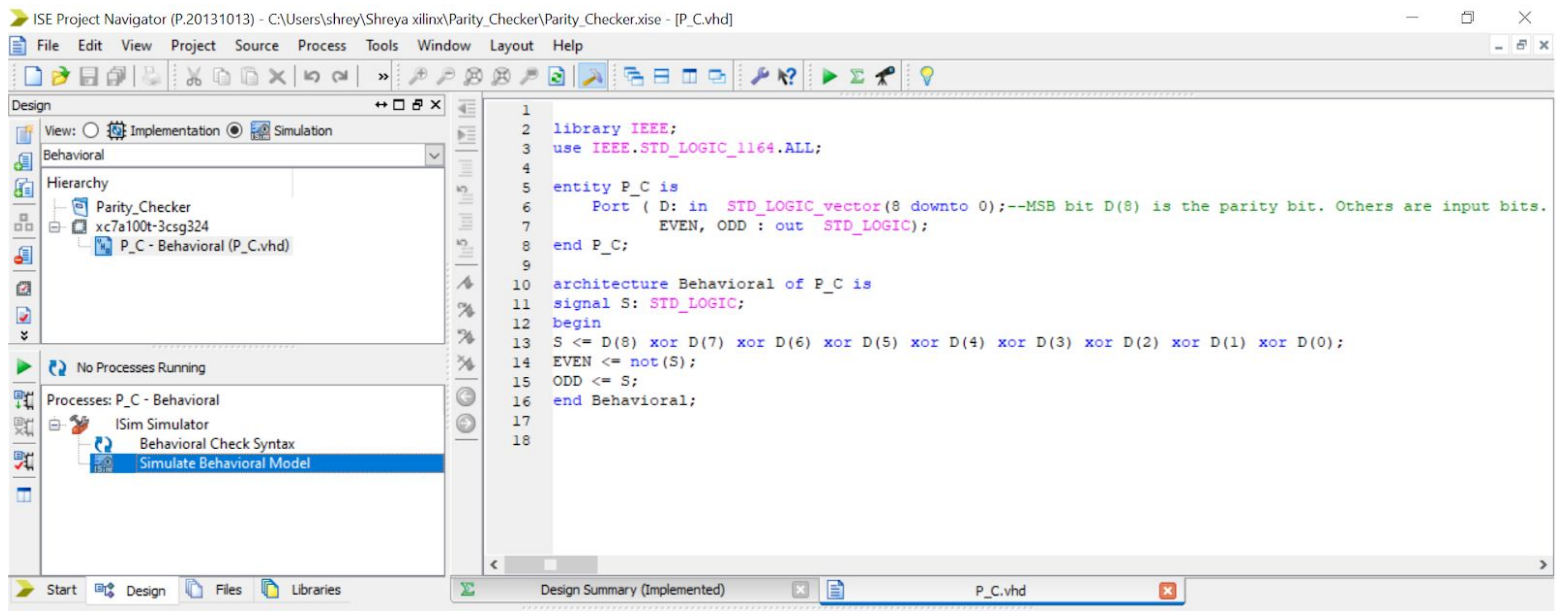


VHDL ASSIGNMENT: PARITY CHECKER

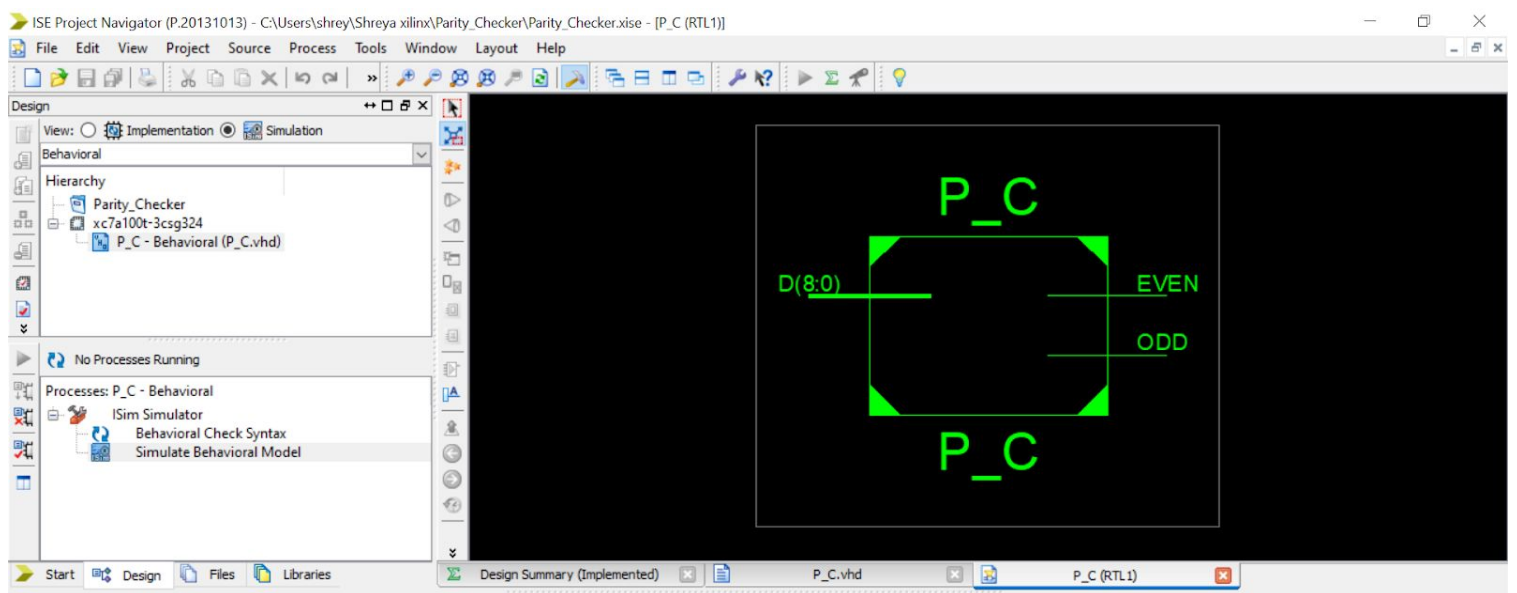
Name: Shreya Mamadapur

Student ID: C0774035

Source Code:



Schematics:



VHDL ASSIGNMENT: PARITY CHECKER

Waveform:

