

I1: ADD R1, R2, R3;  
I2: SUB R5, R1, R4;  
I3: CMP R1, R5;

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I1: SUB R1, R4, R5;  
I2: LD R2, 4[R2];  
I3: ADD R5, R1, R6;

---

I1: LD R2, 4[R2];  
I2: SUB R1, R4, R5;  
I3: ADD R5, R1, R6;

---

I1: LD R2, 10[R2];  
I2: ADD R5, R1, R2;

---

I1: MOV R2, 2;  
I2: MOV R3, 3;  
I3: MOV R5,5;  
I4: ADD R1, R2, R3;  
I5: CMP R1,R5;  
I6: beq .foo  
I7: ADD R7 R8 R9  
I8: ADD R6 R4 R1  
.foo:  
I9:ADD R6,R5,R3

---

I1: MOV R2, 3;  
I2: MOV R3, 3;  
I3: MOV R5,5;  
I4: ADD R1, R2, R3;  
I5: CMP R1,R5;  
I6: beq .foo  
I7: ADD R7 R8 R9  
I8: ADD R6 R4 R1  
.foo:  
I9:ADD R6,R5,R3

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