

PicoZed FMC Carrier Gen2

Avnet Engineering Services

www.picozed.org**Sheet Name**

01 - Avnet Lead Sheet

02 - Block Diagram

03 - Clocks & USB OTG

04 - Peripherals - 1

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07 - Switches, LEDs

08 - HDMI Interface

09 - PCIe & JX1, JX2, JX3

10 - Power & Control

11 - Back Page



PicoZed FMC Carrier Gen2

3/15/2016

10:37:25 AM

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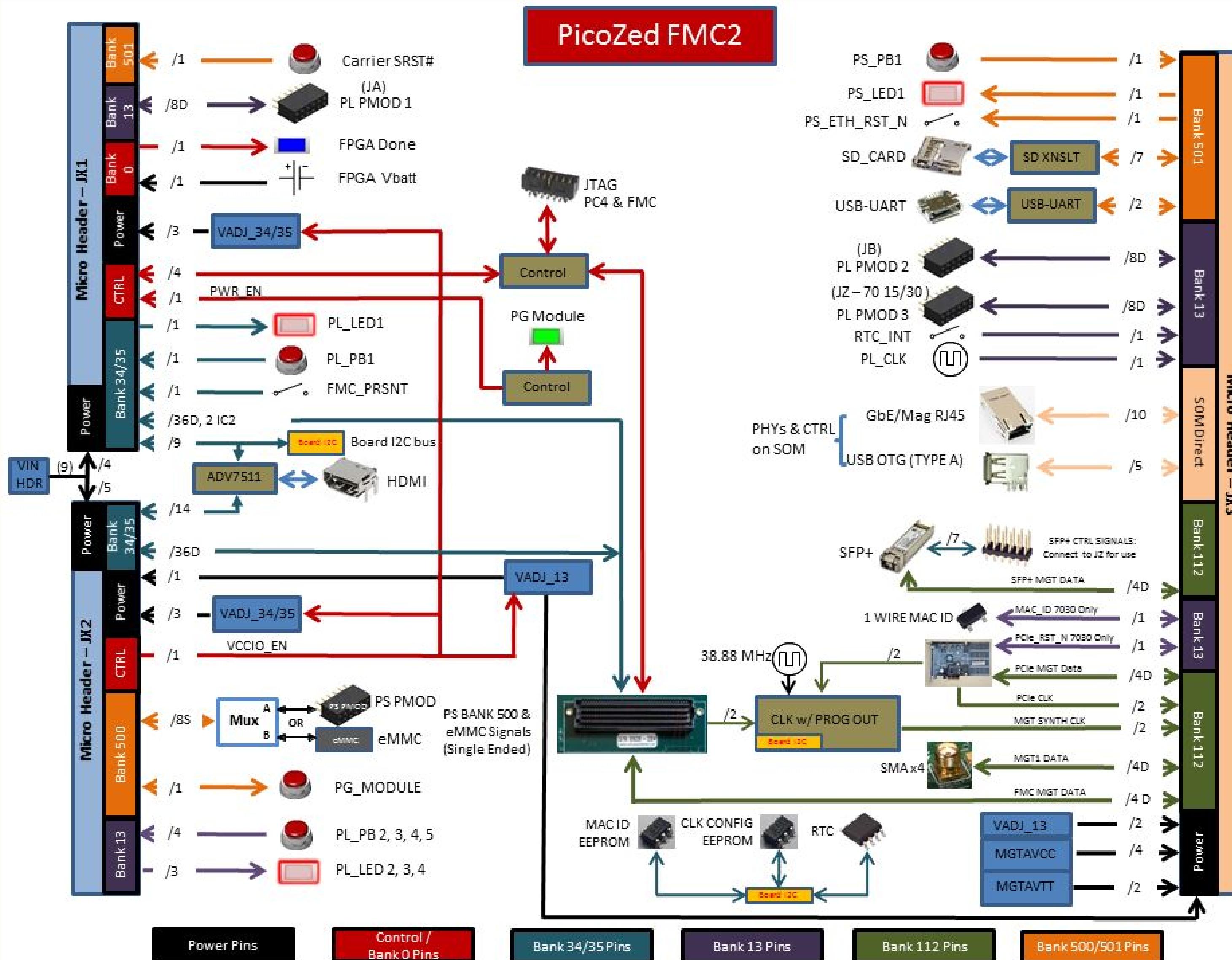
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Note?

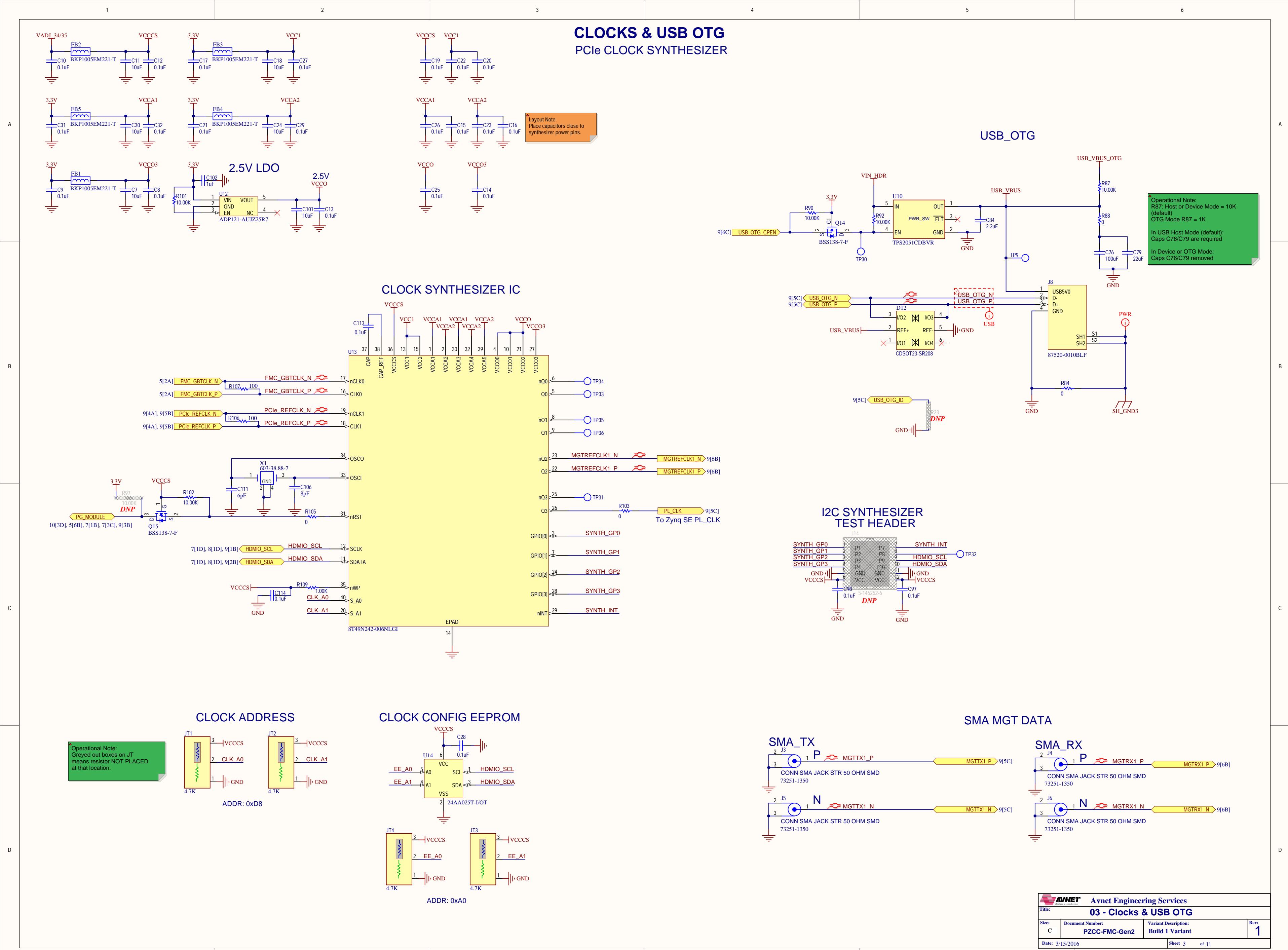
	Avnet Engineering Services
Title:	01 - Avnet Lead Sheet
Size:	B
Document Number:	PZCC-FMC-Gen2
Variant Description:	Build 1 Variant
Date:	3/15/2016
Sheet	1 of 11
Rev:	1

Topology

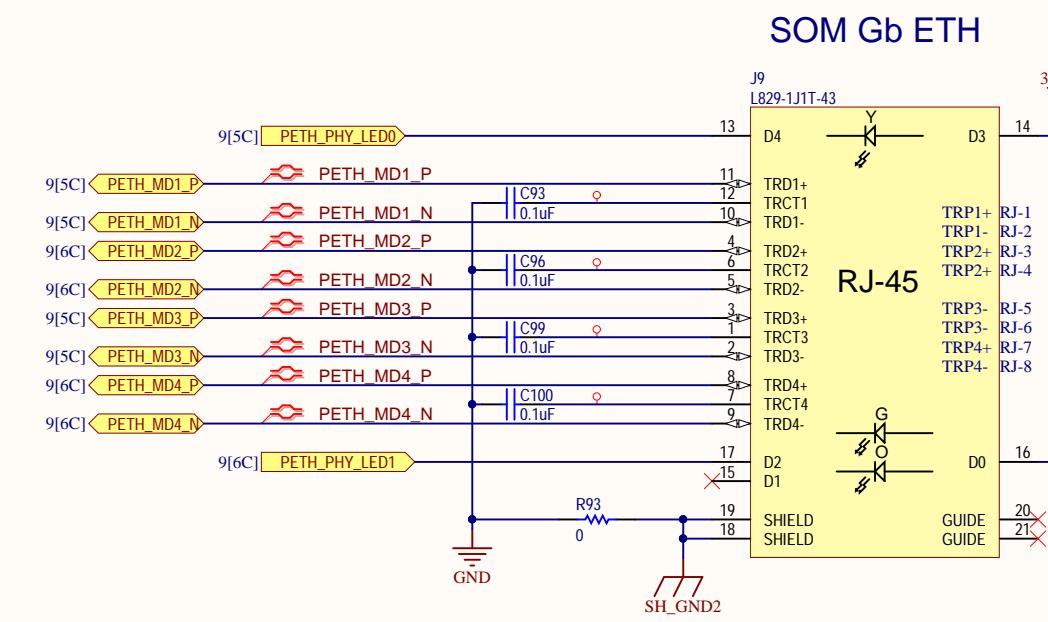
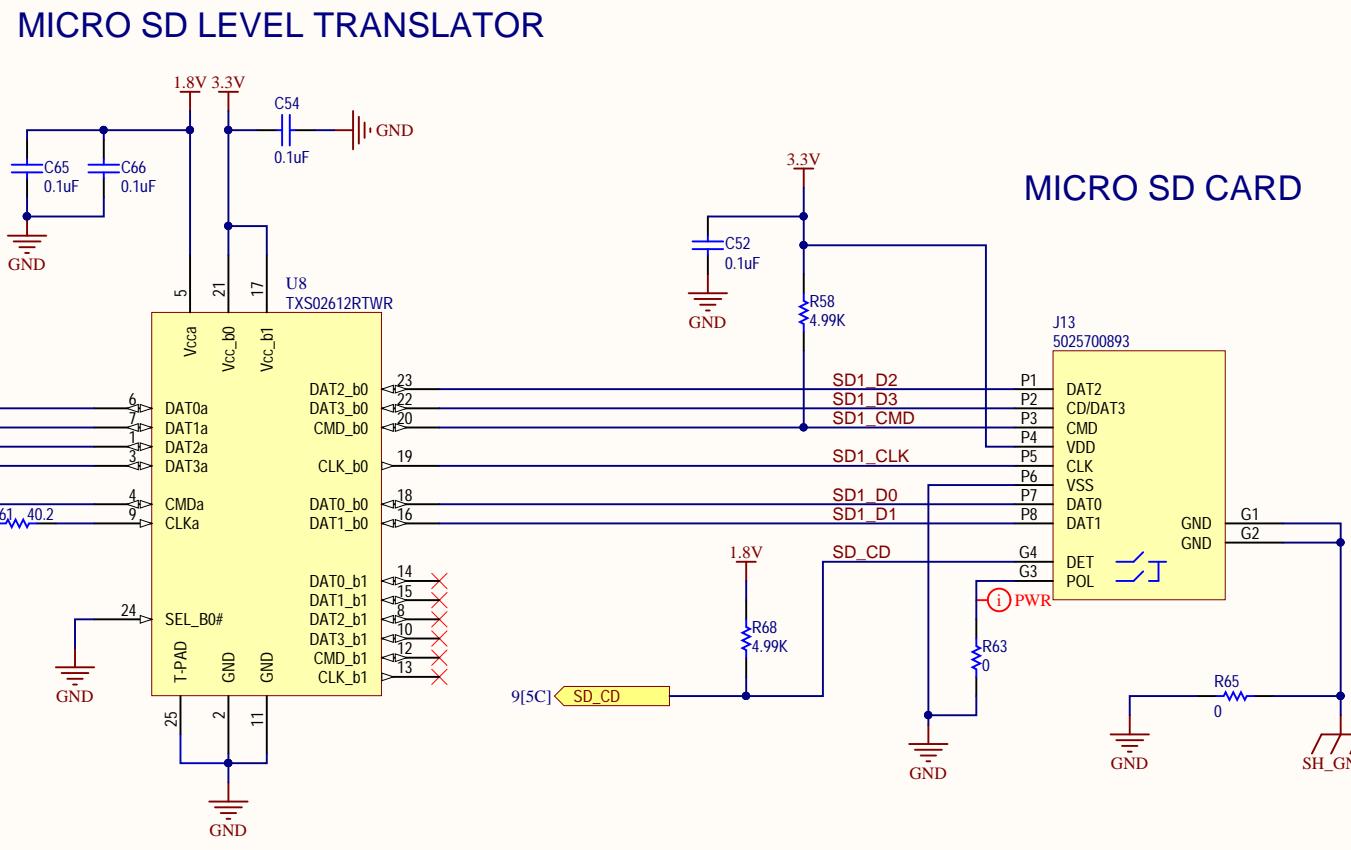


CLOCKS & USB OTG

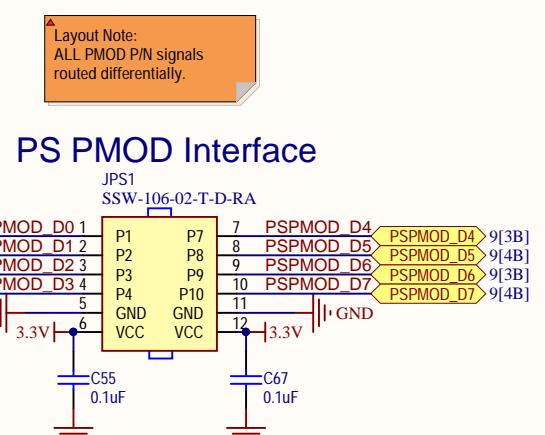
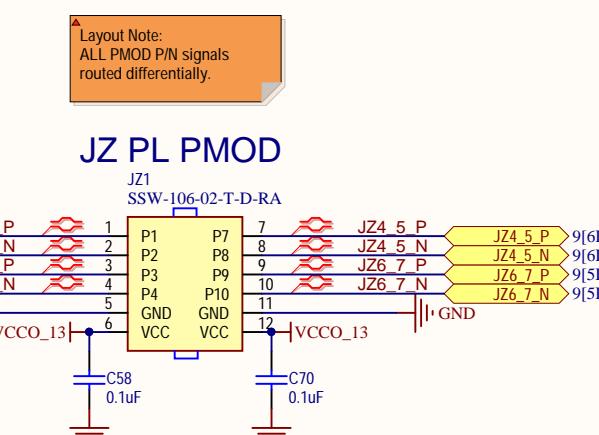
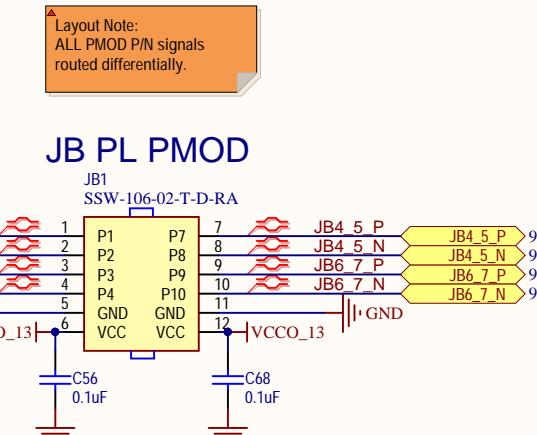
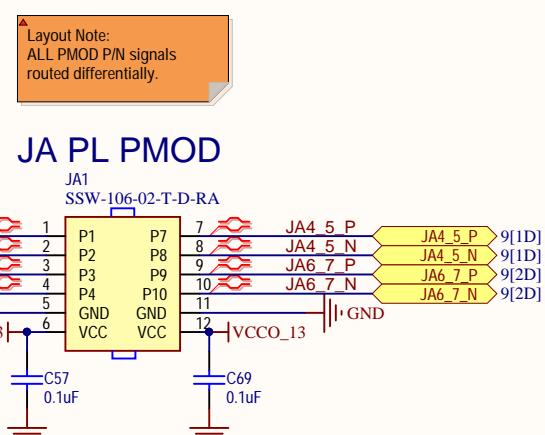
PCIe CLOCK SYNTHESIZER



Peripherals - 1

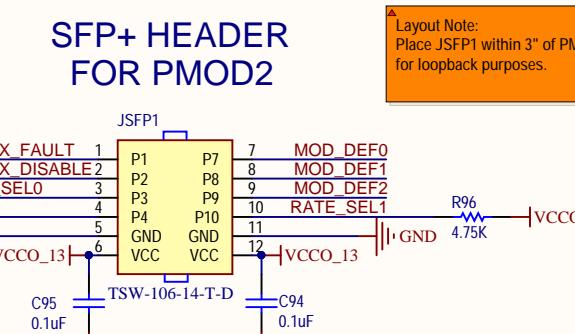
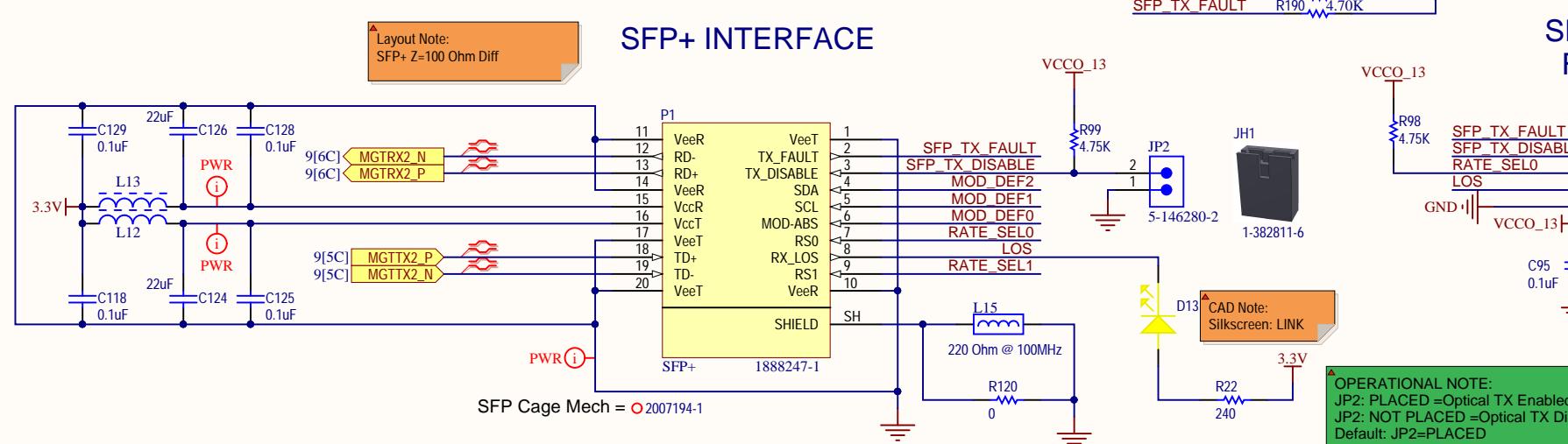


PMOD INTERFACES



JA & JB Available on 7015/20/30 ONLY

JZ Available on 7015/30 ONLY



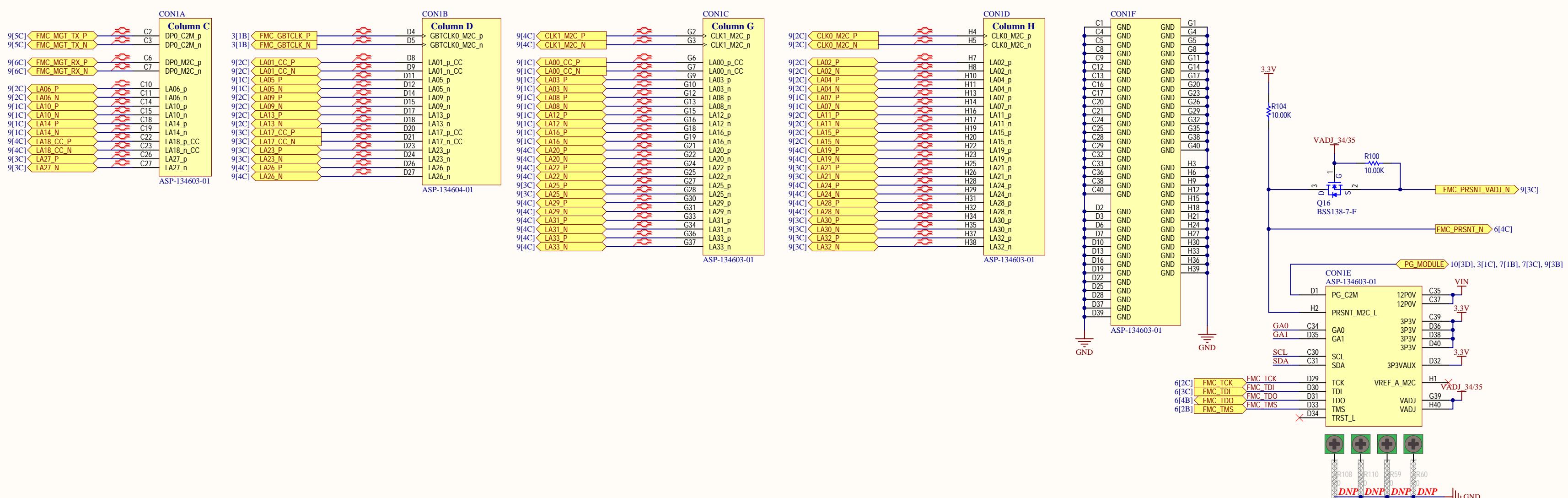
3.3V

▲

OPERATIONAL NOTE:
JP2: PLACED =Optical TX Enabled
JP2: NOT PLACED =Optical TX Disabled
Default: JP2=PLACED

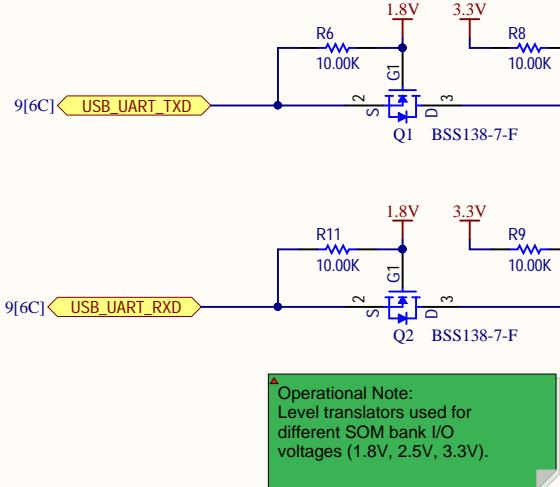
Peripherals - 2

FMC LPC INTERFACE

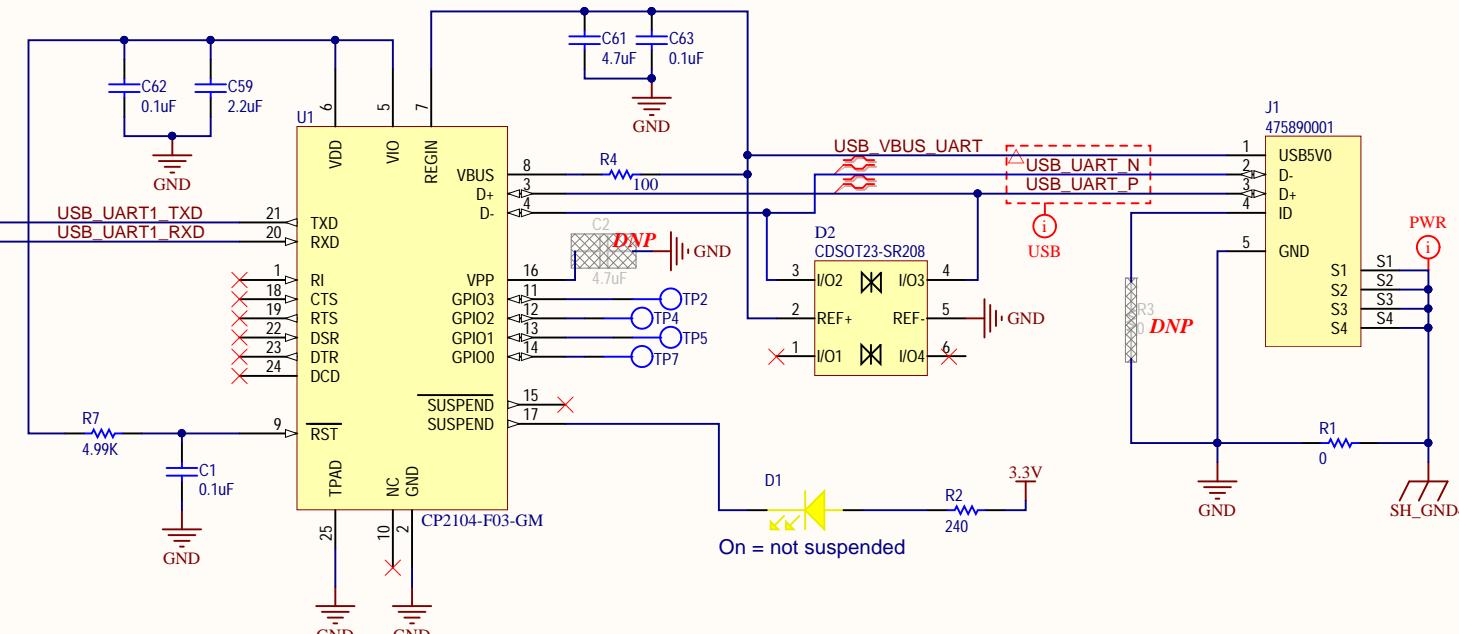


USB UART

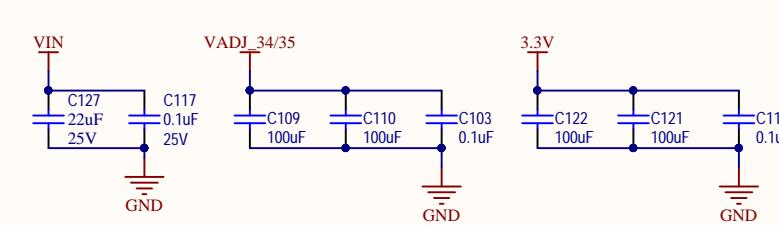
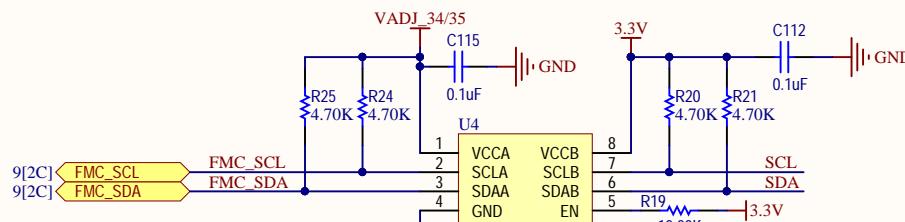
USB TX/RX LEVEL TRANSLATOR



Operational Note:
Level translators used for
different SOM bank I/O
voltages (1.8V, 2.5V, 3.3V)



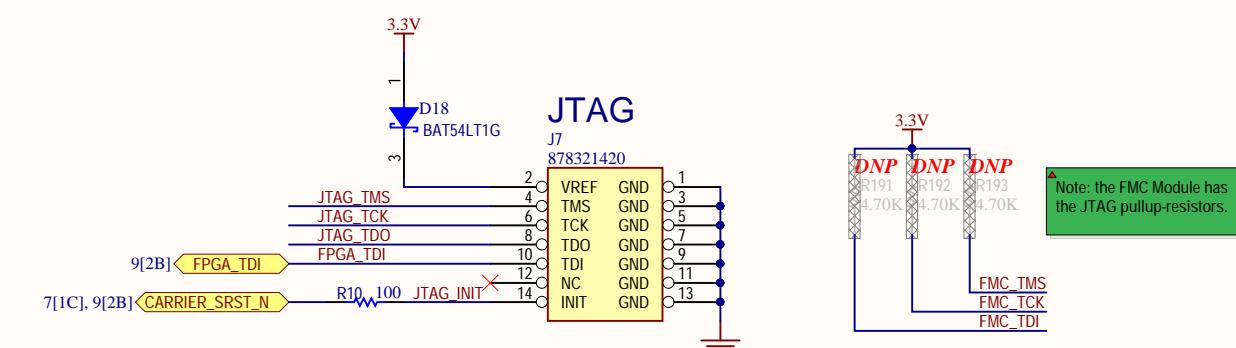
FMC I2C LEVEL TRANSLATOR



Peripherals - 3

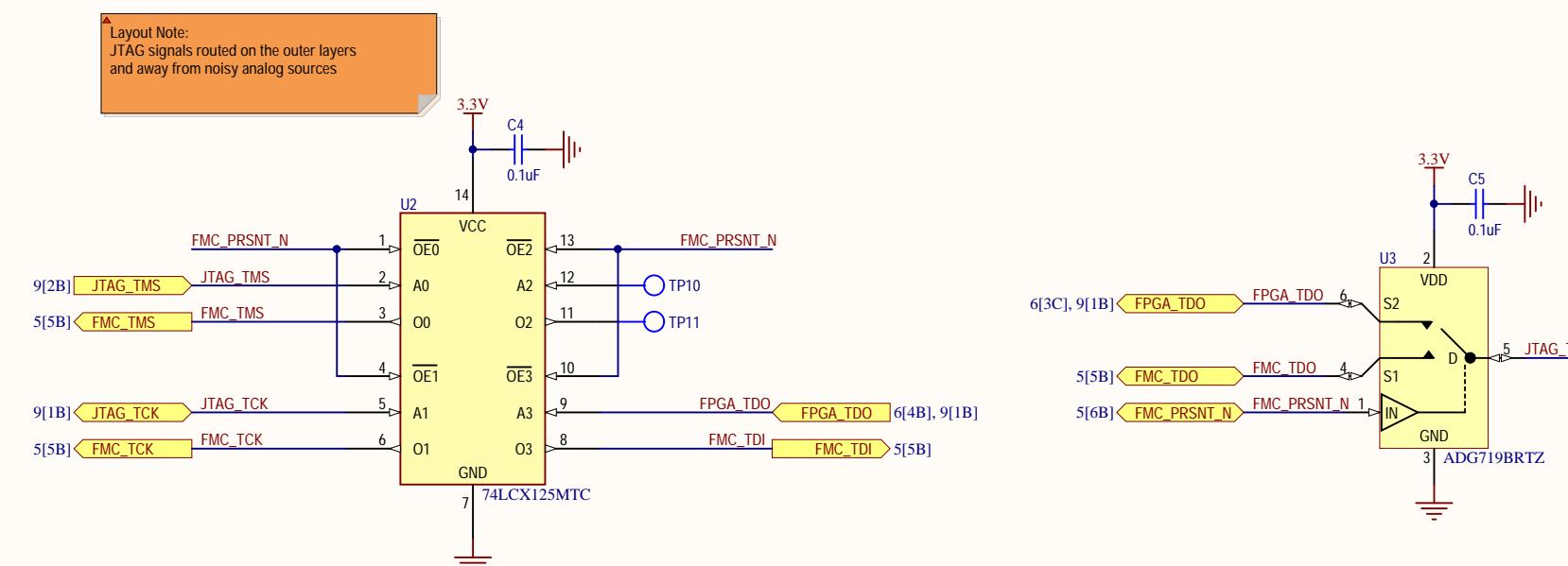
JTAG - PC4 & FMC

A



A

B



B

C

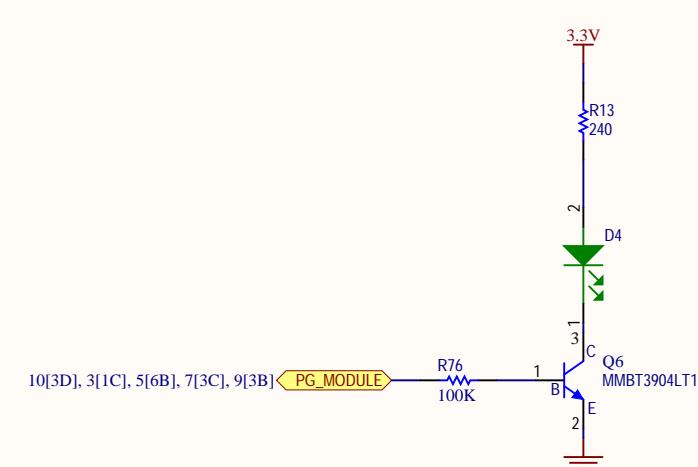
C

D

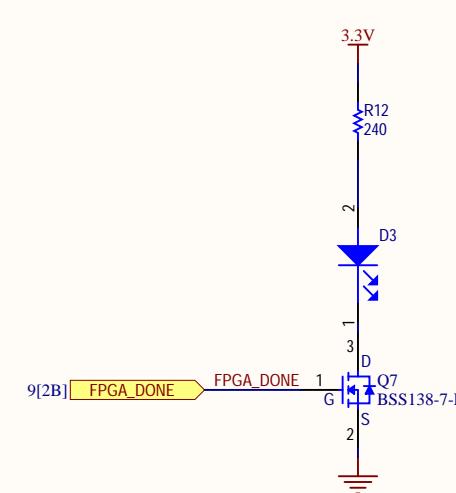
D

SWITCHES & LEDs

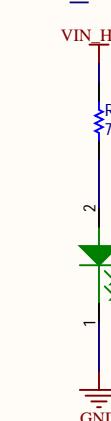
PG MODULE



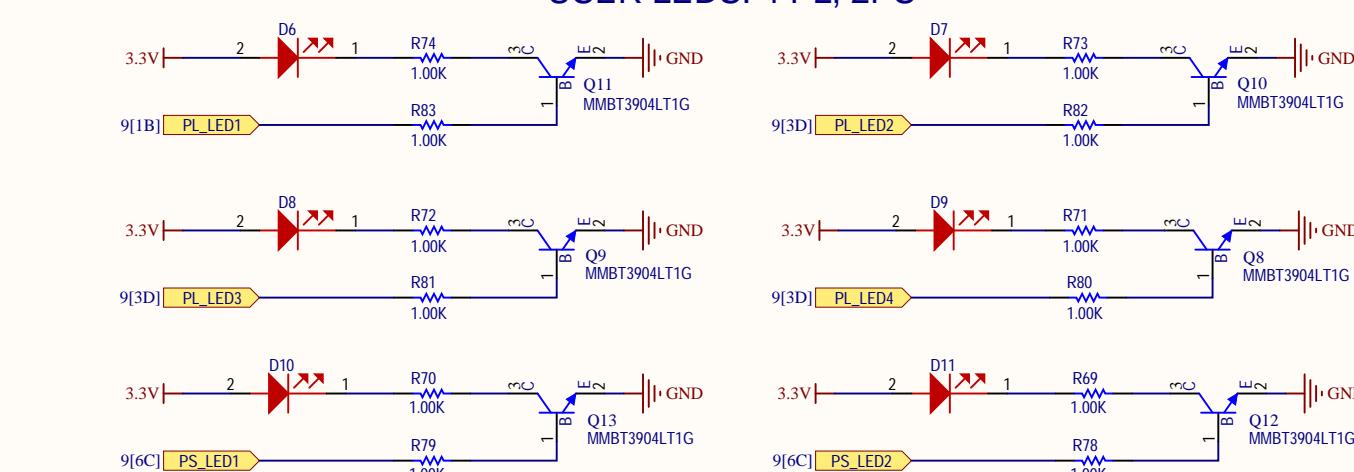
FPGA DONE LED



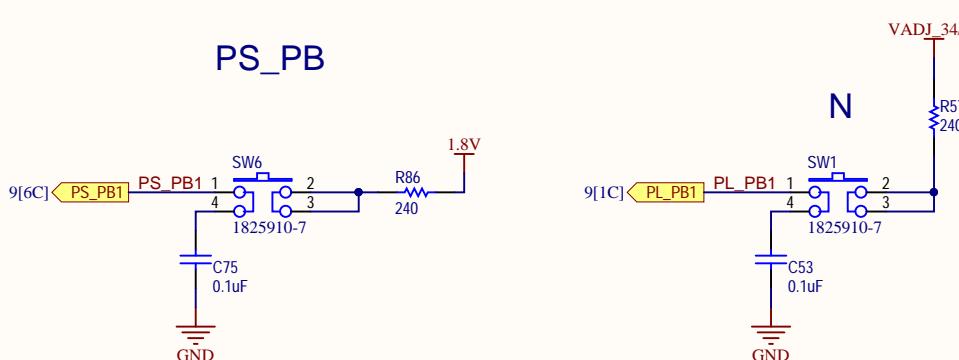
VIN_HDR_LED



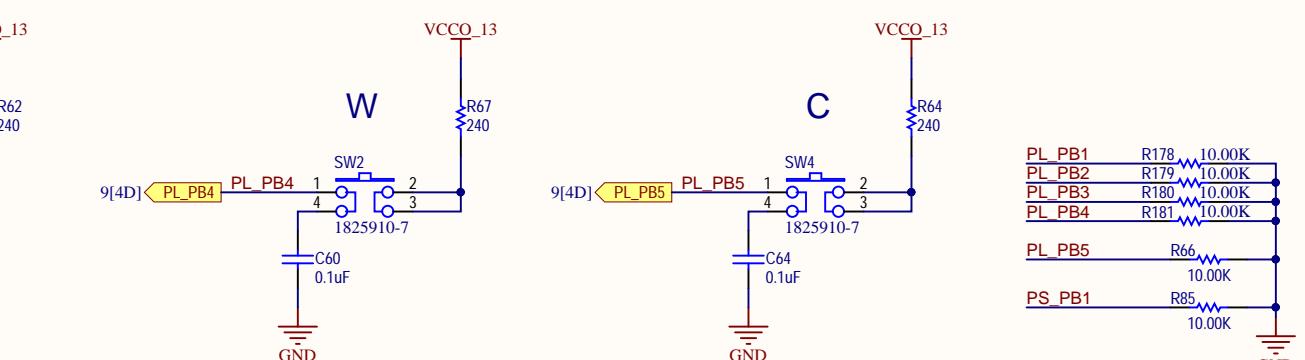
USER LEDS: 4 PL, 2PS



PS_PB

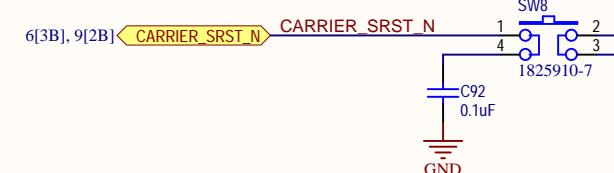


USER PUSH BUTTONS

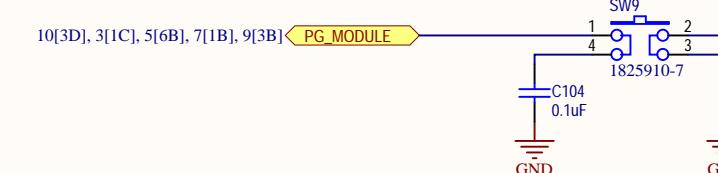


CONFIGURATION & CONTROL SWITCHES/HEADER

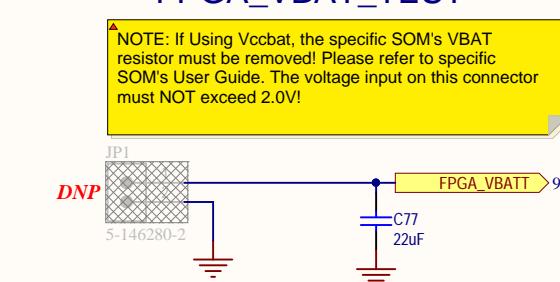
CARRIER_RST_N



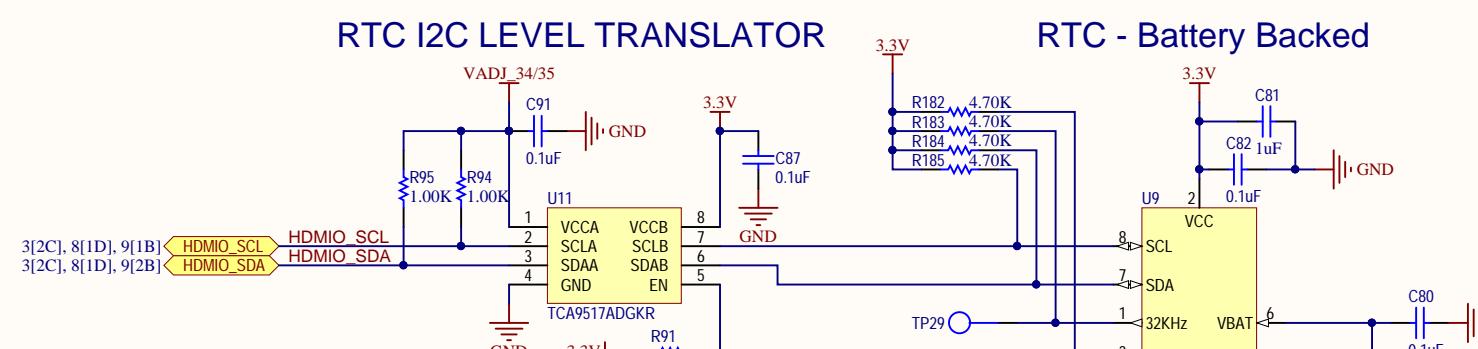
PG_MODULE_N



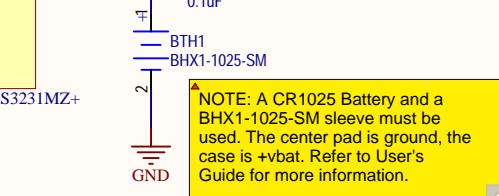
FPGA_VBAT_TEST



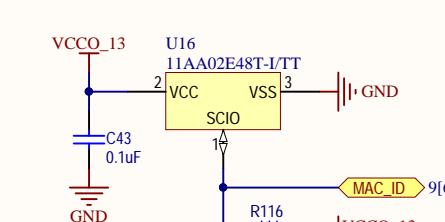
RTC I2C LEVEL TRANSLATOR



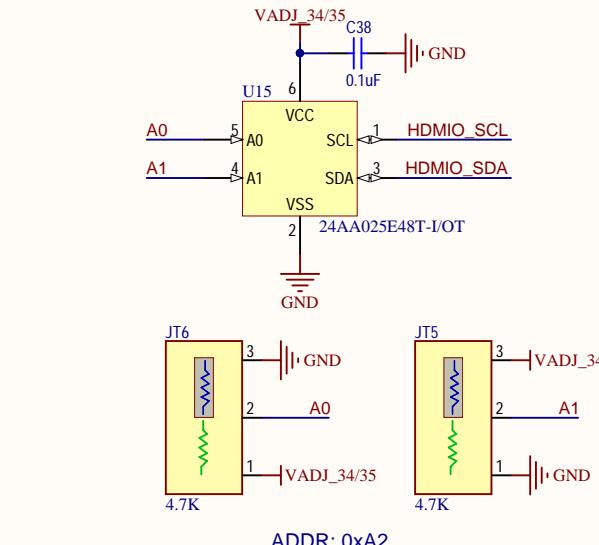
RTC - Battery Backed



MAC ID - UNI/O EEPROM



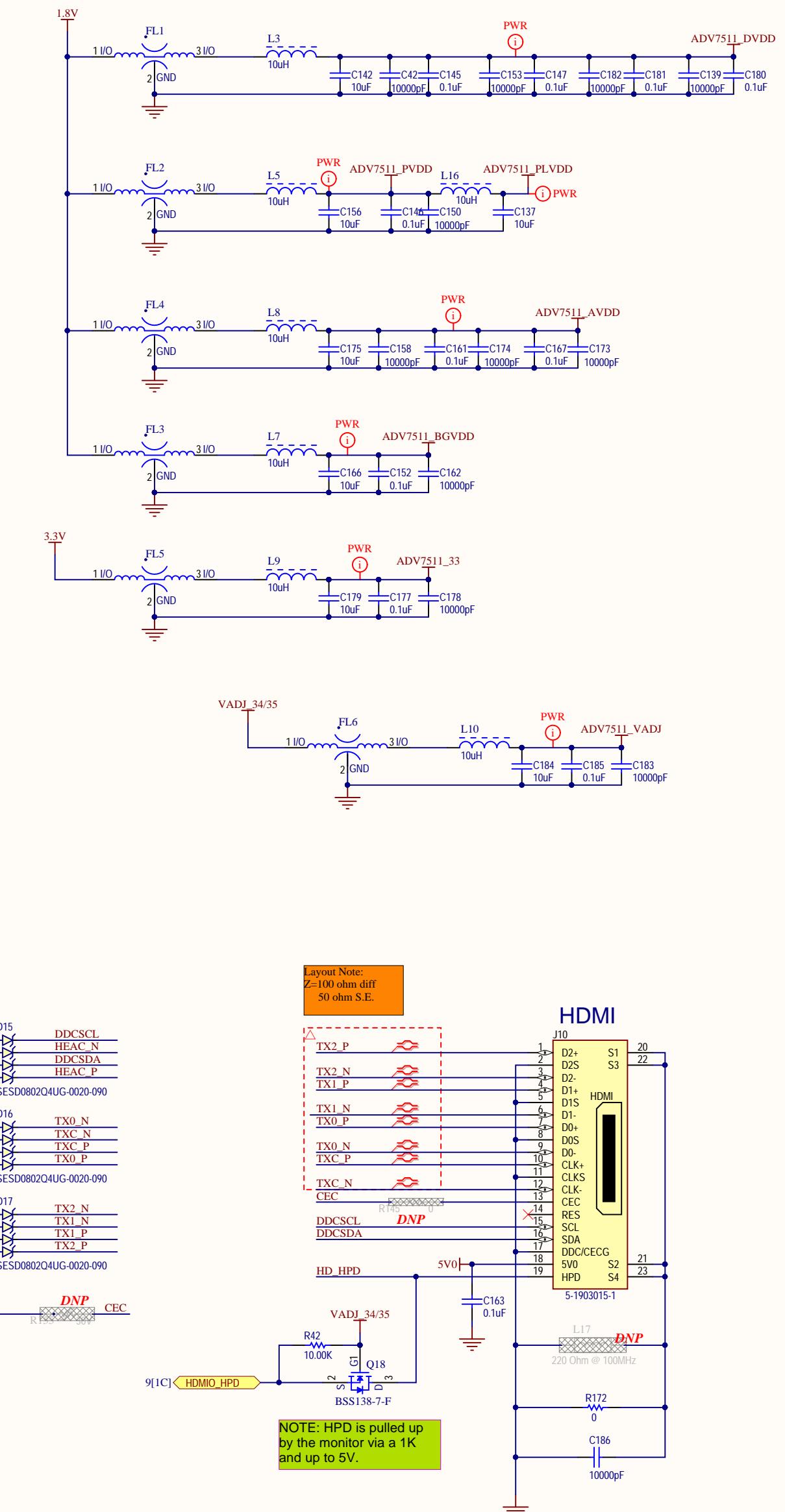
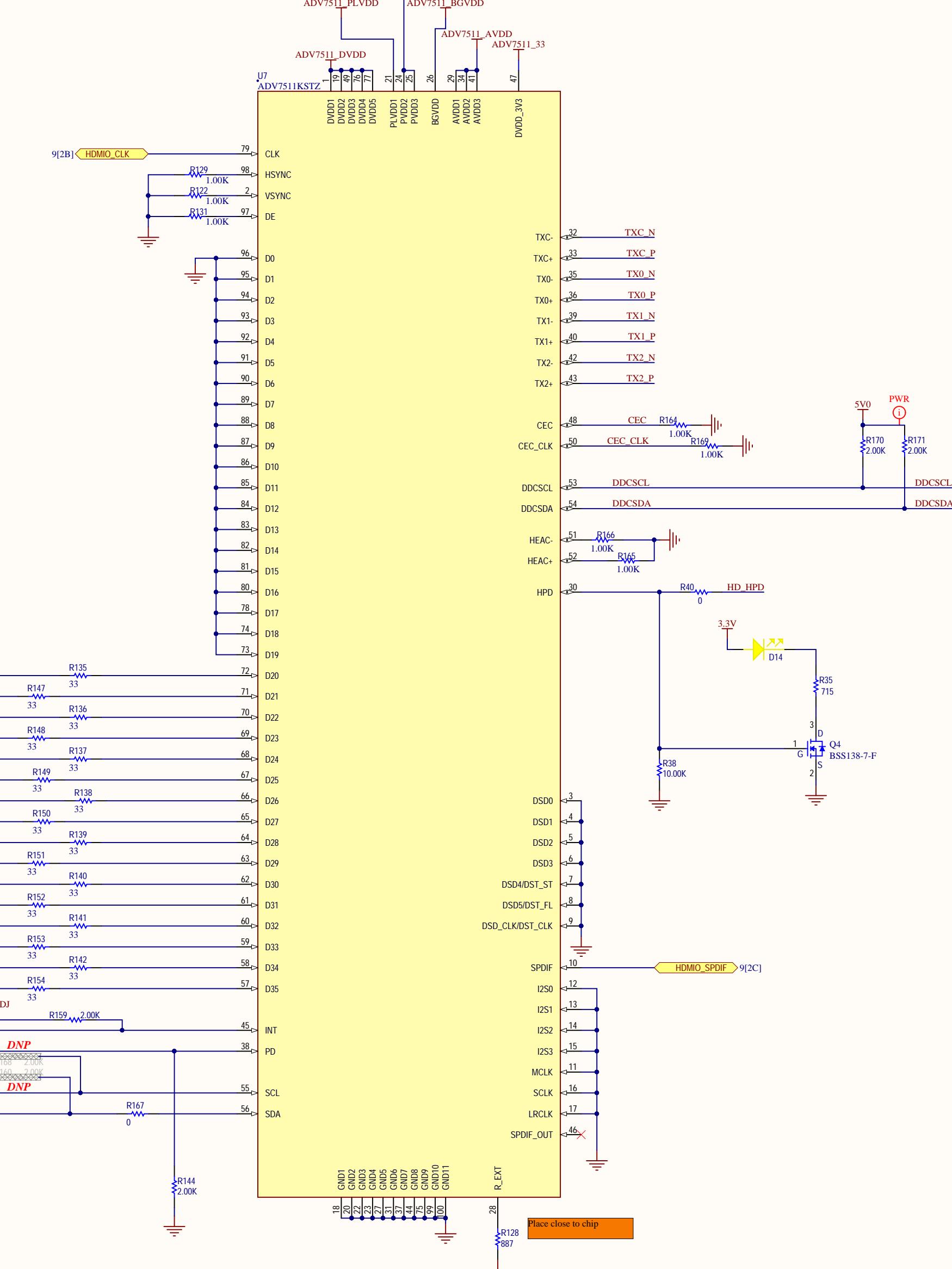
MAC ID - EEPROM



Avnet Engineering Services			
Title:	Document Number:	Variant Description:	Rev:
C	PZCC-FMC-Gen2	Build 1 Variant	1
Date: 3/15/2016		Sheet 7 of 11	

JX2 - HDMI Video Encoder

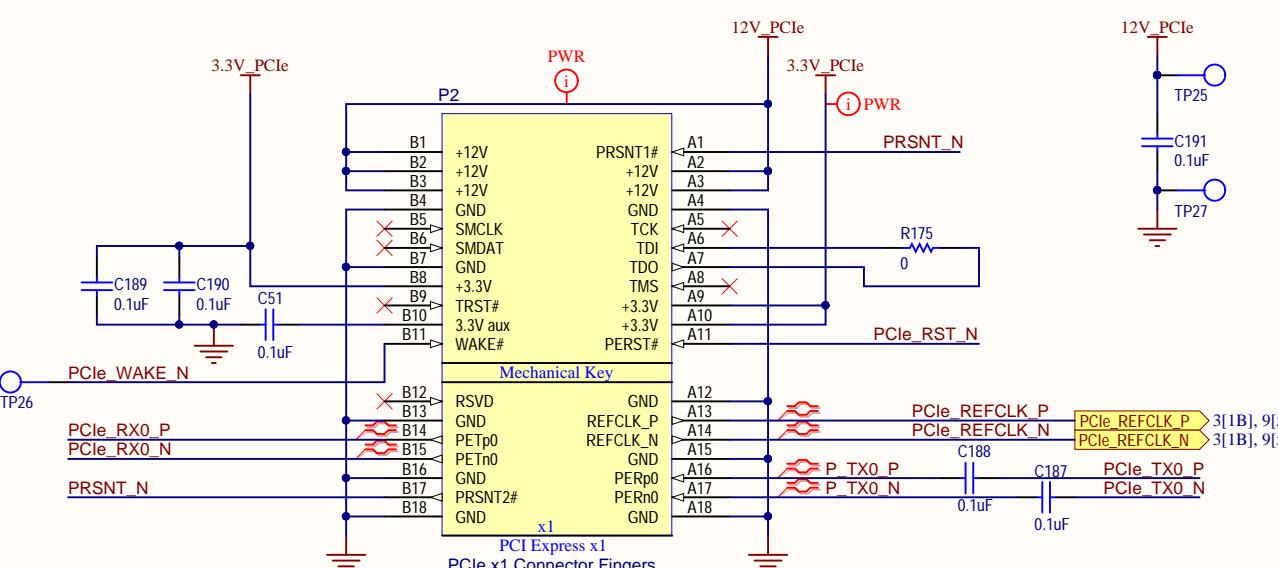
ADV7511 POWER SUPPLY FILTERS



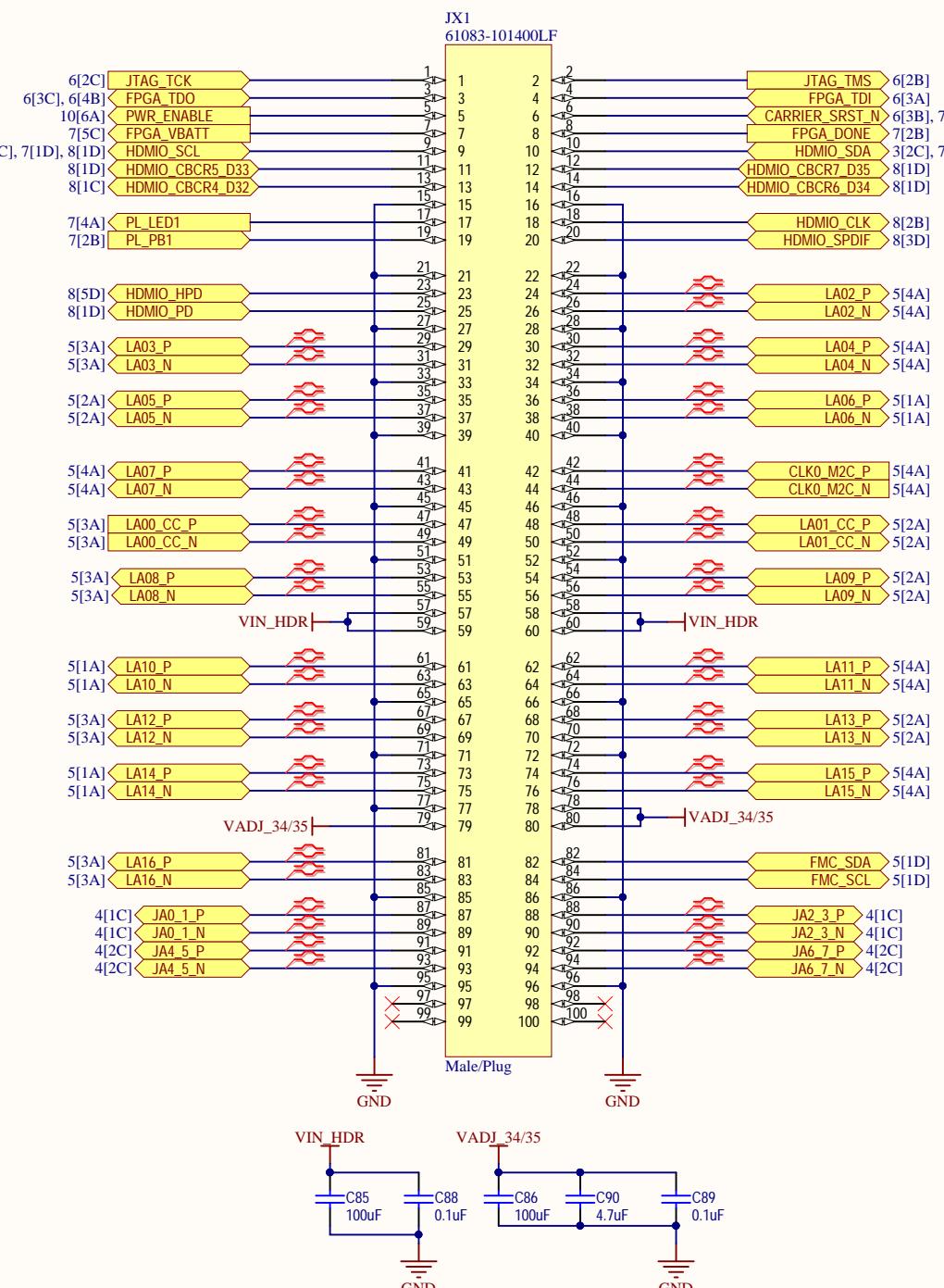
 AVNET electronics marketing	Avnet Engineering Services		
Title: 08 - HDMI Interface			
Size: C	Document Number: PZCC-FMC-Gen2	Variant Description: Build 1 Variant	Rev: 1
Date: 3/15/2016		Sheet 8	of 11

PCIe & SOM Connectors

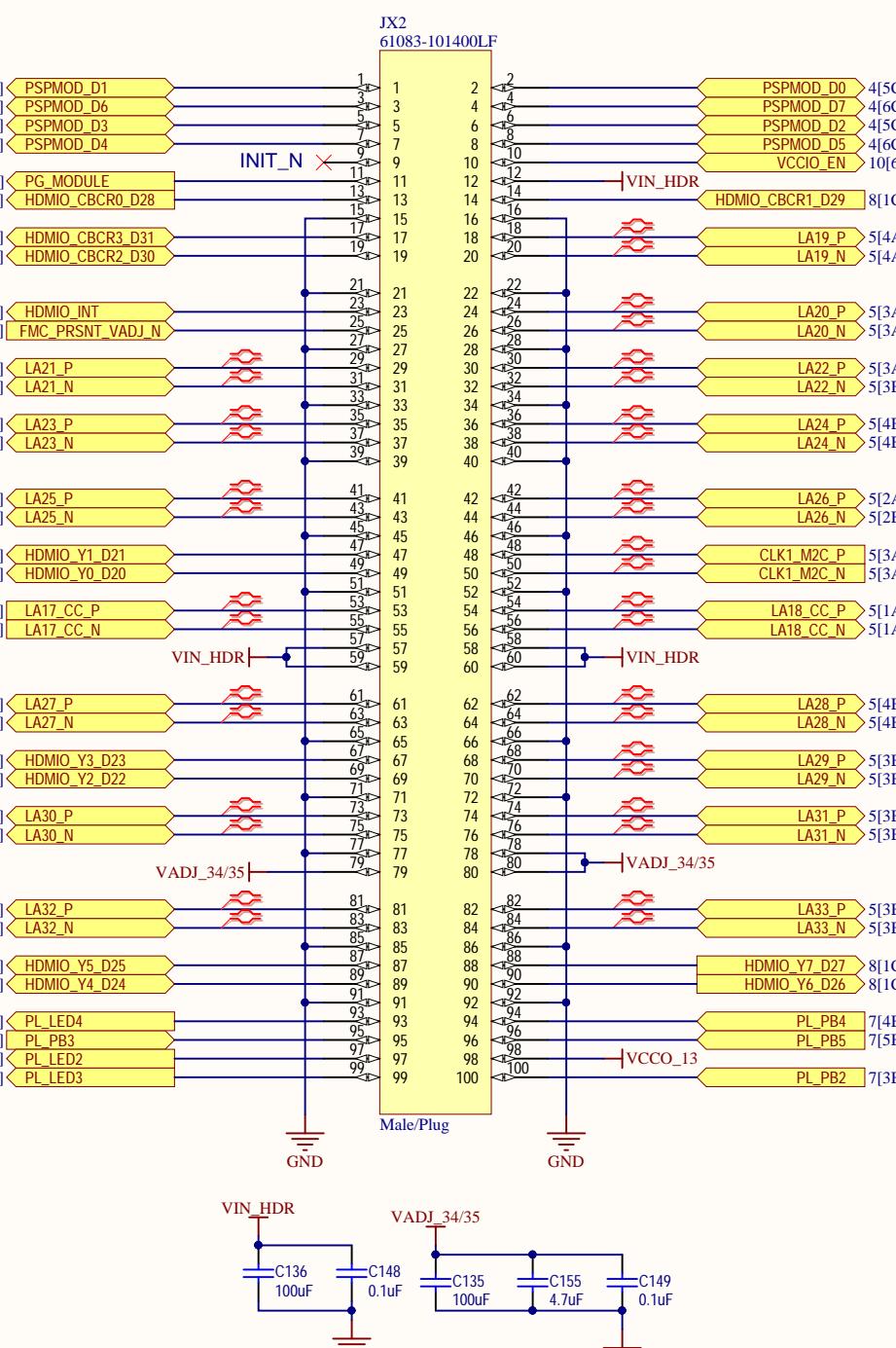
PCIe x 1 EDGE CONNECTOR



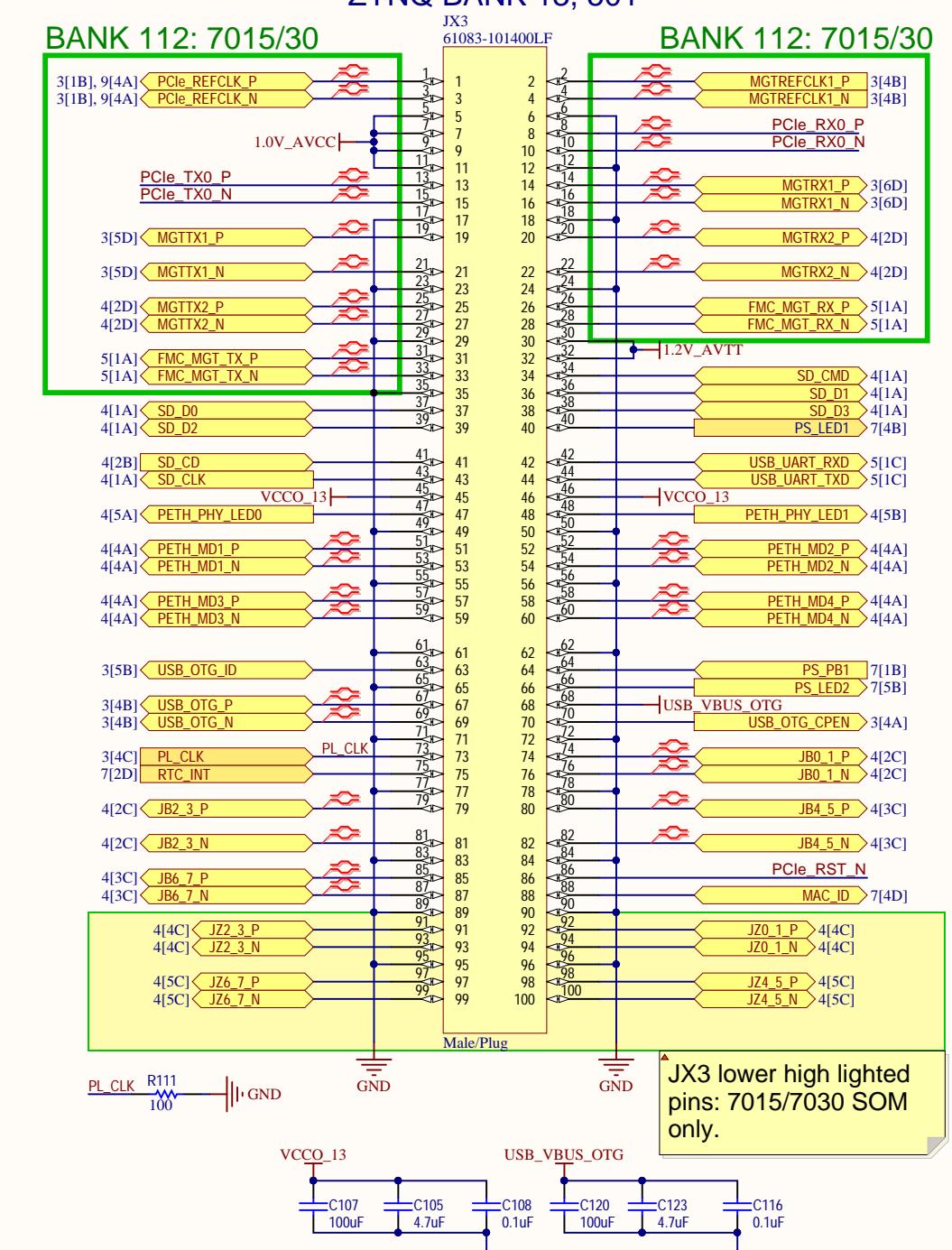
ZYNQ BANK 34/35

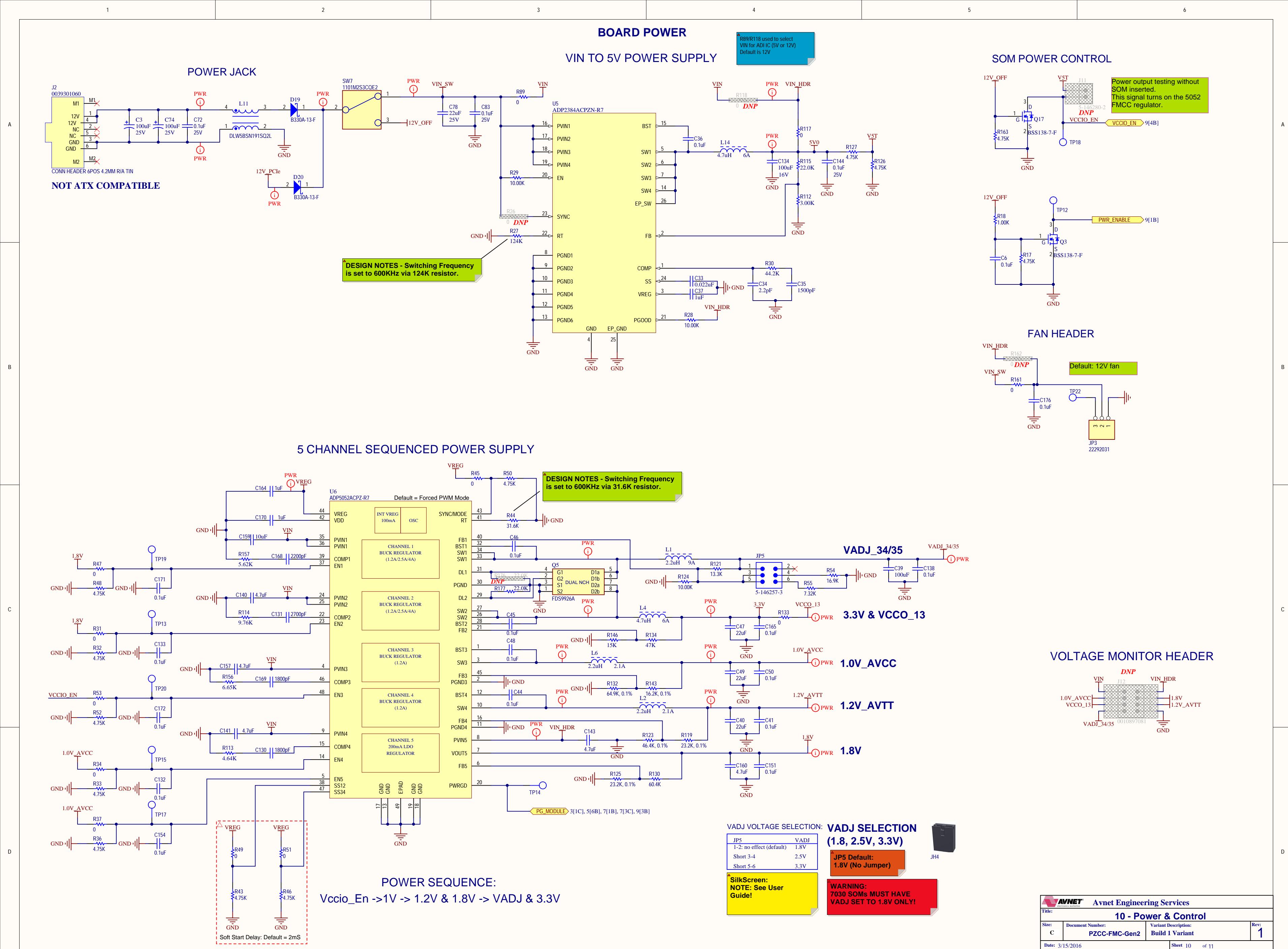


ZYNQ BANK 34/35



ZYNQ BANK 13, 501

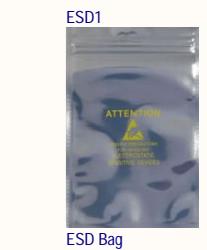




Revision Notes:**Assembly:**

Label1
BD-XXXX-XXXXX-G
Label, Product

Label2
 XXXXXXXX
Label, Serial Number

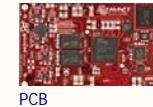
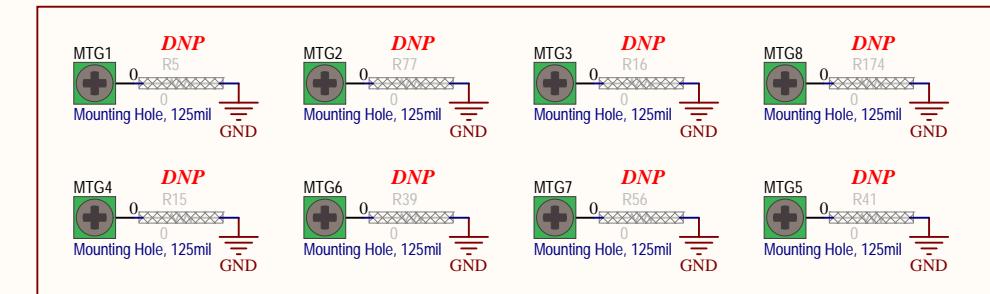
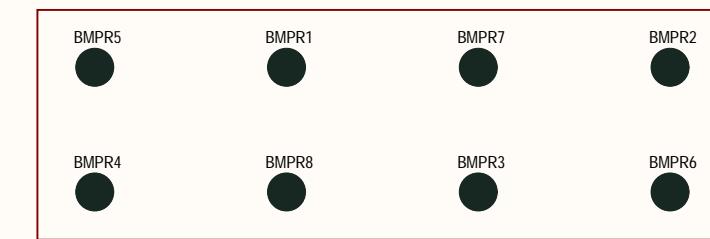


Label_ESD1

ATTENTION
TO HANDLE WITH CARE
Label, ESD

Label_NATX

ATX
Label, Non-ATX

Mechanicals:**PCB Mounting Holes****Bumper Standoffs****GROUND TEST POINTS**