## IIIT Vadodara-International Campus Diu (IIITV-ICD)

Satellite Campus of IIIT Vadodara Education Hub, Kevdi, Diu PIN:362520

Subject: Digital Logic Design Lab Branch: CSE Course Code:EC261 Semester: III

## 2. Perform the following

## (a) Lab work:

- (i) Make the different basic logic gates using the universal gate.
- (ii) Make half adder and subtractor using universal gate.

## (b) Home work

Make a graphical user interface for the truth table of 2/3 input all types of logic gate.