

# IIIT Vadodara-International Campus Diu (IIITV-ICD)

Satellite Campus of IIIT Vadodara

Education Hub, Kevdi, Diu PIN:362520

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Subject: Digital Logic Design Lab	Branch: CSE
Course Code:EC261	Semester: III

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4 Design the following using logic gates

- (a) 2-bit magnitude comparator
- (b)  $4 \times 1$  multiplexer using  $2 \times 1$  multiplexer
- (c)  $4 \times 2$  encoder and  $2 \times 4$  decoder