

IIIT Vadodara-International Campus Diu (IIITV-ICD)

Satellite Campus of IIIT Vadodara
Education Hub, Kevdi, Diu PIN:362520

Subject: Digital Logic Design Lab	Branch: CSE
Course Code:EC261	Semester: III

1. Perform the following

(a) **Lab Work:**

Basic familiarity with logic gate. Verify the truth table of given 74-series IC.

- (i) AND
- (ii) NAND
- (iii) NOT
- (iv) NOR
- (v) OR
- (vi) Xor

(b) **Home Work:**

Make a graphical user interface for number system converter.

- (i) Decimal to binary
- (ii) Binary to decimal
- (iii) Decimal to Hexadecimal vice versa
- (iv) Binary to octal vice versa