

**R.V. COLLEGE OF ENGINEERING  
BENGALURU-560059  
(Autonomous Institution Affiliated to VTU, Belgaum)**



**IMPLEMENTATION OF 2 PASS ASSEMBLER FOR  
8086 MICROPROCESSOR**

**PROJECT REPORT**

*Submitted by*

**ROHIT KUMAR                                      1RV13IS064  
PRABHJEET SINGH CHAWLA      1RV13IS065**

**Under the Guidance of**

Prof. Nagaraj G Cholli  
Assistant Professor,  
ISE, RVCE

Prof. Vanishree K  
Assistant Professor,  
ISE, RVCE

*in partial fulfilment for completion  
of  
System Software Laboratory  
Bachelor of Engineering  
in  
Department of Information Science & Engineering*

**R.V. COLLEGE OF ENGINEERING**  
(Autonomous Institution affiliated to VTU)  
**DEPARTMENT OF INFORMATION SCIENCE AND ENGINEERING**  
Mysore Road, R.V.Vidyaniketan Post, Bengaluru - 560059



**CERTIFICATE**

Certified that the project work titled '**IMPLEMENTATION OF 2 PASS ASSEMBLER FOR 8086 MICROPROCESSOR**' is carried out by **Rohit Kumar(1RV13IS064)** and **Prabhjeet Singh Chawla(1RV13IS065)**, who are bonafide students of R.V College of Engineering, Bengaluru, in partial fulfilment for the completion of **System Software Laboratory (12IS52)**, the requirement for the award of degree of **Bachelor of Engineering** in **Department of Information Science & Engineering** of the Visvesvaraya Technological University, Belgaum during the year **2015-16**. It is certified that all corrections/suggestions indicated for the internal Assessment have been incorporated in the report. The project report has been approved as it satisfies the academic requirements in respect of project work prescribed by the institution for the said degree.

**Prof. Nagaraj G Cholli**  
Assistant Professor,  
Department of ISE

**Prof. Vanishree K**  
Assistant Professor,  
Department of ISE

**Dr. N. K. Cauvery**  
Head of Department,  
Department of ISE

**Name of the Examiners**

**Signature with Date**

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2. \_\_\_\_\_

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**R.V. COLLEGE OF ENGINEERING,  
DEPARTMENT OF INFORMATION SCIENCE AND ENGINEERING  
Mysore Road, R.V.Vidyaniketan Post, Bengaluru - 560059**

## **DECLARATION**

I, **Rohit Kumar**, student of fifth semester, B.E., in the Department of Information Science and Engineering, R.V. College of Engineering, Bengaluru declare that the project entitled “**Implementation of 2 pass assembler for 8086 microprocessor**” has been carried out by us and submitted in partial fulfillment of the course requirements for System Software (**12IS52**) during the academic year **2015 -2016**. The matter embodied in this report has not been submitted to any other university or institution for the award of any other degree or diploma.

**Place : Bengaluru**  
**Date : 09/11/2015**

*Signature of the student*

**Rohit Kumar**  
USN: 1RV13IS064

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