ANNEXURE- A

SECOND YEAR ELECTRONICS AND TELECOMMUNICATION ENGINEERING PROGRAM
SCHEME OF INSTRUCTION AND EXAMINATION, REVISED COURSE (2019-2020)

SEMESTER – III

Course Code	Nomenclature of the Course	Scheme of Instruction Hrs/Week			Scheme of Examination						Credits
		L	T	P#	Duration (Hrs)	Marks					
						Th	IA	TW*	P	Total	
ET310	Mathematics- III	3	1	--	3	100	25	25	--	150	4
ET320	Circuit Analysis and Synthesis	3	--	--	3	100	25	--	--	125	3
ET330	Electronic Devices and Circuits	3	1	--	3	100	25	25	--	150	4
ET340	Digital System Design	3	1	--	3	100	25	25	--	150	4
ET350	Electromagnetic Field & Wave Theory	3	1	--	3	100	25	25	--	150	4
ET360	Electronic Devices and Circuits Lab	--	--	2	--	--	--	25	25	50	1
ET370	Digital System Design Lab	--	--	2	--	--	--	25	25	50	1
HM001	Technical Communication	2	--	--	--	--	--	75	--	75	2
AC390	Mathematics-I and II(Bridge Course*)	--	--	--	--						
	TOTAL	17	4	4	--	500	125	225	50	900	23

L-Lecture T-Tutorial P-Practical   Th-Theory TW-Term Work   IA-Internal Assessment

A candidate is considered to have sucessfully fulfilled the requirement of a semester, provided he/ she submits to the department a certified journal reporting the experiments conducted during the semester.

*Term Work marks are to be awarded through continuous evaluation

SECOND YEAR ELECTRONICS AND TELECOMMUNICATION ENGINEERING PROGRAM
SCHEME OF INSTRUCTION AND EXAMINATION, REVISED COURSE (2019-2020)

SEMESTER – IV

Course Code	Nomenclature of the Course	Scheme of Instruction Hrs/Week			Scheme of Examination						Credits
		L	T	P#	Duration (Hrs)	Marks					
						Th	IA	TW*	P	Total	
ET410	Signals and Systems	3	1	--	3	100	25	25	--	150	4
ET420	Microprocessors and Interfacing	4	--	--	3	100	25	--	--	125	4
ET430	Linear Integrated Circuits	4	--	--	3	100	25	--	--	125	4
ET440	Transmission Lines and Antennas	3	--	--	3	100	25	--	--	125	3
ET450	Statistical Communication Theory	3	1	--	3	100	25	25	--	150	4
ET460	Microprocessors and Interfacing Lab	--	--	2	--	--	--	25	50	75	1
ET470	Linear Integrated Circuits Lab	--	--	2	--	--	--	25	50	75	1
HM008	Engineering Economics and Management	3	--	--	3	100	25	--	--	125	3
	TOTAL	20	2	4	--	600	150	100	100	950	24

L-Lecture T-Tutorial P-Practical Th-Theory TW-Term Work IA-Internal Assessment

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THIRD YEAR ELECTRONICS AND TELECOMMUNICATION ENGINEERING PROGRAM
SCHEME OF INSTRUCTION AND EXAMINATION, REVISED COURSE (2019-2020)
SEMESTER – V

Course Code	Nomenclature of the Course	Scheme of Instruction Hrs/Week			Scheme of Examination							Credits
		L	T	P#	Duration (Hrs)	Marks						
						Th	IA	TW*	O	Total		
ET510	Analog and Digital Communication	3	1	--	3	100	25	25	--	150		4
ET520	Digital Signal Processing	3	1	--	3	100	25	25	--	150		4
ET531	Embedded Systems	3	--	--	3	100	25	--	--	125		3
ET532	Power Electronics											
ET533	Soft Computing											
ET534	Numerical Methods and Approximations											
ET535	Solid State Devices and Technology											
ET541	Microwave Engineering	3	--	--	3	100	25	--	--	125		3
ET542	Electromagnetic Compatibility Engineering											
ET543	Digital Image Processing											
ET544	Electronic Instrumentation and Automation											
ET545	Information Theory and Coding											
ET550	Communication Engineering Lab	--	--	2	--	--	--	25	25	50		1
ET560	Electronic Measurement Lab	--	--	2	--	--	--	25	25	50		1
**	**Open Elective**	3	--	--	3	100	25	--	--	125		3
HM009	Ethics and Entrepreneurship	3	--	--	3	100	25	--	--	125		3
	TOTAL	18	2	4	--	600	150	100	50	900		22

L-Lecture T-Tutorial P-Practical O-Oral Th-Theory TW-Term Work IA-Internal Assessment

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*Term Work marks are to be awarded through continuous evaluation

THIRD YEAR ELECTRONICS AND TELECOMMUNICATION ENGINEERING PROGRAM
SCHEME OF INSTRUCTION AND EXAMINATION, REVISED COURSE (2019-2020)

SEMESTER – VI

Course Code	Nomenclature of the Course	Scheme of Instruction Hrs/Week			Scheme of Examination						Credits
		L	T	P#	Duration (Hrs)	Th	IA	TW*	O	Total	
ET610	Control System Engineering	3	1	--	3	100	25	25	--	150	4
ET620	VLSI Technology and Design	3	1	--	3	100	25	25	--	150	4
ET631	Real Time Operating Systems	3	--	--	3	100	25	--	--	125	3
ET632	Radar System Engineering										
ET633	Artificial Neural Networks										
ET634	Nanoelectronics										
ET635	Wireless Sensor Networks										
ET641	Motor Control and Applications	3	--	--	3	100	25	--	--	125	3
ET642	Adaptive Signal Processing										
ET643	Bio-medical Electronics and Instrumentation										
ET644	Mobile Communication										
ET645	Error Control Coding										
ET650	VLSI Lab	--	--	2	--	--	--	25	25	50	1
ET660	Electronic System Design Laboratory	--	--	2	--	--	--	25	25	50	1
**	Open Elective	3	--	--	3	100	25	--	--	125	3
HM006	Cyber Law and IPR	3	--	--	3	100	25	--	--	125	3
	TOTAL	18	2	4	--	600	150	100	50	900	22

L-Lecture T-Tutorial P-Practical O-Oral Th-Theory TW-Term Work IA-Internal Assessment

A candidate is considered to have successfully fulfilled the requirement of a semester, provided he/ she submits to the department a certified journal reporting the experiments conducted during the semester.

*Term Work marks are to be awarded through continuous evaluation

FOURTH YEAR ELECTRONICS AND TELECOMMUNICATION ENGINEERING PROGRAM
SCHEME OF INSTRUCTION AND EXAMINATION, REVISED COURSE (2019-2020)

SEMESTER – VII

Course Code	Nomenclature of the Course	Scheme of Instruction Hrs/Week			Scheme of Examination						Credits
		L	T	P#	Duration (Hrs)	Th	IA	TW*	O	Total	
ET710	Data Communication	3	1	--	3	100	25	25	--	150	4
ET721	Robotics										
ET722	Machine Learning										
ET723	Wavelets and Multirate Signal Processing	3	--	--	3	100	25	--	--	125	3
ET724	Consumer Electronics										
ET725	Hardware Description Language										
ET730	Data Communication Lab	--	--	2	--	--	--	25	25	50	1
**	Open Elective	3	--	--	3	100	25	--	--	125	3
ET740	Internship	--	--	6	3	--	--	50	50	100	3
ET750	Project Work - Phase I	--	--	6	3	--	--	50	75	125	3
	TOTAL	9	1	14	--	300	75	150	150	675	17

L-Lecture T-Tutorial P-Practical O-Oral Th-Theory TW-Term Work IA-Internal Assessment

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*Term Work marks are to be awarded through continuous evaluation

FOURTH YEAR ELECTRONICS AND TELECOMMUNICATION ENGINEERING PROGRAM
SCHEME OF INSTRUCTION AND EXAMINATION, REVISED COURSE (2019-2020)

SEMESTER – VIII

Course Code	Nomenclature of the Course	Scheme of Instruction Hrs/Week			Scheme of Examination						
		L	T	P#	Duration (Hrs)	Th	IA	TW	O	Total	Credits
ET810	Advanced Communication Engineering	3	--	--	3	100	25	--	--	125	3
ET821	Process Control Instrumentation	3	--	--	3	100	25	--	--	125	3
ET822	RF Design										
ET823	High Performance Computer Architecture										
ET824	Secure Communication										
ET825	System Verification and Validation										
ET830	Elective - NPTEL / MOOC / SWAYAM	3	--	--	--	--	--	50	50	100	3
ET840	Project Work - Phase II	--	--	18	--	--	--	200	200	400	9
	TOTAL	9	0	18	--	200	50	250	250	750	18

L-Lecture T-Tutorial P-Practical O-Oral Th-Theory TW-Term Work IA-Internal Assessment

\# A candidate is considered to have successfully fulfilled the requirement of a semester, provided he/ she submits to the department a certified project report of the work done during the semester.

*Term Work marks are to be awarded through continuous evaluation