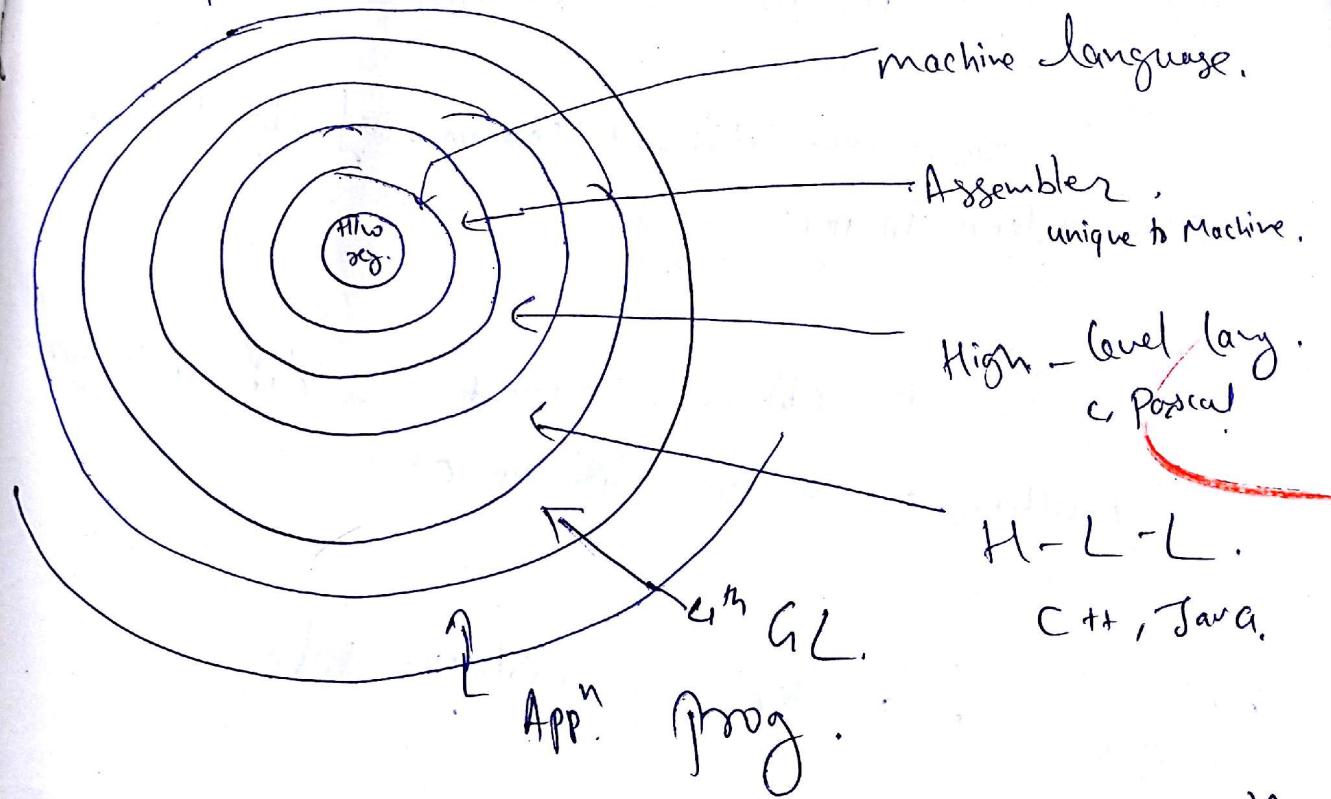


Software.

"Hw is merely a vehicle for allowing the SW to express itself."



from Hw to App<sup>n</sup>.

Combining Hw & Sw

⇒ Natural language description.

⇒ Translation of the Problem Statement into a.

More. Computer-Compatible. form.

↳ by. high-L-L. (e.g C)

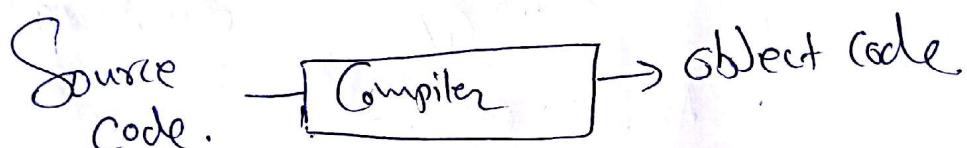
depends on Problem Constraints & performing  
the translation.

Additional levels of Translation are necessary too.

→ HLL must be translated to form which we understand → i.e. o's & 1's, i.e. object code.

→ how are different versions of target language accommodated if necessary?

→ Can the obj code execute on different target machines or run on different OS?



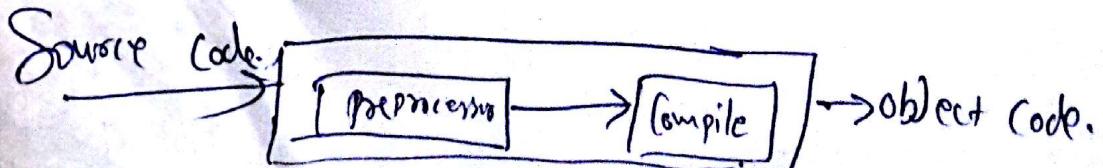
Translating from Source Code to Obj. Code.

⇒ Preprocessor: tool to help in translation process performing.

↳ Several operations to prepare Source file for Compiler.  
↳ inclusion of header file's  
↳ line control  
↳ conditional compilation.

↳ ~~just~~ Macro Processing

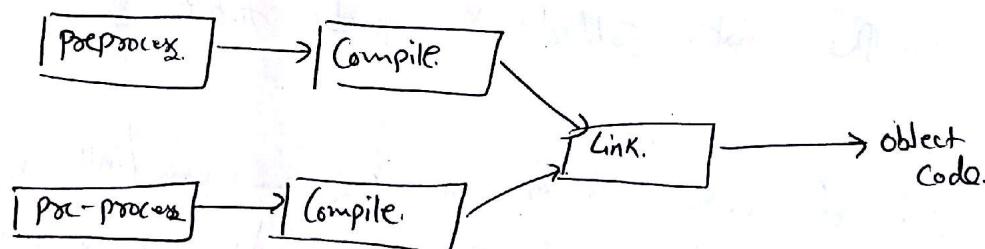
↳ Selecting Source text for compilation incorporating different shared files into one file that will ultimately be compiled.



Cross Compiler tool for translating programs into variety of forms.

⇒ a compiler that runs on one machine typically the development platform, and generates code for a different machine, typically the target machine.

Assembler ⇒ Converts the collection of Assembly language steps into machine language (i.e. 0's and 1's) object codes.



Linker and Loader ⇒ Although the program is now in machine language, it is not ready to be executed.

⇒ Prob: All variables and data structures used in the program must reside in Comp. memory and each needs address in memory.

Q. Which address should be used?

A ⇒ Assembler generates relocatable code.

Q. Is it beneficial to use existing code.

A. Yes, reduce development time & cost.

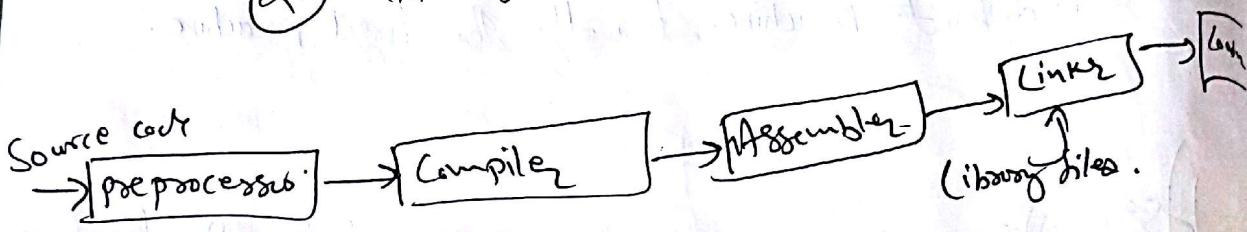
Q. How to incorporate such code into the program without modifying

A ⇒ with linker - loader

Linker/loader does.

① links a collection of prog. module together, and it ~~solves~~ ~~addr prob.~~

② it resolves add problems.



⇒ During the process of translation, the names of all variables are identified, and an entry of each is made in the table called "Symbol table".

⇒ if when linker arrives, and the linker can't find that definition, the process ends with link errors.

Linking. ⇒ involves combining all the machine code into a single file, and re-referencing all addresses to the start (addr 0.)

⇒ Shrinking: app goes to flash-type ROM.

## An Embedded C Prog

A. Prog: any set of instructions.

→ Performance (certain characteristics & attributes)  
e.g. size, speed, utility, etc.

⇒ Robust ⇒ tolerant of failure conditions, misuse  
unexpected inputs, side effects,

⇒ Ease of Change:- modularity, supports modification  
deletion, addition of new features.

⇒ Good design & Coding style.  
i.e.

clarity in Algo and control flow through  
Code, modularity, readability, proper use of indentation.

Style of coding.

Developing Embedded SW ⇒

↳ Specification & design (70-80%)

↳ Abstraction ⇒ ability to minimize or eliminate

details that are unimportant  
↳ focus on essential elements of problem

## Embedded SW

- ↳ recorders, VCR, Cellular phones.
- ↳ guided missiles, Control Satellites.
- ↳ Medical instruments.
- does not terminate.
- Consumes power.
- Takes time.

## Requirement of Em. SW

- ↳ Reliability      ↳ low power consumption
- ↳ Cost effective    ↳ efficient to memory.
- ↳ timeliness.
  - ↳ satisfy timing constraint.
  - ↳ Cache schemes,
  - ↳ speculative. Inst. execution
  - ↳ Branching.

→ Concurrency:-  
    new of variety of Sensors &  
    retain control over actuators.

→ liveness:- WDT?

→ Interfaces:- Static behaviour

→ Heterogeneity. → Different Computational Styles &  
    implementation technologies.

→ interact with  
    event occurring.  
    irregularly in time.  
    Regular in time.

→ Reactivity → React fast to env. at speed of environment.  
    Safety critical  
    Can't fail arbitrarily.

## Running a Prog.

- ↳ Run on host sys.
  - ↳ fast bus sim. 110-sec.
  - ↳ cycle-accurate sim. 100 sec.
- ↳ Run on target sys.

## Model of Programs

- ↳ Why not use the source code directly?
  - ↳ source code  $\rightarrow$  Assembly language, C-code etc.
- ↳ fundamental mode for program is:  
Control/ data flow graph (CDFG)
  - ↳ both data operation & control operations.

### Data flow graph

- ↳ model of program with no conditions.  
(one entry & exit point)  $\Rightarrow$  basic block.

$$w = a + b$$

$$x = a - c$$

$$y = a + d$$

$$z = a + c$$

$$w = y + e$$

A basic block in C.

graph LR  
a --> w  
b --> w  
c --> x1  
d --> z  
e --> y  
w --> y  
x1 --> y  
y --> z

$$w = a + b$$

$$x_1 = a - c$$

$$y = x_1 + d$$

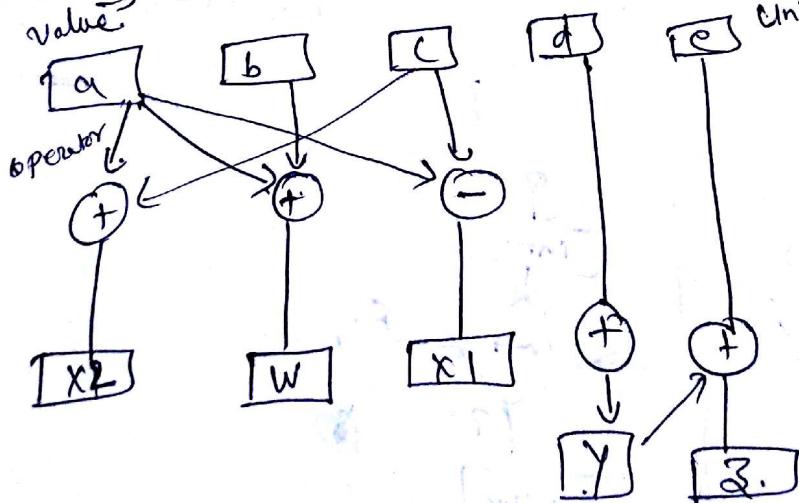
$$z = y + e$$

Sign - assignment form.

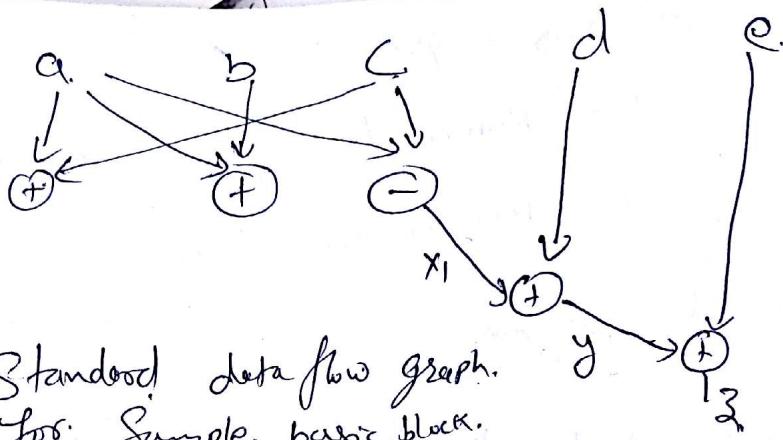
variable appears once only.

allows to identify.

unique location in code.



An extended data flow graph for one sample basic block



CDFG  $\Rightarrow$  use data flow graph as an element.

decision nodes : describe all types of Control in program.

data flow nodes :- encapsulated, complete DFG  
Cond. in loop

for ( $i = 0; i = N; i++$ ) {

loop-body();  
}

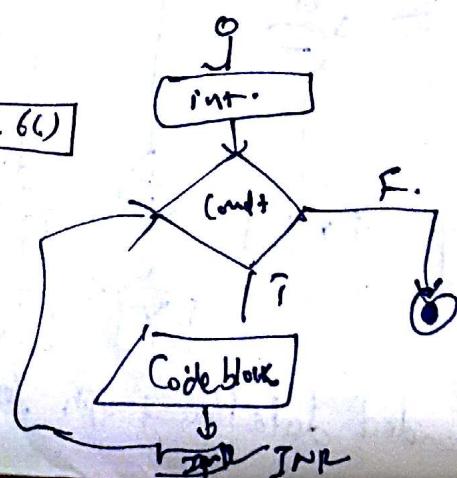
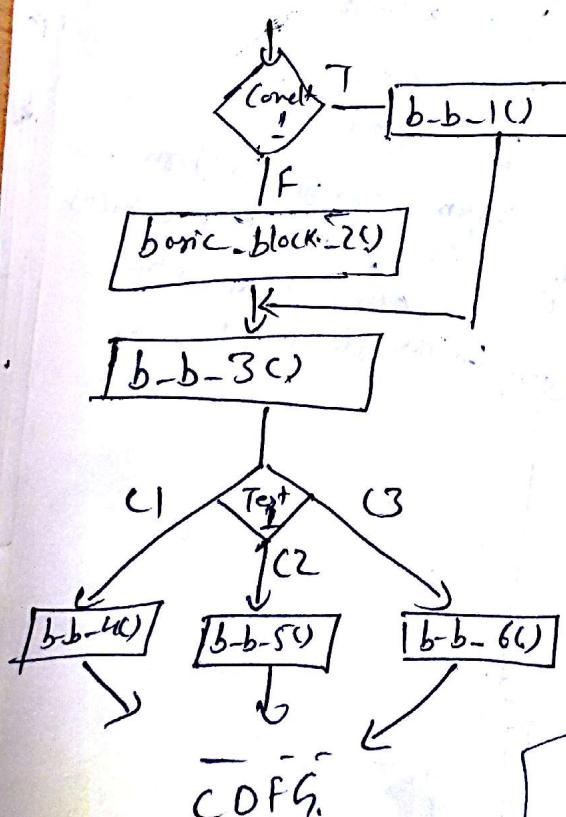
if (cond.)  
basic-block-1();

else  
basic-block-2();  
basic-block-3();

Switch (test) {

case C1: b-b-4(); break;  
case C2: b-b-5(); break;  
case C3: b-b-6(); break;

}



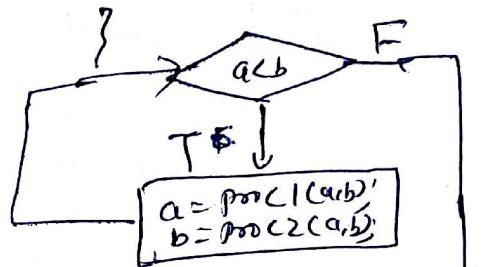
```

i = 0
while (i < n) {
    loop body();
    i++;
}

```

while ( $a < b$ ) {

$a = \text{proc1}(a, b)$ ,  
 $b = \text{proc2}(a, b)$ ,



CFG is hierarchical representation.

↳ a

CFG

Prog. lang:

→ Procedural: C.

→ OOP's

↳ reusability.

→ Java: Code mobility. Code access via  
specification implementation instance. ↳ if JVM supported by OSE  
instance. ↳ n/w application  
(to use 3rd party source's.)

### Use of C.

↳ Processor dependent Object's like  
Character's, bytes, bits and addresses can be  
handled directly.

↳ To be manipulated to control interrupt  
controllers, CPU registers etc.,

↳ Provides special variable types like  
Registers, volatile, static & constant.

### Variable Types inc.

Register → Var to be very ~~bigly~~ used, Compiler  
places in a register.

Static → Private Comm in module  
↳ Shared Globally.

Volatile → Useful for handling memory mapped I/O  
(don't map them to cache)

Compiler uses volatile keyword to avoid putting

Content in Cache. C by generating code like such as

Memory fragmentation: When memory is allocated in a large no. of  
Contiguous blocks or Chunks, leaving a good no. of  
Memory unallocated and unusable: result's out of memory,  
allocation errors!

## Dynamic memory Alloc.

`int *a = malloc(sizeof(int));`  
 (malloc)  $\hookrightarrow$  Calloc.

## Automatic memory Alloc.

$\hookrightarrow$  Stack memory.

`int a = 43;`

Static M.A  $\Rightarrow$  at Compile time

Extern  $\Rightarrow$  so I.C can have access to

G.V of. 2<sup>nd</sup> C

$\hookrightarrow$  Variable is defined elsewhere.

Static Qualifier

at link time

Size

most commonly assigned  
variable's)

size for variables

Support for alloc &

## Access These Variable.

### Dynamic. memory Allocation

$\hookrightarrow$  Alloc at run time on demand.

$\hookrightarrow$  Alloc & deallocation under program control.

(malloc)  
 Alloc for array elements.  
 (calloc)  
 free

$\hookrightarrow$  Accommodated in Heap.

$$T^* P = (T^*) \text{ malloc } (\text{Size of } T)$$

$\rightarrow$  waste  
 $\rightarrow$  internal fragmentation  
 $\rightarrow$  Alloc

in heap area.  
 $\hookrightarrow$  chunk of size 16, 32 etc  
 $\hookrightarrow$  actually acquired or released in units in which memory is divided

## Memory Management in C

### Static Allocation

- ↳ GV, variables with static qualifier
- ↳ Provided during Compile & link time
- ↳ Can increase Code Size  
→ ~~space~~

### Automatic Allocation.

- ↳ At run-time, in stack, for variables defined in every f.

- ↳ Compiler provides support for auto & static
- ↳ compiler places these variable in stack

variable Types  
can be very  
placed in a register.

Var to be very  
placed in a register.

1.10

### Stack

- Computer's memory that stores temp. variables created by f<sup>n</sup>.
- managed & optimized by CPU
- every time when "declare" a new variable, it is pushed on to the Stack. When f<sup>n</sup> exits, all variables are popped off.
- Once freed, memory of stack is available for other Stack Variables.

Very fast

don't have to explicitly de-allocate variable  
CPU manages stack memory. Not become fragmented.

local variables only

limit on stack size (as demanded)  
variable can't be resized

### Heap

- Comp. memory. that is not managed automatically managed for you. (manual) not being managed by CPU.
- free floating region (and is larger)
- To allocate memory on heap: malloc() or calloc f<sup>n</sup>.  
use free() to deallocate that memory once you don't need it... else there will be "memory leak".

Memory leak → memo on heap will still be set aside.

- C/C++ would be available to other processes.
- Global Variable
- no size restriction
- slower
- variable can be by any f<sup>n</sup> anywhere in program.

- Variable's can access globally
- no limit on memory size
- slower access (relatively)
- no guaranteed f<sup>n</sup> use of space, fragmentation possible
- manual memory management
- variable can be resized using realloc()