Smt.Banumathi.K.L
Assistant Professor
Department of E & C E
BIET, Davangere
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Work Experience: 23 years

- **Worked as a lecturer in E&CE Department of Bapuji Polytechnic Shabnur from 16/09/1993 to 26-07-2005**

- **Worked as a lecturer in E&CE Department of Bapuji Institute of engineering and Technology Davangere from Aug 2007 to Aug 2012**

- **Working as Asst. Professor in E&CE Department of Bapuji Institute of engineering and Technology, Davangere since Sept 2012.**

Educational Qualification:

- **M.Tech [VLSI Design & Embedded System] from JC College of Engineering & Technology, Mysore, VTU, 2007.**

- **B.E [Electronics & Communication] from Bapuji Institute of Engineering & Technology, Mysore University, 1993**

*Membership***:**

- **Life Member of Indian Society for Technical Education : MISTELM27490**
- **Life Member of Institute of Electronics & Telecommunication Engineers : IETE-M219025**
- **Life Member of Institute of Engineers: MIE-M/124737/3**

Core Subjects Taught:

Basic Electronics, Analog Electronic Circuits, Electronic Instrumentation , Logic Design, Field Theory , Control System, Linear IC's and Applications, Microcontrollers, Power Electronics, Microprocessors, Embedded Systems, Digital System Design using VHDL, Fundamentals of CMOS VLSI, Microelectronics Circuits, Engineering Electromagnetics, Arm Microcontroller and Embedded Systems, Digital Electronics, Digital Image Processing, Digital System Design.

Seminar/ Workshops Attended:

- **Attended "One day workshop on eSim,a first course in the IoT series for Teachers" held at BIET in IS & E Dept. on 21st Sep 2019 organised by the by the Teaching Learning**

Centre(ICT) at IIT Bombay,funded by the Pandit Madan Malaviya National Mission on Teachers and Training (PMMMNMTT),MHRD,Govt.of India.

- Attended two day faculty development program on "Lab VIEW and its Application" on 2nd and 3rd Aug 2012 organized by department of E&CE in association with ISTE, BIET chapter and Trident Tech Labs Pvt. Ltd. Bangalore.

- Attended a two day Faculty development program on "Low Power Embedded System using MSP430 Microcontroller" organized by Dept. of E&CE in association with Texas Instruments and Cranes Software International Ltd., Bangalore on 9th and 10th Feb 2012

- Attended two day workshop on " MSP 430 Microcontroller" on 12th &13th December 2011

- Attended one day workshop on "Embedded Systems Design-Principles & Practice", Faculty Development Workshop, Organized by Board for IT Education Standards (BITES) & BVBCET, Hubli on 15th Nov 2010.

- Attended three day workshop on "Digital System Design using HDL – Synthesis & Simulation using Test Benches" from 16th to 18th April 2009, at BIET, Davangere.

- Attended workshop on "Electronics in Industrial Automation" from 16th to 21st July 2007 at SJCE, Mysore.

- Attended workshop on "Research Methodologies and Latex" from 20th to 22nd December 2016 at Rajarajeshwari College of Engineering, Bengaluru.

- Attended two day workshop for Faculty on "ARM Cortex M3 Microcontroller" on 23rd & 24th Jan 2018 organized by Department of Electronics & Communication Engineering, in association with Indian Society for Technical Education (ISTE), BIET Chapter & Advanced Electronic Systems (ALS), Bangalore.

- Attended two day workshop program on "ARM Cortex M3 Micrcontroller" on 23rd and 25th January 2018 organized by department of E&CE in association with ISTE, BIET chapter and Advanced Electronic Systems Bangalore, organized.

Publications:

- "A New Method to Estimate Skew Angle in Printed and Historical Document Images",International Journal of Recent Technology and Engineering (IJRTE), July 2019.

- Presented a paper on "An Approach to Estimate Skew Angle in Printed Document Images",First International Conference on Advances in Information Technology(ICAIT-2019) organized by Comuter Science and Engineering,Adichunchanagiri Institute of Technology,Chikkamagaluru during 25th to 27th July 2019.

- Presented a paper on "A New algorithm for Skew Detection and Correction of Historical Kannada Documents in International Conference on Multimediamedia Processing, Communicastion& Information Technology MPCET 2018held at J.N.N. college ofEngineering, Shivmoga, during 8th & 9th June 2018.

- Presented a paper on "Line and word Segmentation of Kannada Handwritten Text documents using Projection Profile Technique" in the International Conference on Electrical, Electronics, Communication, Computer Technologies and Optimization Techniques (ICEECOT-2016) in association with IEEE Bangalore Section, organized by GSSS Institute of Engineering &Technology for Women, Mysuru on 9th & 10th December 2016.

- "Database Development Of Historical Documents: Skew Detection And Correction", International Journal of Advanced Technology in Engineering and Science, IJATES\Certificate, July 2014.

- Presented a paper entitled "Mat lab Implementation of Text Extraction Using Multiscale Edge Based Algorithm" at the international Conference on VLSI and Signal Processing held KSIT, Bangalore on 4th and 5th of May2012.