Sri.V S Patil
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Work Experience: 20 years

• Working in the Department of Electronics and Communication Engineering at Bapuji Institute of Engineering and Technology, Davangere since 1998.

Educational Qualification:

• Diploma in Electronics & Communication Engineering, from Bapuji Polytechnic Davangere, DTE, 1993.

Seminars/Workshops Attended:

- A Course on "VHDL Coding for Synthesis" Organized by SIMS & BIET, Davangere, from 4th Feb 2000 to 19th Feb. 2000.
- Attended AICTE-ISTE Short term Course on "Unix Operating System", at S.J.C.E., Mysore from 4th to 16th Sept. 2000.
- A course on "VLSI &VHDL", organized by SIMS, Bangalore and E&C Dept B.I.E.T, Davangere from 10th Feb to10th Mar 2001.
- A course on "Embedded Systems & Design" organized by B.I.E.T and C.I.T.L, under embedded Tech park from 3rd may 2003 to 8th June 2003 (two weeks)
- Attended one day workshop on "TANNER TOOLS PRO(A complete VLSI Design System) at BIET on16th Nov. 2006.
- Attended Faculty Development Programm, AICTE Sponsored two week short term training program on "Hands on Digital Trainings using HDL" organized by Electronics & Communication Engineering

- dept, at S.J.C.E. Mysore, from 26th March to 7th April 2007.
- Attended one day workshop on "Industrial Automation" at BIET on 4th October 2007.
- Attended one day workshop on "Embedded System & Design" at BIET on 27th April 2007.
- Attended two Day workshop on Li2-Robo-III,Oct 2010
- Attended three Day workshop on 'Digital System Design using HDL-Synthesis and Simulation using test benches', April 2009
- Attended three Day workshop on Hardware Trouble Shooting & Networking, RNSIT, Bangalore July 2010
- Attended a Two day Faculty development program on "Low Power Embedded System using MSP430 Microcontroller" organized by Dept of E&CE in association with Texas Instruments and Cranes Software International Ltd., Bangalore on 9th and 10th Feb 2012.