Sri. S.P.Umapathy
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## Work Experience: 25 years

• Working in the Department of Electronics and Communication Engineering at Bapuji Institute of Engineering and Technology, Davangere since 1994.

## Educational Qualification:

• Diploma in Electronics & Communication Engineering, from Govt. DRR Polytechnic Davangere, DTE, 1991.

## Membership:

Life Member of Indian Society for Technical Education.

## Seminars/Workshops Attended:

- A Course on "VHDL Coding for Synthesis" Organized by SIMS & BIET, Davangere, from 4<sup>th</sup> Feb 2000 to 19<sup>th</sup> Feb. 2000.
- Attended AICTE-ISTE Short term Course on "Unix Operating System", at S.J.C.E., Mysore from 4<sup>th</sup> to 16<sup>th</sup> Sept. 2000.
- A course on "VLSI &VHDL", organized by SIMS, Bangalore and E&C Dept B.I.E.T, Davangere from 10<sup>th</sup> Feb to10<sup>th</sup> Mar 2001.
- A course on "Embedded Systems & Design" organized by B.I.E.T and C.I.T.L , under embedded Tech park from  $3^{rd}$  may 2003 to  $8^{th}$  June 2003 (two weeks)
- Attended AICTE-ISTE Sponsored Short term Course on "Skill and Efficiency Improvement for Technical Staff", at S.J.C.E., Mysore from 2<sup>nd</sup> to 14<sup>th</sup> August 2004.

- Attended one day workshop on "TANNER TOOLS PRO(A complete VLSI Design System) at BIET on16th Nov. 2006.
- Attended one day workshop on "Industrial Automation" at BIET on 4<sup>th</sup> October 2007.
- Attended one day workshop on "Embedded System & Design" at BIET on 27th April 2007.
- Attended two Day workshop on Li2-Robo-III,Oct 2010
- Attended three Day workshop on 'Digital System Design using HDL-Synthesis and Simulation using test benches', April 2009
- Attended a Two day Faculty development program on "Low Power Embedded System using MSP430 Microcontroller" organized by Dept of E&CE in association with Texas Instruments and Cranes Software International Ltd., Bangalore on 9<sup>th</sup> and 10<sup>th</sup> Feb 2012