Sri. SHIVAKUMARASWAMY G M Assistant Professor, Department of E & C E BIET, Davangere

Phone: +91 8192 221461 Fax: +91-8192-223261, Mobile: +91 9590621959.

Email: shivkumar_gm@rediffmail.com



Work Experience: 12 years

• Working in the Department of Electronics and Communication Engineering at Bapuji Institute of Engineering and Technology, Davangere since 02-08-2004.

Educational Qualification:

- M.Tech [Electronics] from Sir MVIT College of Engineering & Technology, Bangalore, VTU, 2005, first class
- B.E [Electrical & Electronics] from Bapuji Institute of Engineering and Technology, Kuvempu University, Nov 2000 with first class.

Membership:

- Life Member of Indian Society for Technical Education: LM53857
- Life Member of Institute of Electronics & Telecommunication Engineers: M-219040

Core Subjects Taught:

Basic Electronics, Computer Concepts and C Programming, Analog Electronic Circuit, Signals & Systems, Network Analysis, Logic Design, Control Systems, Operating System, Operation and Research, Linear Integrated Circuits & Applications

Expert Lectures Delivered:

- Worked as counselor for IGNOU.
- Worked as Guest Lecture for STJIT Ranebennur.

Seminar/ Workshops Attended:

- Participated in Two day FDP on LabView & its Applications on 2nd & 3rd Aug 2012 at BIET, Davangere
- Participated in VTU VGST Faculty Development Programme on "Recent Advances In Medical Imaging Techniques" on 26th 30th Mar 2012 at GSSSIETW, Mysore.
- Participated in Two day Hands-on on FDP on Low Power Embedded Systems Using Texas Instruments MSP430 Microcontroller on9th-10th Feb2012 at BIET, Davangere
- Participated in Two day workshop on "LABVIEW & NI Hardware" on2nd - 3rd Sep-2011 at KSIT, Bangalore
- Participated in Two day workshop on "Advanced Electrical Power Systems and Artificial Intelligence Techniques" on 11th -12th Feb 2011 at BIET, Davangere
 - Participated in Two day workshop on "Artificial Neural Network & its Applications." on 17th- 18th Sep-2010 at STJIT, Ranebennur
 - Participated One day workshop on "Mathematics & its Applications In Engineering" On 18th Feb 2010 at BIET, Davangere
 - Participated in Three day workshop on "Digital System Design using HDL – Synthesis & Simulation using Test Benches" on 16th-18th Apr 2009 at BIET Davangere
 - Participated in One day workshop on "Mechatronics and Microprocessor" on 7th March 2009 at BIET, Davangere
 - Participated in Three day workshop on "Theory & Practical's On Microcontroller" on 29th 31st Jan 2009 at Nitte Meenakshi Institute of Technology, Bangalore.

- Attended a seminar on "Mathematics & Applications in Engineering", on 18th Feb 2010 at BIET, Davangere
- Workshop on Embedded System Design, organized by IIPC, E&C Dept, BIET, Davangere and Advanced Electronic Systems, Bangalore 27th April 2007.
- Workshop on "Tanner Tools. Pro- A complete VLSI design", organized by E&C Dept., BIET, Davangere and Sovereign Technologies, Bangalore on 16th Nov.2006.
- Attended a workshop on "Disaster prediction, Prevention & Mgmt" ISTE 35th Annual Convention from 15th -17th Dec 2004 at BIET, Davangere.