

Mr. Manjesh K.O

Assistant Professor

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EDUCATIONAL QUALIFICATION:

- B.E (Electronics and Communication) with first class from KIT, Visvesvaraya Technological University, 2014.
- M.Tech (VLSI DESIGN AND EMBEDDED SYSTEMS) with first class Distinction from KIT, Visvesvaraya Technological university, 2016.

CORE SUBJECTS HANDLING:

- Analog Electronic Circuits
- Basic Electronics
- Basic Electrical Engineering LAB
- Electronic Circuits LAB
- Information Theory and Coding

SEMINAR/ WORKSHOPS ATTENDED:

- “ **ADVANCE TELECOM TECHNOLOGY** ” at Regional Telecom Training Centre, Mysore , during 10-08-2015 to 30-11-2015.
- “ **NATIONAL CONFERENCE ON EMERGING TRENDS IN ENGINEERING SCIENCES AND MANAGEMENT**” 2016.
- “ **ANALOG AND DIGITAL IC DESIGN USING CADENCE TOOL** ” at KIT, Tiptur, during 05- 04-2016 and 06-04-2016.
- “ **ANALOG AND DIGITAL SYSTEM DESIGN USING CADENCE TOOL** ” at GSSSIET, Mysore, during 22- 01-2019 and 24-01-2019.
- “ **IOT SERIES FOR TEACHERS USING ESIM** ” held at Bapuji Institute Of Engineering & Technology organized by the Teaching Learning Centre ICT at IIT Bombay, on 21 September 2019.

PUBLICATIONS:

- Presented a paper on “**BIOMETRIC BASED ELECTRONIC VOTING MACHINE**” in **INTERNATIONAL JOURNAL OF ADVANCED RESEARCH IN ELECTRICAL, ELECTRONICS AND INSTRUMENTATION ENGINEERING**, VOLUME 5, ISSUE 5, May 2016