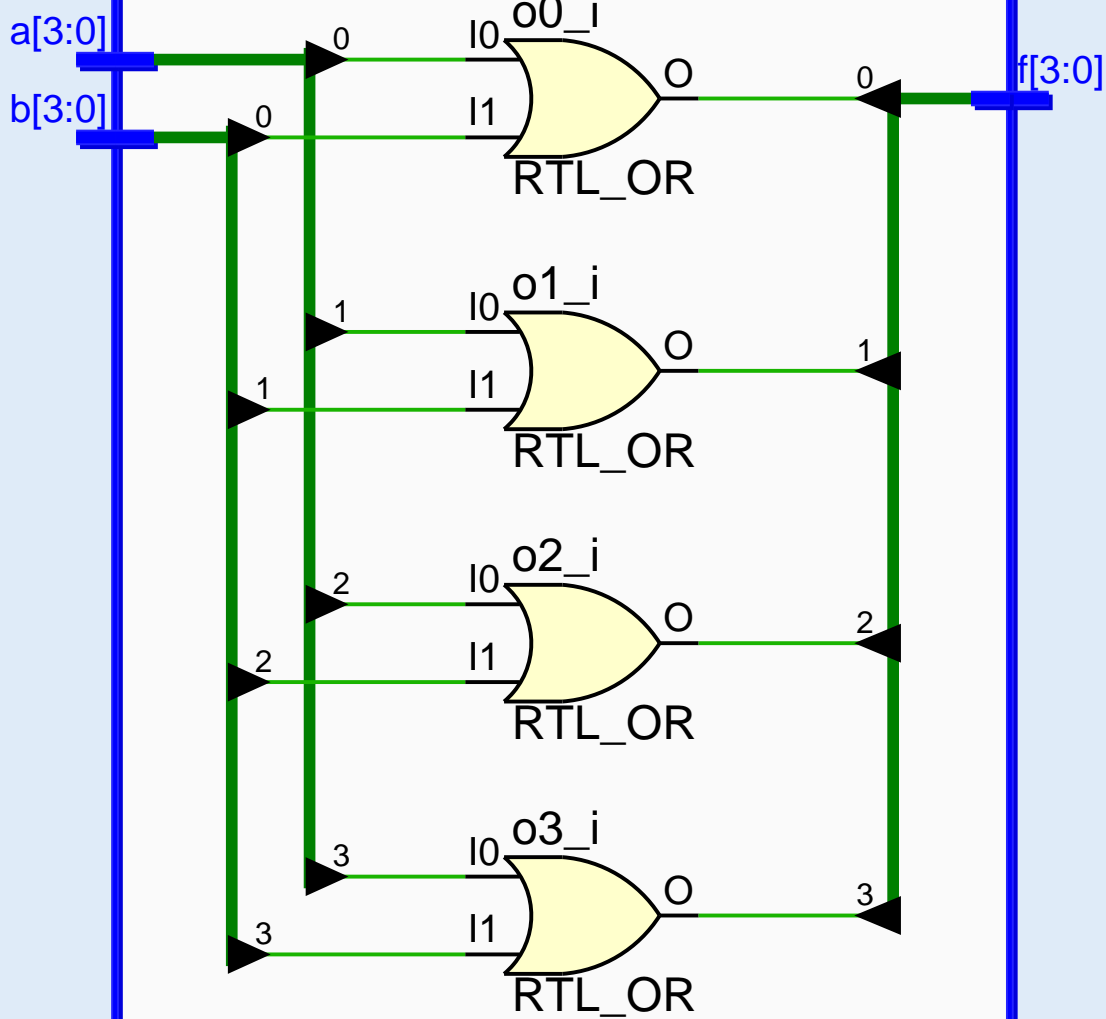


m1

turn3

tmp3



Or_4x4_4bit

Mux_2x1_4bit

Crossbar_2x2_4bit