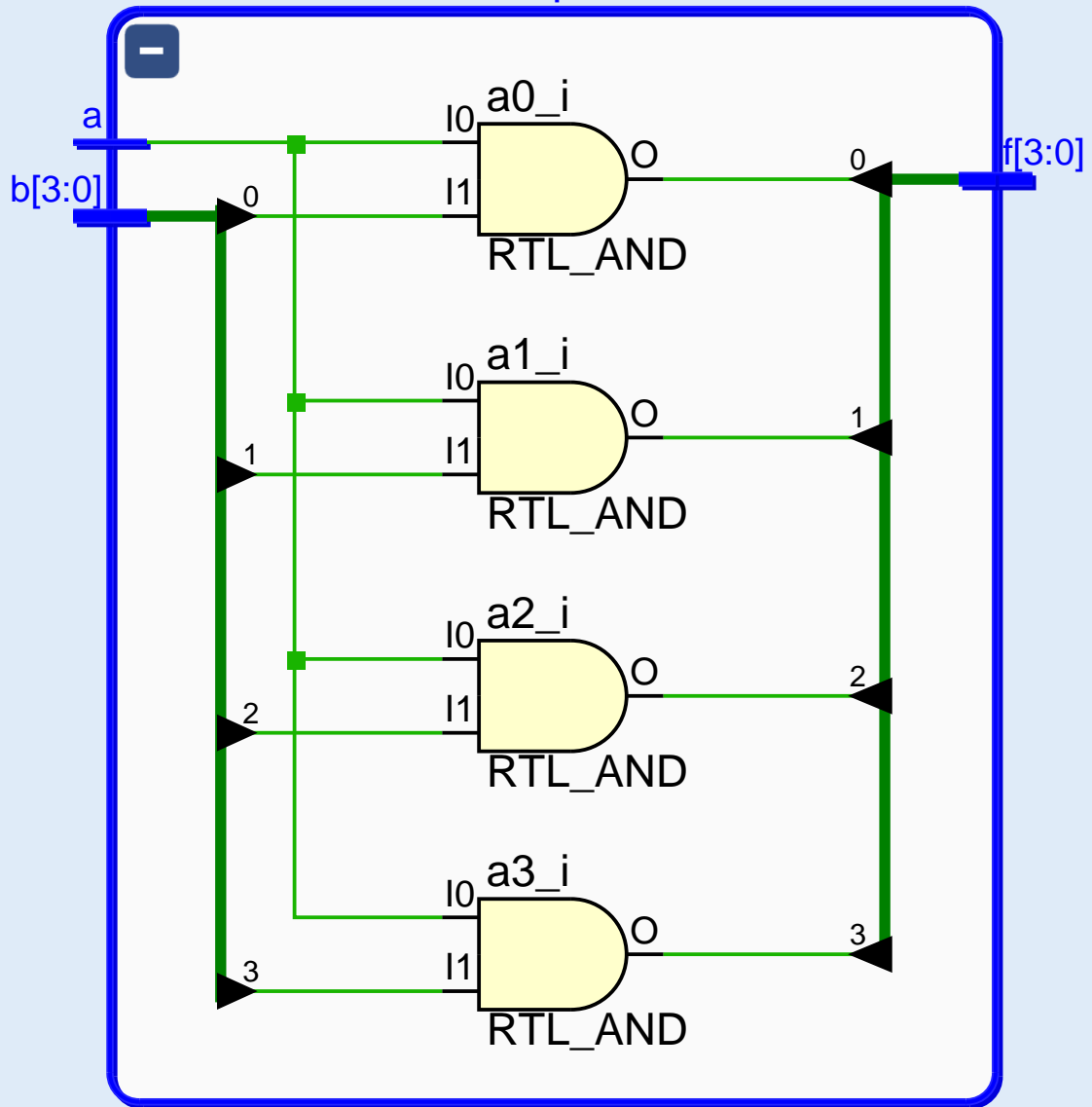


m1

turn3

tmp1



And_1x4_4bit

Mux_2x1_4bit

Crossbar_2x2_4bit