Instructions for running the code:

1. We will run the TCL code on Linux, so open terminal at the designated folder

2. Type "tclsh generate_sve_gui.tcl" and press enter

3. The GUI window will come up

4. Give the required inputs and then select a destination folder from the GUI

5. Click on "Generate" and the files will be added to the selected folder

6. (Optional) To simulate the test bench, make sure that any version of verilog that takes in terminal commands is installed

    6.1. Click on " Simulate" button, this

opens the installed verilog software and automatically

runs the simulation.