Spiral

A second of Advancement
0.6 - This a become that manages combuter's Handware,
0.5 -> It's a program that manages computer's Handware,  -provides a basis for application programs and act as an  intermediary blur and user and comp handware  went went went users won a serin geg-gove one.  It perform no useful  funir but provide
poer des la susta que apportation pougrants
usen 1 wen 2 word wern geg- gove one
Compiler assembler terdile Dalabase en jer every othe perogram?
Compiler assembler terdfle Dalabase en for every othe system?
System } programs
System and app programs  0.5
( John Markey & Hardward & Hardward & Holl)
. User's p-o.v: • It provide ease of use . Maximum resource utilization and Stability
· System's PO·V: · Resource allocator · Control program & manages user program  (·C.P.V time, IIO, memory and prevent every?
(.C.PV time, 110, memory and prevent evolor?
8 4 The program owning all times on computer called Kornel.
OSis usvally
Hultiprogramming V's Multi-tashing (Time-shaving os
to share CPU (max CPU utilisation) by rapidly switching blw them
(It's so frequent that gives illusion that
> Non-premptive ( C.PV switches only when premptive scheduling type
removed process completes or blocks of Switching based on negative grantum
-> Loss prequent content switching . More proquent content switching
Main Idea: Simulaneously
John John John John John John John John
7013
Job pool main- por bick Job one by one and execute them
one and french con
· priori librard > Non-fram one b/c-o
(i) Improved responsiveness
in Retter utilization of recomme

- and points of multi-las hing.

* Multimode - Multiple mode of eration process of machine manager were mode suffered albus visted a machine manager to less than Kernel Date
Li les than Kernel Date
Interrupt Trapox Execution
hardware of internal devices to to an error or a specific
hardware of internal devices to to an error or a specific
notify cel about an event strate needs construction in program
immediate oftention leg- Keyboard or leg-dousisionly zero, invalid
immediate oftention (eg: Keyboard or leg: division by zero, invalid  Time Intervall) memory access)
-> Triggered by: Hardware or external events . Software -
-> Handling: Interrupt handler Exception Handler
User 1 Ideanel mode (Superviser or system mode)
- restricted mode where app own with . privileged mode where O.S
Imited access to system resources has full access to system
· mo de bit: 1 1
service from 0.5 (via system call), System transist blus Kernel mode storls
service from 0.5 (via system call), System transist blus Kernel mode stools
User (=> Kurnel mode
· Times: It's a mechanism that ensures the O.S maintain control
over CPU, by interrupting a process at regular intervals
It may be fixed as variable, I when counter oreaches 10', an intorupt occurs f
and the smithes to Hund made control transfers
Level: 1 2 3 4 5
registers coche main-memory 55.0 Magnetic disk
Table
Aller 0.25-0.5 0.5-25 80-250 25K-50K 5M
time (no)
· Laure: faster storage system, store info on temp basis
If we need or to use particular info, we first but it matopy in cache
· Coche Coherency: It ensures that when data is updated in one
possesses suche, the change is suffected in all other caches
that store same data preventing inconsistencies and
· Coche Coherency: It ensures that when data is inflated in one processors cache, the change is neffected in all other caches that store same data preventing inconsistencies and asynchromiation asynchromiation spiral

Date:		Date
· O.S Senvices:		Walter State of the State of th
P) CmJ line	y and alle comme	en the representative states
2) U.I. batch interface of bat, cond, sh)	5) Communication 1	Process embange info
2) U·I > batch interface of bat, cmd, sh)  GUI	in same system (	Showed memory exmag passing)
2) Prognam: System load prognam in memory	in across system	(Network Comm)
execution own it, and program must end	MONEY DE WALLAND	enable Collab blw Process +> System
2) Program: System load program in memory execution our it, and program must end either normally or abnormally		Constant monitoring for
3) I/O oper: O.S provides a means to 10 I/O		up and to action lkc
Can't control 710 devices directly	Halt System Cil	nequire), terminale problem frag
Can't control 7/0 devices directly		
of file system: program can near, write, dit,		former but for System efficiency)
manipulation search in fale or div	allocation	age presources like CPU cycle, oxy, storage etc.
. also give permission management based on ownership	mem mem	ory, storage etc
	· usc u	lgo like CPU, stisk scheduling for two
as Accounting. Touches resource usage by users or processes	roms into brond of	v) Companyatehous I rese
3) Protection and security: ensure	controlled access	to resource
· User a	unhentication for acconnection after	cerd wht
· emphasi	re: end to end precaul	ion alla seconda de la constanta de la constan
· System Calls and AFIS	Children with	Darrie Marine Marine
System Calls	A.P.1	Chr. March 1 - M
	. Provides high	run Level interface for developers
· Provides an interface to access 0.5 services  Services. { Written in C, C++}  or assembly	hiding combles	details of Custom Calle
	· eg: Nindows. Ap	a Jetouls of System Calls I, POSIX API
Accompanies to the second second	. Makes bortable ha	the same
	Sufferling same	og to access across off 0.5
. of Shell of Margaretic licely cless	1 A Maria	W. W
System-Call Inte	yace made to	the way to the tell the tell the
:Intercepts A.P.I calls and translate into	actual System Cal	Unano agree at a
· System calls bypically are Indened by —	interlace > Number :	to Kornel's internal call.
· System calls bypically are Indened by — numbers — m	ops those	ON TRANSPORT TOTAL
· Parameters are passed through - higister	B. Memory block	in the A
flastest methods as		· Par are pushed into stand
they're discibly access by	and its allress is	and pepped off by 0.5 during
CPUZ	to 0.5	· Support Variable
		no of potometros

Types of System Calls · System calls manages life yell of processes - creating, (1) Protection management: terminating [Normal-end(), abnormal-abort()), and Handling errors of errors details enamined by debugger to fixit. evers have level (0 to severe), level canguide necessary · Locks (acquire, rulease lak(), ensure data integrity in concurred system. , load, enec, wait The file management: (reade (), delete(), move (), ropy (), get-file-all vibule ()

Open (), nead() · orequest device (), orelease device () an Device manage; get-dwice altin Set - 11 - alt () (1) Info may many: get or set time or date, system data, process fele (V) Communication: I'message passing: for small data exchange, and inter compi connections (un Protection: La Shared memory: for fast romm but negurus synchron ration (reale, delete comm' - son), necerre unseg -> atlant or delant stemple durce from user & provide service like httpd (HTTP), aparte 2 (webserve) · System program: · Designed to manage system resorvices, and help users interact with system · Higher land rebility e.g. of Shell, Diagnostic tools, etc} @ How O.S handled Interrupt Signal? in CPU stops execution of awarent process, and saves its state m) It then Jumps to interrup harden, to address Interrupt m Once Intorupt handled, Chunestores prices or resume normally Q List any two privile ged Instruction? ( In I I I I Memory mane Inst a) System control Inst dr Interrupt Control

0.5 Struture:	Date
(a) Simple Structure: · Straightforward Jesign in which	all system temperates are
bundled together in I block of code & Monolithic	Lernel Z
· No division of modules	
· e.g. M-S DOS and UNIX Kernel &	in early stage?
in Layered Structure: Divides 0.5 into multiple layers	, each of which has specific
functionalty, and interculs only with the lay	er below it (organized Hierarchy)
Layer 0: Hardware layer, Layer can developed and be easier debugging fonce lower correctly	sled Ind.
· easier debugging Conce lower	layer Jetrigged, it assumed funch
Layer N (U.Z) correctly	, while beligging above layer y
- Tope made good to ag	the layers wone into content
· less efficient, as each systemest pass twoogh multiple layers, assing overhead	m call   device driver (below)
must have through multiple layers, assing overhead	d (alobe) C.P.V Schedul },
(1) Microheurels: This opproach involved designing	g an 0.5 by snemoving all
non-essentials component from kennel and imples	menting system and user
level programs, making it more smaller, more	lan.
non-essentials component from kennel and implement level programs, making it more smaller, more It facilitates committee them	program and services owning
in user space through message fassing.	<u> </u>
egt Mach Mount ( Darwin),	
adv - New services abled without modifi	yng Kernel
· Into eased Security & smaller a	nd partable 0.5
dsadi - It suffers from performance msg passing and communication	overhead due to need for
msg' passing and communication	in I problem in Windows NTZ
de Madulas . T. 1	system drace
(d) Modules: This design was Loadable House ino dula	System druce
to add flexibility and scalability, instead of building	melsdacs]
features directly into Kernel, services are implement	Mond! Inter M.M C.PU mode pracus scheduling (Murcheoung)
or separale modules  rodules can be added to boot time	(Mischeens)
without need to recompile Kernel	1 1
	L Handara L
- Company on	Handware
h. Similar to Layered, but modules can call ay module	(Micro-herni) Structure
ove without payormance	( rulto structure)

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D-4	the state of			
Date	e_		No. No. of Street, or other teams, and the street, and the str	

In mag passing - processes don't place memory, and don't use ghared memory, instead they somm and shared data through comm' system, such as kernel or a mag queue.