ADVANCED CONTROL DESIGN TECHNIQUES FOR POWER CONVERTERS(EE608)

TOPIC: CURRENT MODE CONTROL IN BUCK CONVERTER

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BUCK CONVERTER CURRENT MODE CONTROL

Objective 1: Simulate a closed loop buck converter with current mode control

Objective 2: Obtain the bode plot before and after the controller design and show that a phase margin of 35 degrees is achieved.

Objective 3: Draw the nyquist plot of open loop gain and confirm the same as in 2

Objective 4:Draw the root-locus of the inductor series resistance variation and show the limits of the inductor series resistance variation for stable operation.

CALCULATIONS

f = 100KHz

Vi = 48V

Vo = 12V

 $\Delta Vo/Vo = 0.05$

 $R = 15\Omega$

 $L=100\mu H$

 $C = 26\mu F$

D = 0.25

 $\Delta iL = 2\%$

 $\triangle Vo = 5\%$

Outer loop

Kp=0.08

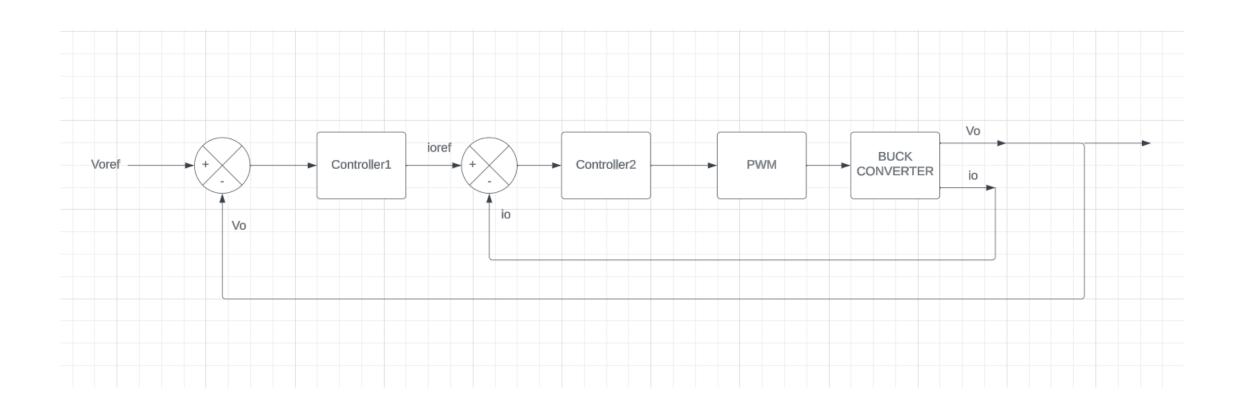
Ki = 704.03

Inner loop Kp=0.009

Ki=50.90

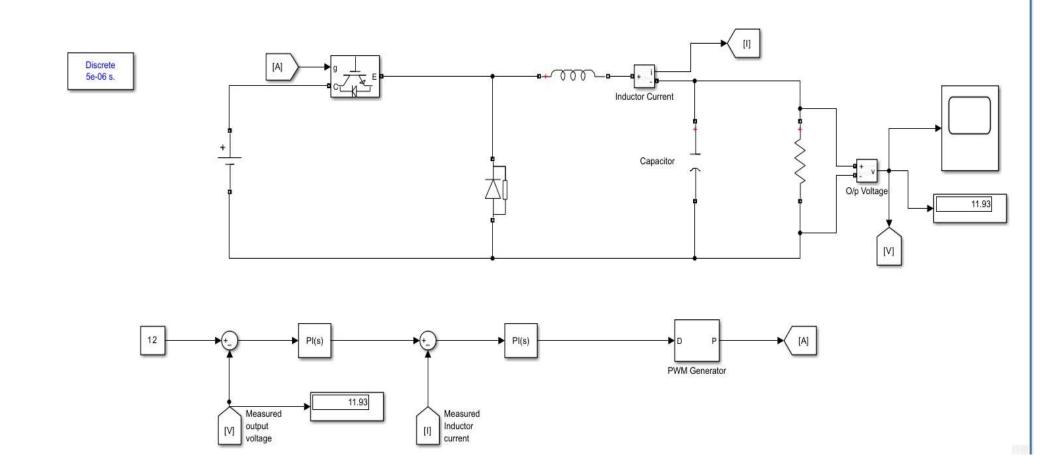
- ❖Inner loop is faster than outer loop.
- Some tolerance is also considered.

BLOCK DIAGRAM

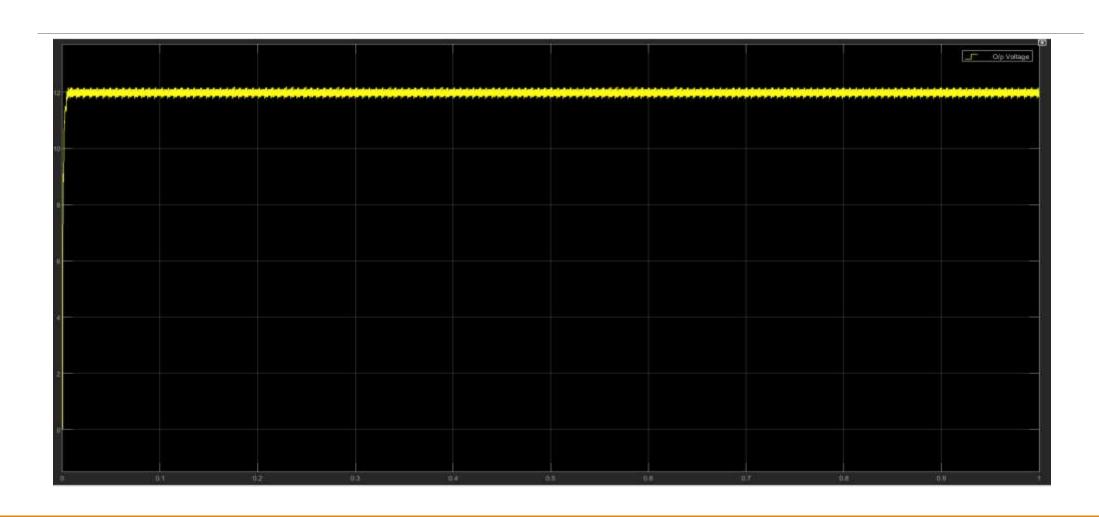


- 1. Simulate a closed loop buck converter with current mode control

- Obtain the bode plot before and after the controller design and show that a phase margin of 35 degrees is achieved.
 Draw the nyquist plot of open loop gain and confirm the same as in 2
 Draw the root-locus of the inductor series resistance variation and show the limits of the inductor series resistance variation for stable operation.

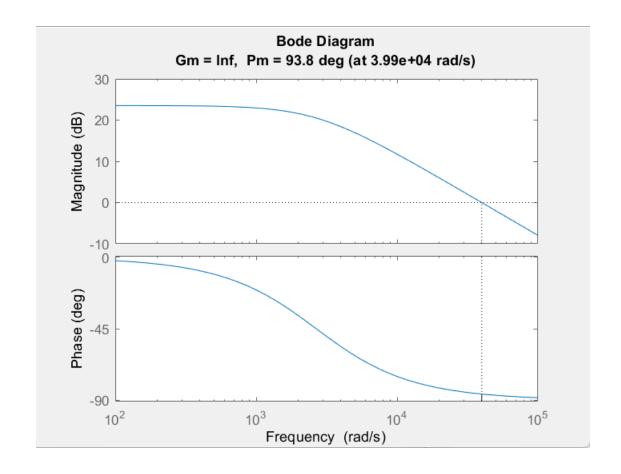


Output Voltage



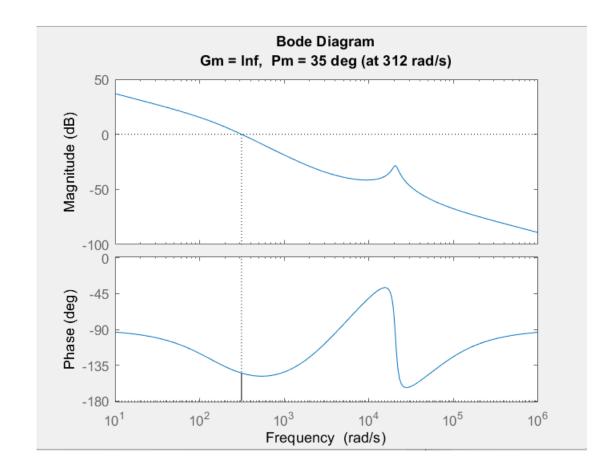
MATLAB CODE FOR FINDING PHASE MARGIN Without Controller

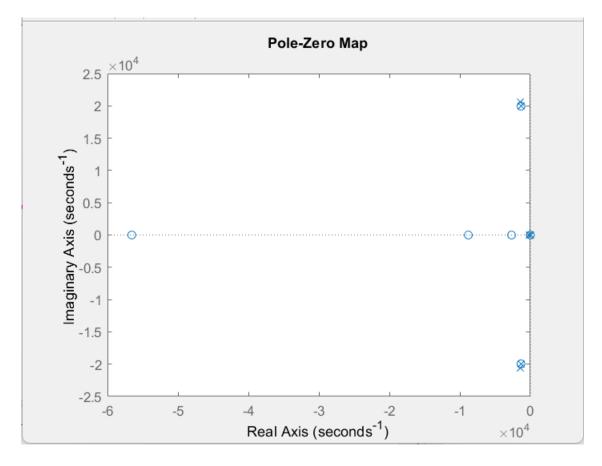
```
clear all;clc;
Vdc=48:
                                                       \n Transfer Function without PI controller:
R=15;
L=100e-6;
                                                       flag =
C=25e-6;
n = [Vdc/L Vdc/(R*L*C)];
                                                                  1.92e10 \text{ s}^2 + 5.12e13 \text{ s} + 7.68e18
d=[11/(R*C)1/(L*C)];
t = tf(n,d); %(iL/d) wo PI
n 1=[Vdc/(L*C)];
                                                          480000 \text{ s}^3 + 2.56e09 \text{ s}^2 + 1.954e14 \text{ s} + 5.12e17
d 1=[1 1/(R*C) 1/(L*C)];
t 1=tf(n 1,d 1); % Vo/d wo PI
                                                       Continuous-time transfer function.
disp('\n Transfer Function without PI controller:');
flag= (t 1)/t % Vo/iL wo PI
```

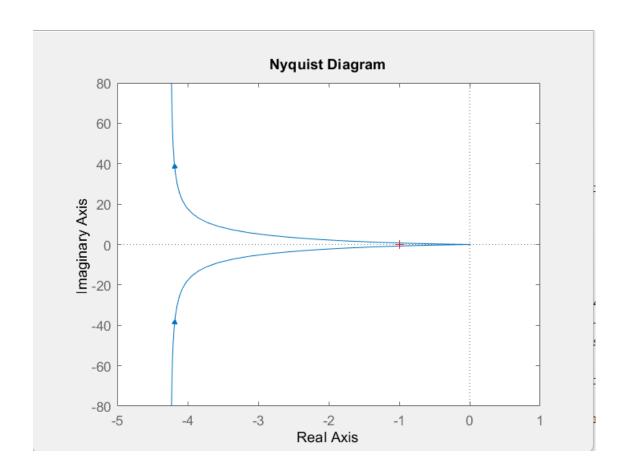


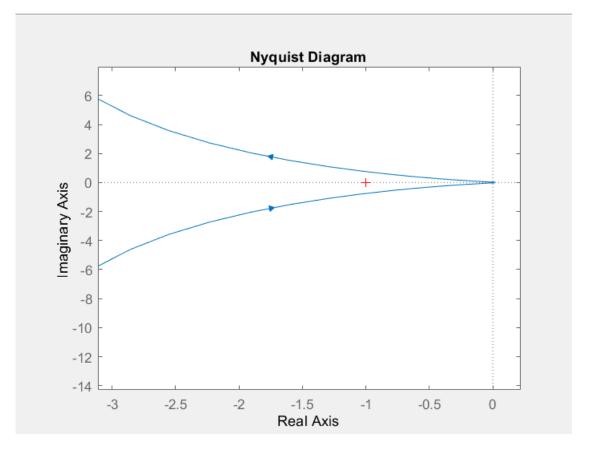
MATLAB CODE FOR FINDING PHASE MARGIN With Controller

```
kp_i = [0.0009];
a=50.90;
m = [1 0];
ki i= a*tf(1,m);
c= kp i+ki i;
t 11=t*c; %iL/d w PI inner loop
In= t_11/(t_11+1);
 kp_o = [0.08];
p = 704.03;
m_1 = [1 0];
ki_2= p*tf(1,m_1);
 c_2= kp_o+ki_2;
 disp('\n Transfer function after adding PI contoller:')
 flag 2= (In*c_2)
%margin(flag_2);
%pzplot(flag_2)
%nyquist(flag_2)
%r=0.005:0.02:1
```

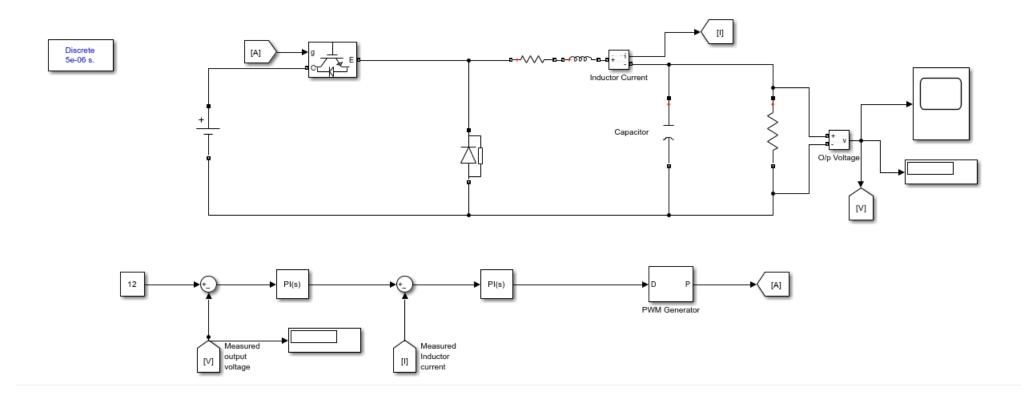






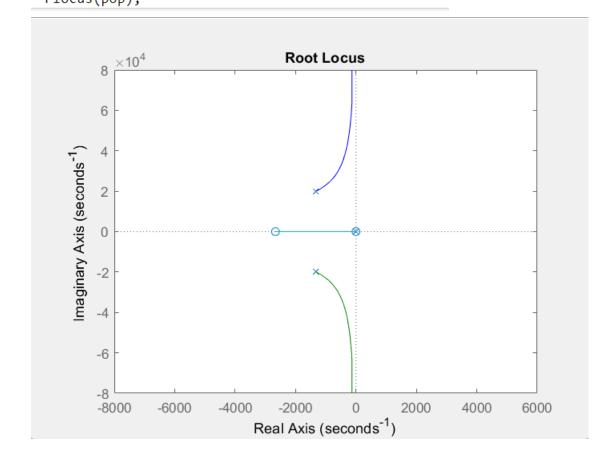


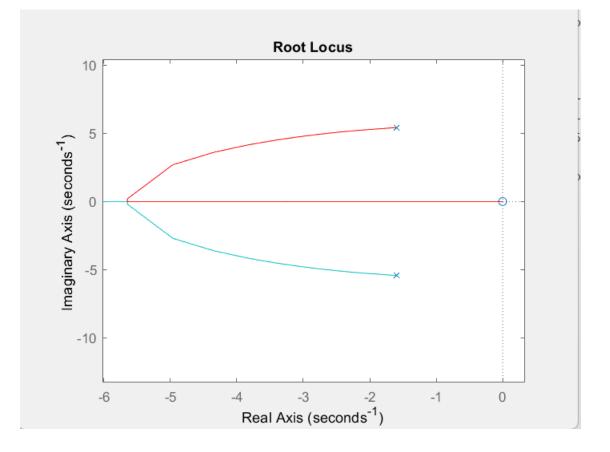
Draw the root-locus of the inductor series Resistance variation and show the limits of the inductor series resistance variation for stable operation.



```
\n Transfer Function of iL/d::
                Vdc
    / 2 / r 1 \ 1 r \
C L R | s + | - + --- | s + --- |
     \ \L CR/ CL CLR/
\n Transfer function of open loop:
                             kii \
            Vdc (ki + kp s) | kpp + --- |
Vdc ki + Rs + rs + Ls + Vdc kps + CLRs + CRrs
Expression with "r" factored out from the denominator:
   Vdc ki kii + Vdc kp kpp s + Vdc kii kp s + Vdc ki kpp s
(CRs + s) r + Ls + Rs + Vdc ki s + Vdc kp s + CLRs
```

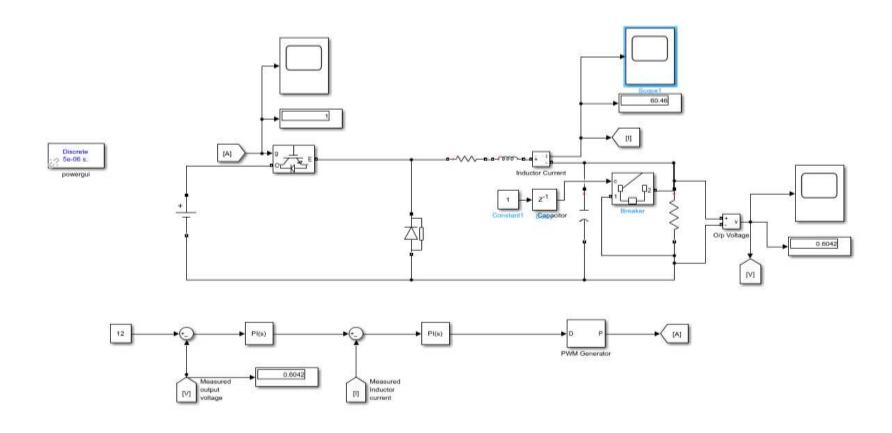
```
Num = [(R*C) 1 0 0];
den = [(R*L*C) L R (Vdc*(kp_i)) (Vdc*(a)) 0];
disp('\n Transfer function with controller::');
pop=tf(Num,den)
rlocus(pop);
kp_i=1; %kp inner
a=10; %ki inner
```

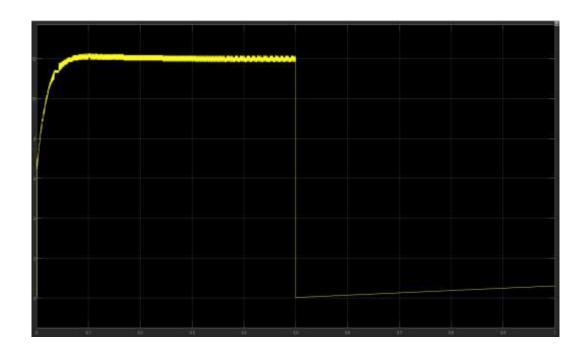




EXTRA TASKS

SHORT CIRCUIT



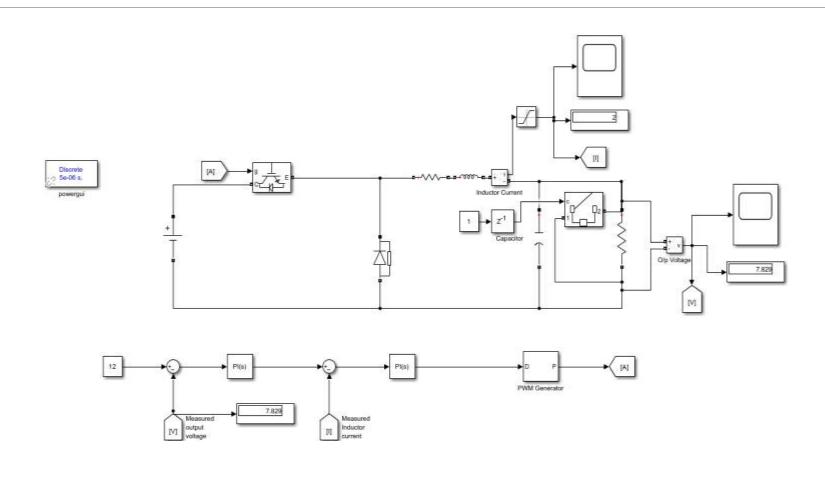


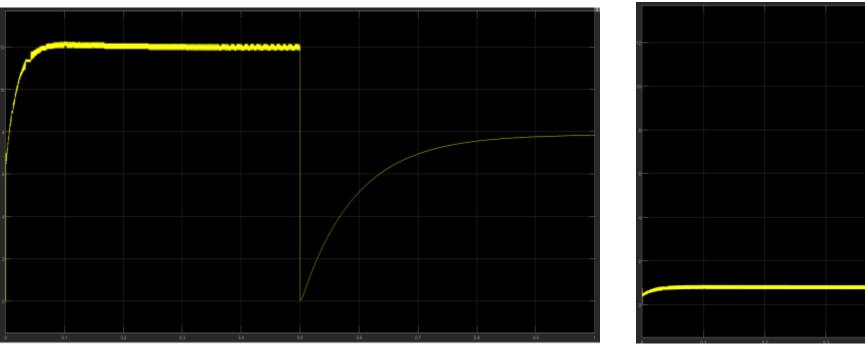
Voltage Profile



Current Profile

SHORT CIRCUIT WITH SATURATION BLOCK



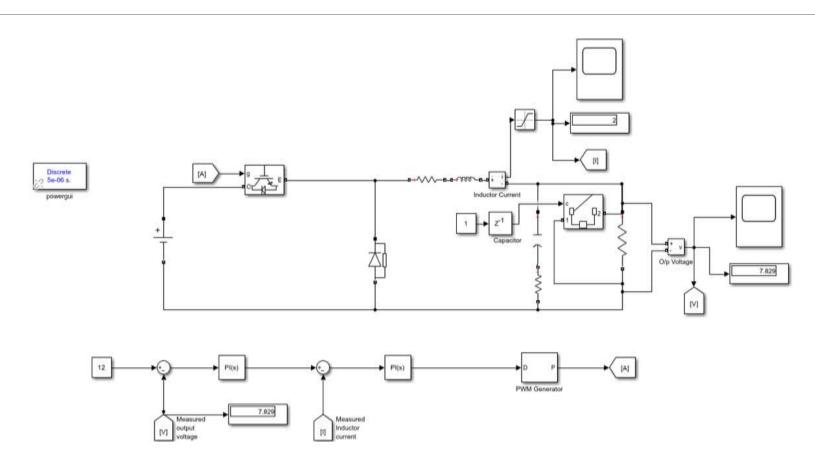




Voltage Profile

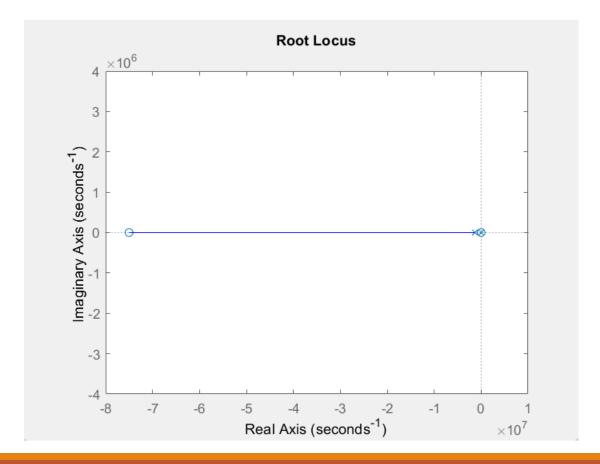
Current Profile

Resistance variation and show the limits of the Capacitor series resistance variation for stable operation.



```
\n Transfer Function of iL/d::
             Vdc (L - C Rc Rl)
    / 2 L + Rl Rl s (L + C R Rc) \
C L (R + Rc) | s + ----- + -----
          C L (R + Rc) 2
                          C L (R + Rc) /
\n Transfer function of open loop:
                                      / kii \
                             Vdc (ki + kp s) | kpp + --- | (L - C Rc Rl)
L s + L Vdc ki + L Rl s + L Rl s + C L R s + C L Rc s + L Vdc kp s - C Rc Rl Vdc ki + C R Rc Rl s - C Rc Rl Vdc kp s
Expression with "Rc" factored out from the denominator:
(C Rl Vdc ki kii + C Rl Vdc kii kp s + C Rl Vdc ki kpp s + C Rl Vdc kp kpp s ) Rc - L Vdc ki kii - L Vdc kii kp s - L Vdc ki kpp s - L Vdc kp kpp s
            2 4 3 2 2 2 3 2
        (-CLs-CRRls+CRlVdckps+CRlVdckis)Rc-Ls-LRls-LRls-LVdckps-CLRs-LVdckis
```

Continuous-time transfer function.



```
clear all;clc;
Vdc=48;
R=15;
L=100e-6;
C=25e-6;
r=0.05;
n = [Vdc/L Vdc/(R*L*C)];
d=[11/(R*C)1/(L*C)];
t = tf(n,d); %(iL/d) wo PI
n 1=[Vdc/(L*C)];
d_1=[1 1/(R*C) 1/(L*C)];
t 1=tf(n 1,d 1); % Vo/d wo PI
disp('\n Transfer Function without PI controller:');
                 % Vo/iL wo PI
flag= (t 1)/t
kp i = [0.0009];
a=50.90;
m = [1 0];
ki i= a*tf(1,m);
 c= kp i+ki i;
 t 11=t*c; %iL/d w PI inner loop
 In= t \frac{11}{(t 11+1)};
 kp o= [0.08];
p = 704.03;
m 1 = [1 0];
ki 2 = p*tf(1, m 1);
 c_2= kp_o+ki_2;
Num = [((L^2)*C) (C*R*r) (-(C*r*Vdc*(kp i))) (-(C*r*Vdc*(50.90))) 0]
den = [(R^*(L^2)^*C) (L^*r) ((L^2)+(L^*r)+(L^*Vdc^*(kp_i))) (L^*Vdc^*(50.90))]
disp('\n Transfer function with controller::');
pop=tf(Num,den)
rlocus(pop);
```

REFERENCE

- [1] Hasan Sucu, Taner Goktas, Muslum Arkan, "Design, Simulation and Application of Buck Converter with Digital PI Controller", Balkan journal of electrical & computer engineering, Vol. 9, No. 2, April 2021.
- [2] P.C. Sen, Zaohong Yang, "DC-TO-DC buck Converters with novel current mode control", 1999 IEEE

THANK YOU