# Synthesis of Maximally Permissive Strategies for LTL<sub>f</sub> Specifications

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#### LTL<sub>f</sub> Synthesis

- Autonomy, one of the grand objectives AI
  - Autonomous system, react to environment changes
- LTL<sub>f</sub> synthesis [De Giacomo & Vardi, 2015]
  - Linear Temporal Logic on finite traces [De Giacomo & Vardi, 2013]
  - Obtain a winning strategy (system model) From a declarative specification

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  - preserves more autonomy
  - agent chooses det. strategy while in execution, without committing to any specific one beforehand

## Maximally Permissive Strategy for LTL<sub>f</sub> Specifications

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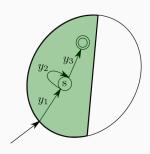
#### **Theorem**

 $\mathsf{LTL}_f$  **DOES NOT** admit a single nondeterministic strategy that captures MaxSet.

## MaxSet of LTL<sub>f</sub> through Reachability Games

#### Observation

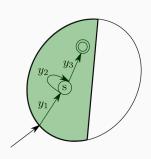
- Winning region maintains the ability to win
- "staying in the winning region" ≠ "win the game"
  - Taking y<sub>2</sub> at s forever does not lead to the goal



# MaxSet of LTL<sub>f</sub> through Reachability Games

#### Key idea

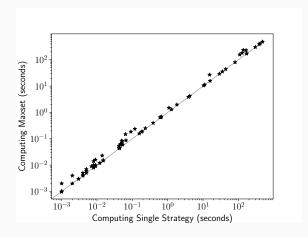
- Every winning strategy switches
  - from "deferring" (y<sub>2</sub> at s, staying in the winning region)
  - to "non-deferring" (y<sub>3</sub> at s, surely reaching the goal)
- No matter when to switch, the switching is mandatory



#### MaxSet of LTL<sub>f</sub> Specifications

- A single nondet. strategy  $\Pi_{df}$  captures all the deferring strategies (maintaining the ability to win)
- A single nondet. strategy  $\Pi_{ndf}$  captures all the non-deferring strategies (assuring to win)
- A declarative constraint forcing any choice function ch eventually switch from  $\Pi_{df}$  to  $\Pi_{ndf}$

# **Experimental Evaluations**



 Computing MaxSet only brings a minor overhead comparing to computing a single strategy

#### **Conclusions and Future Research**

- MaxSet of LTL<sub>f</sub> specifications
  - Two nondeterministic strategies and a constraint
  - A minimal overhead
- Improve the performance of LTL<sub>f</sub> synthesis
- MaxSet of other specification languages, e.g., LTL

Poster: Slot 201 at Row 6