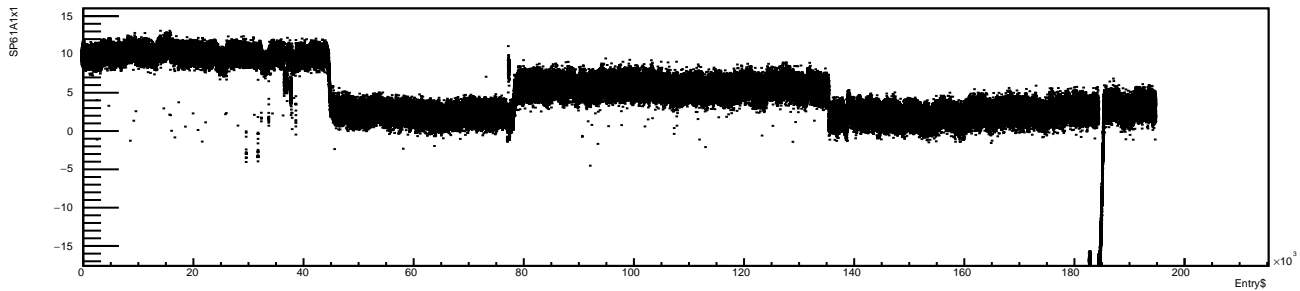
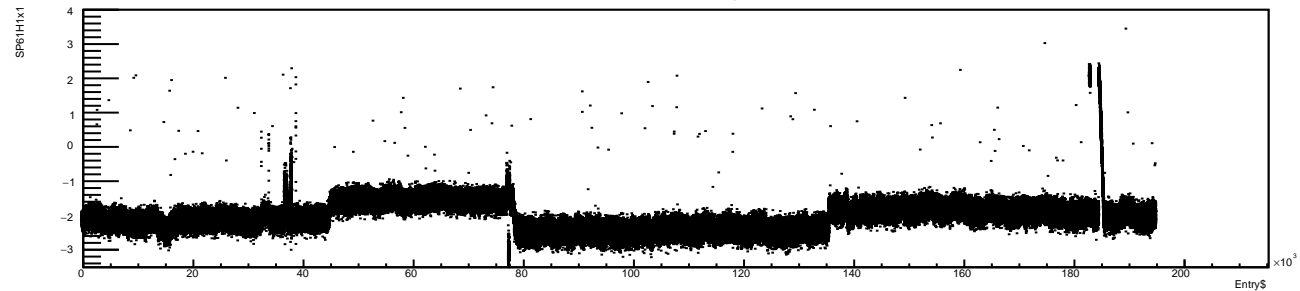


SP61A1x1:Entry\$



SP61H1x1:Entry\$



adc[0]:Entry\$

