TMDS = Transition minimised differential signalling HDMI = High Definition Multimedia Interface.

TMDS Data 1+ Audio 11 Shield 11

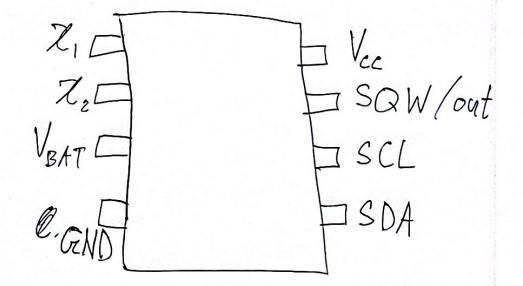
Data 1+ 7. TMDS 11 O Shield > Data control period

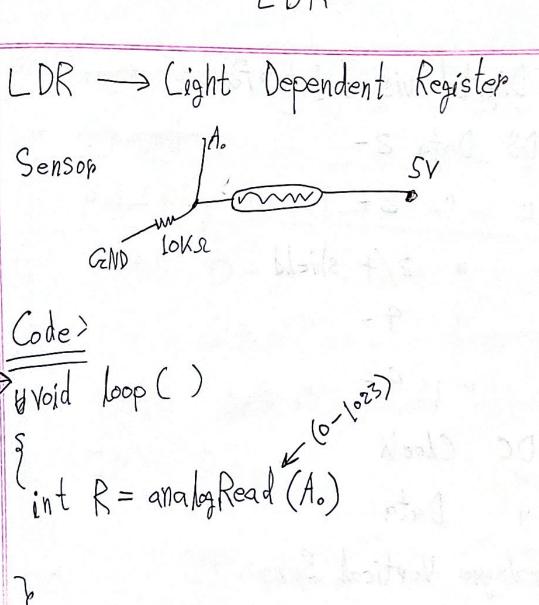
(11. FOTMDS Clock + 11. Shield 13. CEC (Consumer Electronic Control) 14 No connection 15. DDC elock (Display Data Channel) Clock. 16. 11 Data. 17 GND. 18 +5V 19. Hot Plug Detect (i) Video data period (ii) Data island period

(iii) 11 control 11

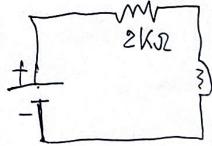
RTC Module

RTC = Real Time Clock

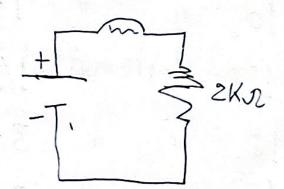




#define pin A.



Resistance connected to positive of source

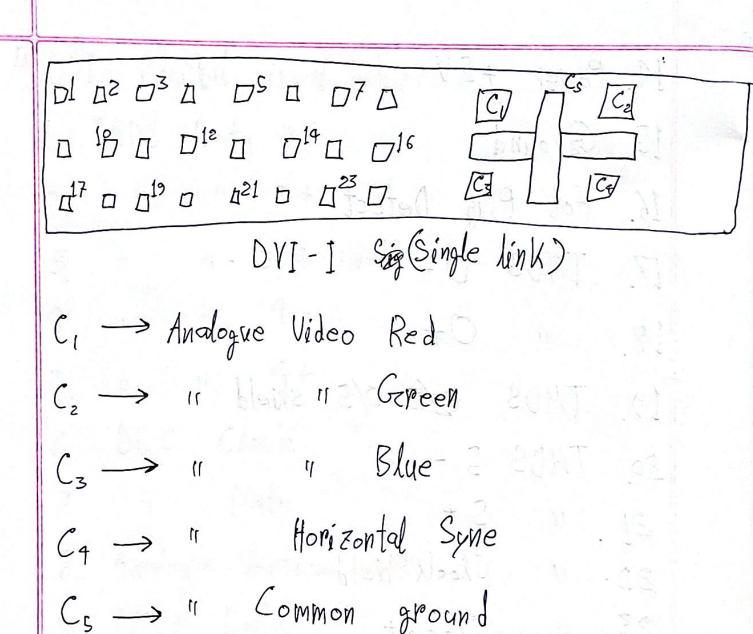


Resistance connected to hegative of source

DVI = Digital visual Interface 1. TMDS Data 2-11 " 2+ 3 11 11 2/4 shield 4. 11 4-11 11 4+ 6 DDC Cloek 11 Data (1) 8. Analogue Vertical Sync 9 TMDS Data 1-10 11. "-1/3 shield 12. " " 3-11 3+

14. Power +SV 15 Ground 16. Hot Plag Detect 17. TMDS O-18 11 Otal webl englant = 1) 19 TMDS 0/5 shield 20 TMDS 5 -21. 11 St 22. 11 Clock Shield 23. 11 Clock+ 24 molt -1 = 2 en en 2 = 1 10 f

In Transition Minimised Differential Signaling = TMDS



 Φ DVI-A -> Same as DVI-I without Pins 3,4,5, 9, 10, 11, 12, 13, 19, 20, 21, 2 and 22.

VSync

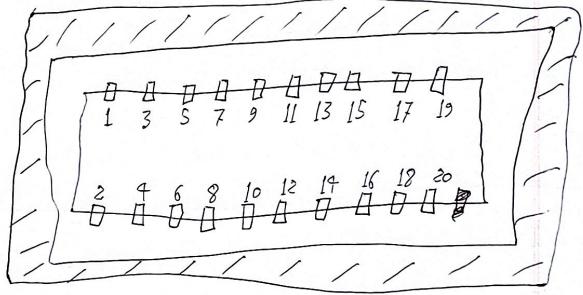
ID3

1024 X768

| 15 pins;- |
|-----------------|
| 1. Red 14. |
| 2. Green (15. |
| 3. Blue |
| 4. ID,2 |
| 5. A. GND |
| 6. Red return |
| 7. Green 11 |
| 8. Blue 11 |
| 9. 5V |
| 10. GND (VSyne) |
| 11. [D0 |
| 12. [0] |
| 13. HSync |

21/09/22

| | | | 10,05.22 |
|-----|------|-------------------------------|-------------------|
| | 5, 4 | 3 • 2 • 8 • 7 • 13 • 12 | • 11 • 11 |
| | | Ty | 1 |
| M4: | 791 | 12 | |
| IDS | IDO. | IDI | 2 |
| N/C | N/C | N/c- | -> no monitor |
| N/C | N/C | GND - | → mono monitor |
| N/C | GND | N/C _ | > Colour |
| GND | GND | N/c | not 1024 X768 |
| | | 7 | colour |



10, GND

20 PWR

HPD=Hot Ply Detect
HS=High SPSpeed
T= Transfer
R=Receiver
LSR=Low Speed Transfer