

Cache

W2	T3	D3	<i>Set 3</i>
WID	tag	data	<i>Set 2</i>
W1	T1	D1	<i>Set 1</i>
W0	T0	D0	<i>Set 0</i>

Victim tag array

W3	tag	tag	...	tag
W2	tag	tag	...	tag
W1	tag	tag	...	tag
W0	tag	tag	...	tag

(W0) T0
Written to VTA

(a)

Cache

W2	T3	D3	<i>Set 3</i>
WID	tag	data	<i>Set 2</i>
W1	T1	D1	<i>Set 1</i>
W3	T4	D4	<i>Set 0</i>

Victim tag array

W3	tag	tag	...	tag
W2	tag	tag	...	tag
W1	tag	tag	...	tag
W0	T0	tag	...	tag

(W0) T0
Probe on cache miss



(b)