Refer to one doc previously(what is bluespec and all..)

Dumping C Code: ■ Dumping C Code on Shakti

Board setup: Board Setup Shakti

### Links for XADC Documentation:

https://www.xiio 31linx.com/content/dam/xilinx/support/documents/ip documentation/axi xadc/v 1\_00\_a/pg019\_axi\_xadc.pdf

https://docs.xilinx.com/r/en-US/ug480\_7Series\_XADC/7-Series-FPGAs-and-Zynq-7000-SoC-XADC-Dual-12-Bit-1-MSPS-Analog-to-Digital-Converter-User-Guide-UG480

#### Shakti group:

https://groups.io/g/sp2020-shakti/topic https://groups.io/g/sp2020-shakti/topic/81373741#1328

## Shakti Blog:

https://blogshakti.org.in/

#### Shakti website:

https://shakti.org.in/

#### Shakti gitlab:

https://gitlab.com/shaktiproject/software/shakti-sdk/-/find\_file/maste

#### Board manual:

https://digilent.com/reference/programmable-logic/arty-a7/start https://digilent.com/reference/programmable-logic/arty-a7/reference-manual

Link for the bit file that can program the board with shakti: https://github.com/Tanish25/Shakti/blob/Shweta/fpga\_top.bit

#### Location of bitfile:

/home/iitdh/shakti/shakti\_working/shakti\_working.runs/core\_impl\_1/

#### Pin mapping:

https://gitlab.com/shaktiproject/sp2020/-/blob/master/e32-a100/README.rst

#### Arty-A7 master XDC

https://github.com/Digilent/digilent-xdc/blob/master/Arty-A7-100-Master.xdc

For Ports checking- Is /dev/tty\*

# Shakti pin mapping:

https://gitlab.com/shaktiproject/sp2020/-/tree/master/e32-a100

## PLIC Manual:

https://gitlab.com/shaktiproject/software/shakti-sdk/-/blob/master/doc/plic\_user\_manual.pdf