FPGA Project RAILWAY TICKET RESERVATION SYSTEM

Name: G.Sriharsha

Name: G.Badrinath Reddy

Name: K.Vamsi Pramod

Aim:

Allotment of limited number of tickets(tatkal) to the users in a similar way of online railway ticket booking

Apparatus

SEVEN SEGMENT DISPLAY LED FPGA BREAD BOARD RESISTORS ULN DRIVER

About Railway System

Railway website can be accessed by user who has a login id and password. When he enters in to the website he checks whether the tickets are available or not. If the tickets are available he will further proceed to book that tickets. Then a session will be allocated to the user to make payment for booking ticket. If he

fails to make payment in that session he will be thrown out of the website. If he pays in that session then he will receive booking confirmation message.

Specifications

- *At a time ,maximum 4 users can access the railway website.
- *A user can book maximum 3 tickets at a time.
- *Number of tickets available are 15.
- *User should make payment with in 15 seconds (session).
- *Number of tickets available are displayed on 7segment display

Description

A particular user can enter in to the website by pressing the push button. Since the maximum number of users access website at a time is 4, four push buttons will be there. When the user entered in to the website, a session will be allocated to that user. With in that session he should complete the whole process of ticket booking. This operation is done by the counter. The counter starts when the user entered in to the website. counter will run from 0 to 15.

Number of tickets available are displayed on 7segment display. When the user entered in to the website he checks the available number of tickets on the seven segment display. Two inputs will be given to the user to enter number of tickets he want to buy. Using those inputs(binary) he can buy atmost 3 tickets. He checks whether the input given is more or less than available number of tickets on the red led light. The red led light will be on if the available tickets are more than or equal to the user input. If not the red led will be off.

Then he makes payment by pressing the same push button which he used to enter the website. Then he receives the confirmation message from a green led (which will be on if payment is successful). Now the number of tickets available will be updated i.e difference of number of tickets available before

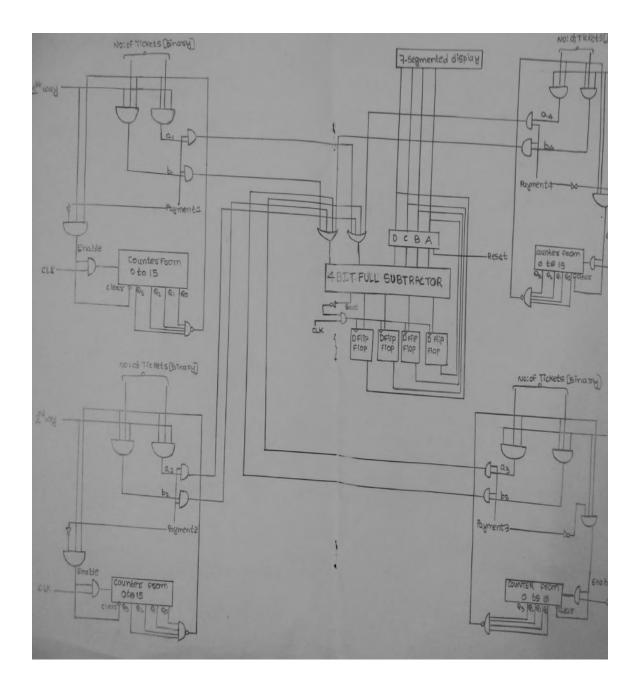
booking and number of tickets user booked. This operation is done by full subtractor.

At a time, 4 different users can access the website through 4 ways. The same process is done for all the users. Once the above process is completed by user, another user can enter the website or if the session of that user completes another user can enter the website. If any user is ongoing through the process, another used can't enter the website through that way. The next user can check this by the led light. The led light will be on if any user is ongoing through the process. So the next user can enter only after that led is off.

If suppose 3 tickets are available, if the first user enters and select the tickets but he didn't complete the payment in the session during that time another user may enter, select and pays the money then the second user gets the 3 tickets though the first user session is going on.

To reset the entire process, give the reset input as high.

Circuit Diagram



Explanation of Circuit Diagram:

- *When the user enters the website the clock Enable is high and the clear is high so the counter starts running from "0".
- *And the user can choose number of tickets upto 3 in meantime.
- *The clock Enable remains high until the counter reaches value 15 or the user makes payment.

*CASE1:

If counter reaches 15 the count Enable becomes low so user becomes inactive[Displayed by LED1].

*As soon as the Enable becomes low clear is low so the values of flipflops in counter are cleared to zero.

*So when user again enters the website counter starts form zero.

*CASE2:

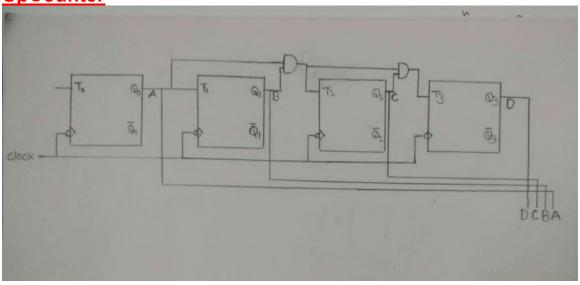
If user makes payment the count Enable becomes low and remaining logic is same as in CASE1 for counter

*If the number of tickets selected by the user are available [Displayed by LED2].

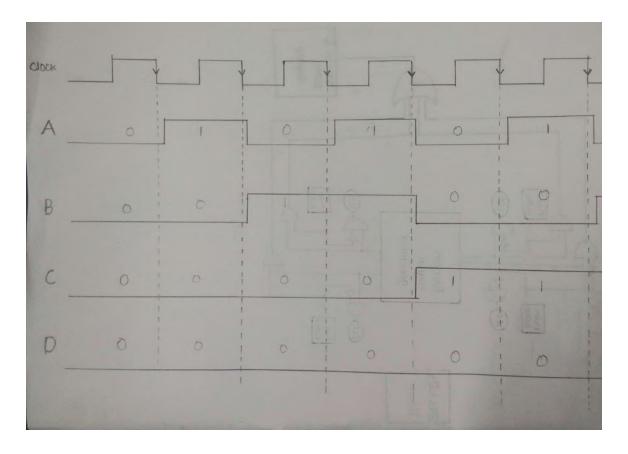
*Then payment is shown sucessfull [Displayed by LED3]

*The logic is same for all the users and the clock provided to all the counters is the same clock.

UpCounter



TIMING DIAGRAM



This is the timing diagram for an up counter from 0 to 15 and again comes back to 0.We have used 4 up counters and the timing diagram is same for all the counters.

INFERENCES:

The verilog code written in vivado simulator is shown below

```
module code(
                    input CLK,
                    input way1,way2,way3,way4,
input ticket11,ticket21,ticket31,ticket41,
                    input reset,
input ticket12,ticket22,ticket32,ticket42,
                    output a,b,c,d,e,f,g,
output reg active1,reg active2,reg active3,reg active4,
output reg success1,reg success2,reg success3,reg success4,
output reg avail2,reg avail3,reg avail4,
output reg avail1
11
12
13
14
15
16
                   );
reg k1,k2,k3,k4,avail1,temp;
reg [4:0]selected1;
reg [4:0]selected2;
reg [4:0]selected3;
reg [4:0]selected4;
wire A,B,C,D;
reg [3:0] totaltickets;
reg [32:0] counter;
reg [4:0] timer1;
reg [4:0] timer2;
reg [4:0] timer3;
reg [4:0] timer4;
initial counter=0;
17
18
19
20
21
22
23
24
25
26
27
28
29
30
                    initial counter=0;
initial totaltickets=4d15;
                    initial timer1=0;
initial timer2=0;
                    initial timer3=0;
initial timer4=0;
31
32
33
34
                    initial k1=0;
                    initial k2=0;
initial k3=0;
35
36
                    initial k4=0;
37
38
                    initial success1=0;
initial avail1=0;
                    initial active1=0;
                    initial success2=0;
initial avail2=0;
initial active2=0;
                    initial success3=0;
initial avail3=0;
                    initial active3=0;
                    initial success4=0;
initial avail4=0;
initial active4=0;
50
51
52
53
54
55
56
57
58
59
60
                    always @ (posedge CLK)
                    begin
                             if(counter==32d125000000)
                             begin
                                                counter=0;
                                                   f(reset==1)
                                               begin
                                               k1=0;
                                               active1=0;
                                               success1=0;
```

```
avail1=0;
k2=0;
active2=0;
success2=0;
avail2=0;
k3=0;
active3=0;
success3=0;
avail3=0;
k4=0;
active4=0;
success4=0;
avail4=0;
totaltickets=4d15;
end
if(way1==1)
begin
k1=~k1;
     end
active1=k1;
if(activel==1)
begin
    if (timerl==5d16 )
        begin
        kl=0;
        activel=0;
        timerl=0;
        avail1=0;
        success1=0;
end
       end
else
begin
if(ticket11==0 && ticket12==0)
                                begin
selected1=4d0;
               selected1=4d0;
end
if(ticket11==0 && ticket12==1)
begin
selected1=4d1;
end
if(ticket11==1 && ticket12==0)
begin
                               begin
selected1=4d2;
                end
if(ticket11==1 && ticket12==1)
                                begin
selected1=4d3;
               selected1=4d3;
end
if(totaltickets>=selected1 && selected1!=4d0)
begin
                            avail1=1;
                end
else
begin
               avail1=0;
```

```
if(way1==1 && kl==1)
begin
    timer1=0;
    timer1<=timer1+1;
success1=0;
end</pre>
                        successi=0;
end
if(wayl==0 && kl==1)
begin
timer1<=timer1+1;
end
end
if(way1==1 && k1==0)
begin
                                                if(avail1==1)
begin
    success1=1;
    avail1=0;
    totaltickets=totaltickets-selected1;
    temp=totaltickets;
    if(temp-selected2)
    begin
        avail2=0;
    end
    if(temp<selected3)
    begin
        avail3=0;
    end
    if(temp<selected4)
begin
        avail4=0;
    end
end</pre>
                                                   begin
avail1=0;
end
       // SECOND USER CODE BEGINS if(way2==1)
                                                                              begin
k2=~k2;
                                                                            end
active2=k2;
                                                                  if(active2==1)
begin
    if(timer2==5d16)
    begin
     k2=0;
    active2=0;
    timer2=0;
    avail2=0;
    success2=0;
end
                                                                                          end
                                                                             enu
else
begin
if(ticket21==0 && ticket22==0)
begin
selected2=4d0;
```

```
begin
      k3=~k3;
    end
   active3=k3;
if(active3==1)
begin
if(timer3==5d16)
        begin
            k3=0;
            active3=0;
            timer3=0;
            avail3=0;
            success3=0;
        end
        if(ticket31==0 && ticket32==0)
                begin
                    selected3=4d0;
                end
        if(ticket31==0 && ticket32==1)
                begin
                    selected3=4d1;
                end
        if(ticket31==1 && ticket32==0)
                begin
                    selected3=4d2;
                end
        if(ticket31==1 && ticket32==1)
                begin
                    selected3=4d3;
                end
        if(totaltickets>=selected3 && selected3!=4d0)
        begin
              avail3=1;
        end
        begin
            avail3=0;
        end
        if(way3==1 && k3==1) // user entered website , timer starts
        begin
            timer3=0;
timer3<=timer3+1;</pre>
            success3=0;
        if(way3==0 && k3==1)
        begin
            timer3<=timer3+1;
        end
     end
end
if(way3==1 && k3==0) //payment of user3
      begin
                if(avail3==1)
                begin
                    success3=1;
                    avail3=0;
```

```
### Titled the state of a tickets - selected;

total tickets - selected;

total tickets - selected;

total tickets - selected;

popularity

popularity

end

// Moder Code Become

// Moder Code Becom
```

```
| Timer4<=timer4+1; | success4=0; | stress4=0; | stress4=
```

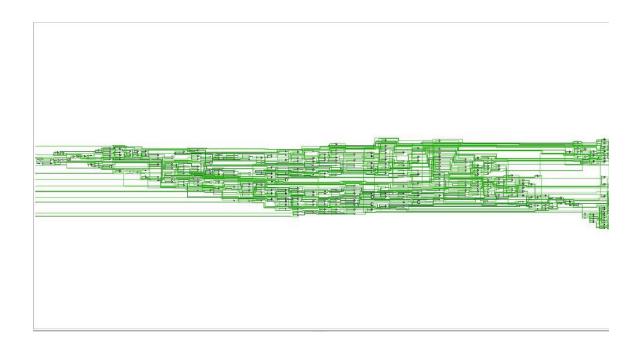
	INPUTS			OUTPUTS	;
SL.N	ENTER/	SELECT	ACTIVITIY	AVAILABILITY	
SUCESS	PAYMENT	TICKETS	[LED1]	[LED2]	[LED3]
user1	way1	ticket11 ticket12	active1	avail1	success1
user2	way2	ticket21 ticket22	active2	avail2	success2
user3	way3	ticket31 ticket32	active3	avail3	success3
user4	way4	ticket41 ticket42	active4	avail4	success4

Simulation

^{*}The input/output corresponding to each user is as follows.

™ CLK	z						
way1	1						
way2	1						
¹a way3	1						
way4	0						
ticket11	1						
To ticket21	1						
™ ticket31	1	F					
₹ ticket41	1						
1 reset	0						
ticket12	1						
ticket22	1						
ticket32	1						
16 ticket42	1						
l a a	1						
1 _m b	0						
™ c	0		-				
T∰ d	o						
T _e e	1						
₩ f	1						
T _e g	1						
active1	0				-		
active2	0	-					
1 active3	0						l
☐ active4	0						
¹₀ success1	0						
□ success2	0						
1 success3	0						
1 success4	0						
avail2	o						
Te avail3	0						
avail4	0		_				
Ta avail 1	0		_				
				1			
70 44							
avail 1	0						
™ k1	0						
™ k2	0						
1 <u>6</u> k3	0						
™ k4	0						
1⋒ temp	x						
selected 1 [4:0] selected 1 [4:0]	xx			×	X.		
selected2[4:0]	xx			×			
■ selected2[4:0] ■ selected3[4:0]	xx				X		
selected4[4:0]	xx			×	×		
Ten A	1						
U _B B	1						
₩ c	1					<u> </u>	
U _d D	1						
■ ■ totaltickets[3:0]	f						
	000000000			00000	00000		
	00				0		
	00				0		
	257.575						
	00			0			
₩ witimer 4[4:0]	00			0	0		

RTL Schematic



Constraints:
*The CONSTRAINT file to implement code on zybo is shown below

```
1 set property -dict {PACKAGE_PIN L16 IOSTANDARD LVCMOS33 } [get ports {CLK}];
 3 set_property -dict {PACKAGE_PIN Y16 IOSTANDARD LVCMOS33 } [get_ports {way1}];
 4 set_property -dict {PACKAGE_PIN V16 IOSTANDARD LVCMOS33 } [get_ports {way2}];
 5 set property -dict {PACKAGE_PIN P16 IOSTANDARD LVCMOS33 } [get ports {way3}];
 6 set property -dict {PACKAGE_PIN R18 IOSTANDARD LVCMOS33 } [get_ports {way4}];
 9 set property -dict {PACKAGE PIN N15 IOSTANDARD LVCMOS33 } [get ports {ticket12}];
10 set_property -dict {PACKAGE_PIN L14 IOSTANDARD LVCMOS33 } [get_ports {ticket11}];
12 set_property -dict {PACKAGE_PIN K16 IOSTANDARD LVCMOS33 } [get_ports {ticket22}];
13 set property -dict {PACKAGE_PIN K14 IOSTANDARD LVCMOS33 } [get ports {ticket21}];
15 set_property -dict {PACKAGE_PIN N16 IOSTANDARD LVCMOS33 } [get_ports {ticket32}];
16 set property -dict {PACKAGE_PIN L15 IOSTANDARD LVCMOS33 } [get_ports {ticket31}];
18 set property -dict {PACKAGE_PIN J16 IOSTANDARD LVCMOS33 } [get ports {ticket42}];
19 set_property -dict {PACKAGE_PIN J14 IOSTANDARD LVCMOS33 } [get_ports {ticket41}];
21 set property -dict {PACKAGE_PIN T14 IOSTANDARD LVCMOS33 } [get ports {a}];
                                                                                  //JD port
22 set property -dict {PACKAGE_PIN T15 IOSTANDARD LVCMOS33 } [get ports {b}];
23 set property -dict {PACKAGE_PIN P14 IOSTANDARD LVCMOS33 } [get ports {c}];
24 set property -dict {PACKAGE_PIN R14 IOSTANDARD LVCMOS33 } [get_ports {d}];
25 set property -dict {PACKAGE PIN U14 IOSTANDARD LVCMOS33 } [get ports {e}];
26 set property -dict {PACKAGE_PIN U15 IOSTANDARD LVCMOS33 } [get_ports {f}];
27 set property -dict {PACKAGE_PIN V17 IOSTANDARD LVCMOS33 } [get ports {g}];
29 set property -dict {PACKAGE PIN T20 IOSTANDARD LVCMOS33 } [get ports {active1}]; // JB port 1st row
30 set_property -dict {PACKAGE_PIN U20 IOSTANDARD LVCMOS33 } [get_ports {avail1}];
31 set property -dict {PACKAGE_PIN V20 IOSTANDARD LVCMOS33 } [get ports {success1}];
33 set property -dict {PACKAGE_PIN Y18 IOSTANDARD LVCMOS33 } [get ports {active2}]; // JB port 2st row
34 set_property -dict {PACKAGE_PIN Y19 IOSTANDARD LVCMOS33 } [get_ports {avail2}];
35 set_property -dict {PACKAGE_PIN W18 IOSTANDARD LVCMOS33 } [get_ports {success2}];
37 set property -dict {PACKAGE_PIN V15 IOSTANDARD LVCMOS33 } [get_ports {active3}]; // JC port 1st row
38 set property -dict {PACKAGE_PIN W15 IOSTANDARD LVCMOS33 } [get ports {avail3}];
39 set property -dict {PACKAGE_PIN T11 IOSTANDARD LVCMOS33 } [get ports {success3}];
41 set property -dict {PACKAGE_PIN W14 IOSTANDARD LVCMOS33 } [get_ports {active4}]; // JC port 4st row
42 set_property -dict {PACKAGE_PIN Y14 IOSTANDARD LVCMOS33 } [get_ports {avail4}];
43 set property -dict {PACKAGE_PIN T12 IOSTANDARD LVCMOS33 } [get ports {success4}];
45
46 set_property -dict {PACKAGE_PIN G15 IOSTANDARD LVCMOS33 } [get_ports {reset}];
```

RESULTS:

For each user 3 led lights are provided

LED1 - Shows whether user is active or not

LED2 - Shows whether the selected number of tickets by the user are available or not

LED3 - Confirms the payment of the user

Led Glowing Conditions for each user are as follows.

SL.NO ACTIVE[LED1] AVAIL[LED2] SUCCESS[LED3]

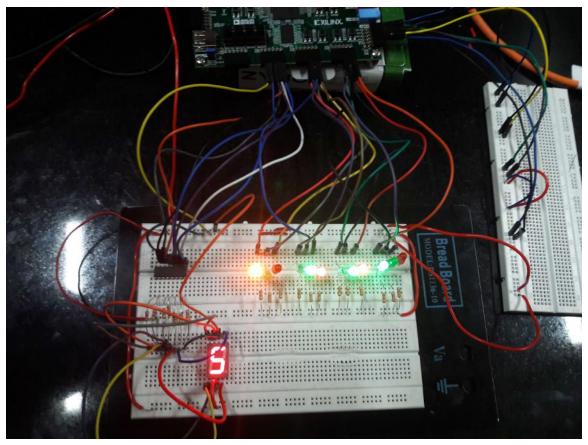
_	_	-	_

USER1	USER1 IS ACTIVE	INPUT TICKETS PAYMENT OF OF USER1 USER1
		ARE AVAILABLE SUCCESSFULL
USER2	USER2 IS ACTIVE	INPUT TICKETS PAYMENT OF
		OF USER2 USER2 ARE AVAILABLE SUCCESSFULL
USER3	USER3 IS ACTIVE	INPUT TICKETS PAYMENT OF
OOLING	OOLNO IO AOTIVE	OF USER3 USER3
		ARE AVAILABLE SUCCESSFULL
USER4	USER4 IS ACTIVE	INPUT TICKETS PAYMENT OF
		OF USER4 USER4
		ARE AVAILABLE SUCCESSFULL

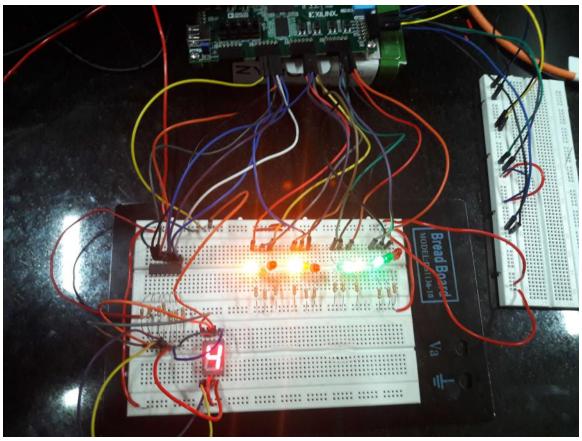
CONCLUSIONS:

Snapshots of Hardware Implementation

^{*}In this way if one user entered the website and made the payment then another user can enter the website through the same way like this 4 ways are provided in which users come and go irrespective of other 3 ways.

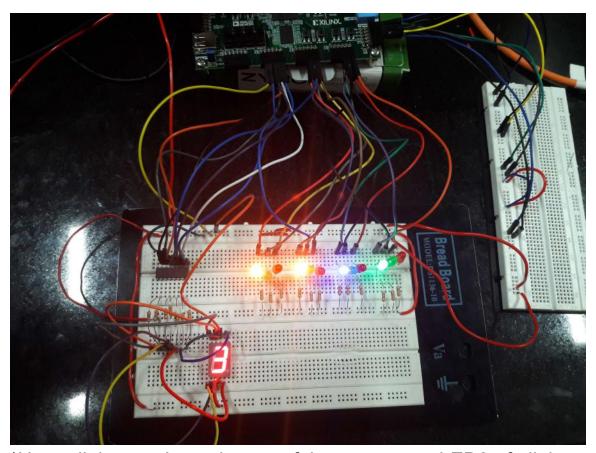


*Here user1,user4 made sucessfull payment so LED3 corresponding to user1,user4 are glowing *user2,user3 are in active state and selected tickets by both the users are so LED1,LED2 of both the users are glowing.



*Here user1,3,4 made sucessful payment so LED3 is glowing for user's1,3,4.

^{*}user2 is in active state and selected number of tickets are available so LED1,2 f user2 are glowing.



*Here all the user's made sucesful payments so LED3 of all the user's are glowing.

By this way we have implemented ticket reservation system on zybo.