





GND

**SI LAB**  
Silizium Labor Bonn  
Rev 1.0 HK 1/2022

UNI  
BONN

Embedded  
System  
Lab

SAR-ADC

VDAC

COMP

ADC

R4

U2

C1

U3

R3

R5

R2

C5

C7

R1

TSV

ADC IN

D7

D6

D5

D4

D3

D2

D1

D0

C11

RAMP

U4

VREF

C3

C4

T1

C9

U6

R32

C8

U7

RAMP OUT

SPI

ON

R30

R36

D9

D8

R31

RAMP

R35