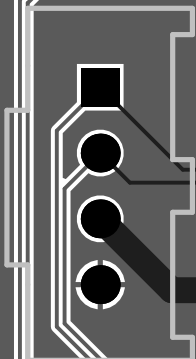




Embedded
System
Lab

SMU



I2C



ON

GND



$U_{out} = 0..4\text{ V}$
 $I_{max} = 50\text{ mA}$

OUT1

INTERNAL NMOS

OUT2