3V3 5V

C64
100n
1005

C65
100n
1005

J8

(SDA1)	GPIO2	1	2		
(SCL1)	GPIO3	3	4		
(GPIO_GCLK)	GPIO4	5	6		
		7	8	GPIO14	(TXD0)
		9	10	GPIO15	(RXD0)
(GPIO_GEN0)	GPIO17	11	12	GPIO18	(GPIO_GEN1)
(GPIO_GEN2)	GPIO27	13	14		
(GPIO_GEN3)	GPIO22	15	16	GPIO23	(GPIO_GEN4)
		17	18	GPIO24	(GPIO_GEN5)
(SPI_MOSI)	GPIO10	19	20		
(SPI_MISO)	GPIO9	21	22	GPIO25	(GPIO_GEN6)
(SPI_SCLK)	GPIO11	23	24	GPIO8	(SPI_CE0_N)
		25	26	GPIO7	(SPI_CE1_N)
	ID_SD	27	28	ID_SC	
	GPIO5	29	30		
	GPIO6	31	32	GPIO12	
	GPIO13	33	34		
	GPIO19	35	36	GPIO16	
	GPIO26	37	38	GPIO20	
		39	40	GPIO21	

40W 0.1" PIN HDR

ID_SD and ID_SC PINS:

These pins are reserved for HAT ID EEPROM.

At boot time this I2C interface will be
interrogated to look for an EEPROM
that identifes the attached board and
allows automagic setup of the GPIOs
(and optionally, Linux drivers).

*DO NOT USE these pins for anything other
than attaching an I2C ID EEPROM. Leave
unconnected if ID EEPROM not required.*

GPIO EXPANSION