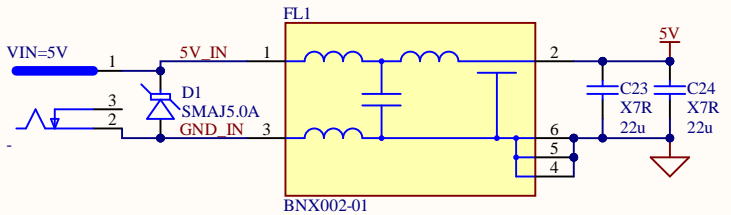
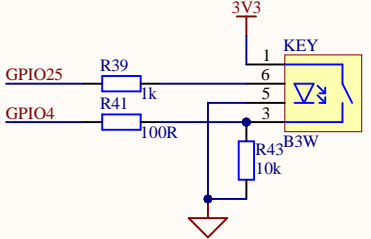
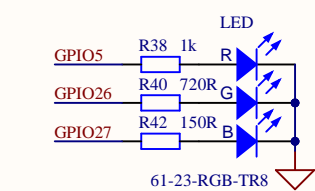
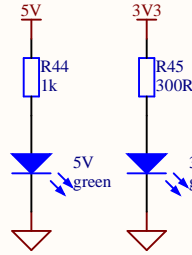
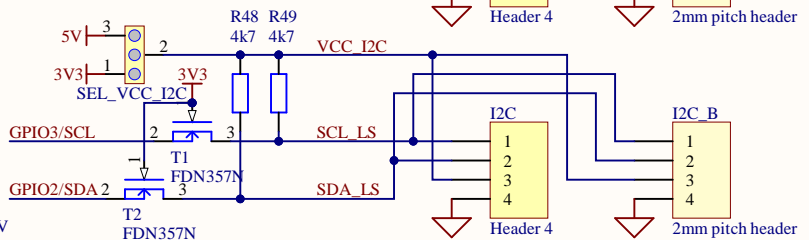
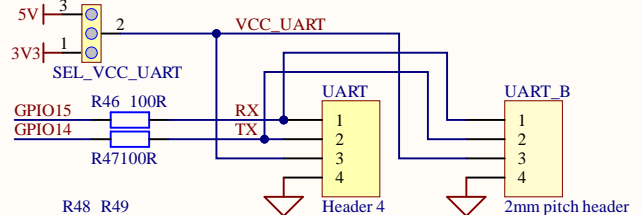
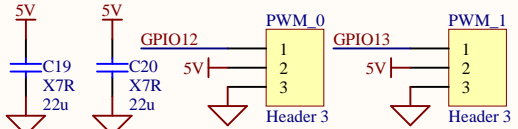
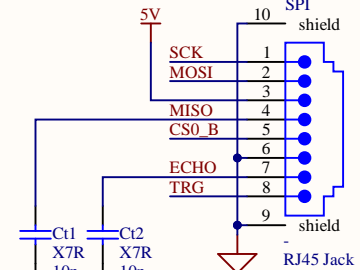
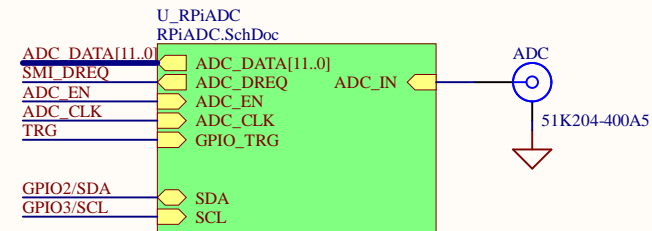


SMI_OE_B

SMI_DATA4 / PWM0
SMI_DATA5 / PWM1
SMI_DATA6 / UART_TX
SMI_DATA7 / UART_RX
SMI_DATA8
SMI_DATA9
SMI_DATA10
SMI_DATA11
SMI_DATA12
SMI_DATA13
SMI_DATA14
SMI_DATA15
SMI_DREQ



FID1 FID2 FID3
✖ ✖ ✖



Title: Raspberry Pi Base Board

Project: Embedded System Lab

Date: 22/03/2022 Time: 15:31:14

File: RPiLabBaseBoard.SchDoc

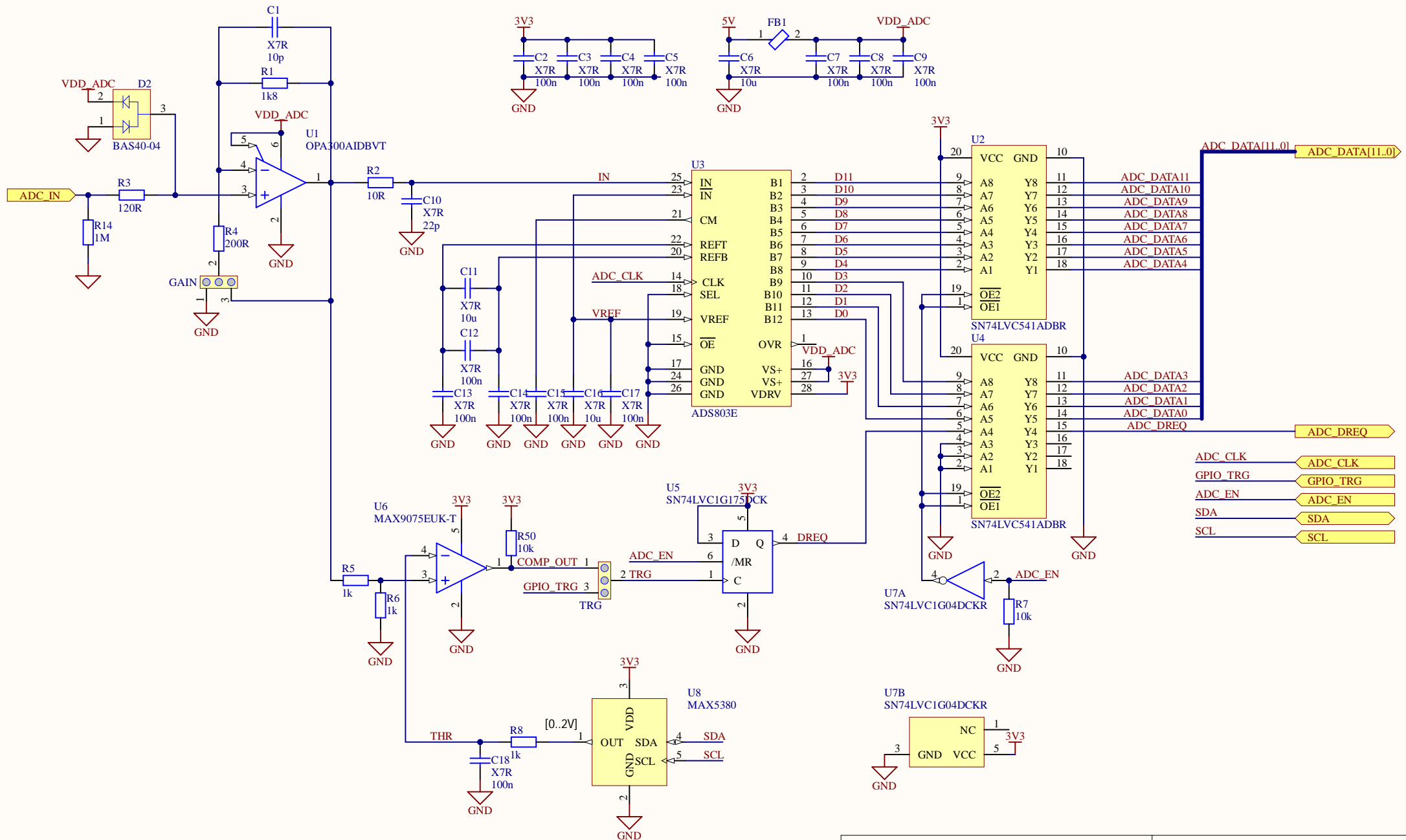
Drawn by: HK


Revision: 1.0

Sheet 1 of 2

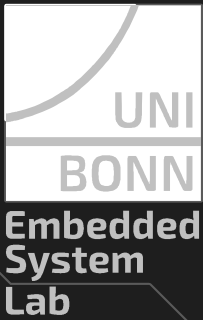


Physikalisches Institut
Universität Bonn
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SI LAB
Silizium Labor Bonn



**UNI
BONN**
**Embedded
System
Lab**

VIN=5V

5V ☐ R44

3.3V ☐ R45

C24 C23

FL1

Rev 1.0 HK 1/2022

×

×

×

×

R42 R38 R40

LED

U3

TRG

GPIO INT

C4 C8 C17 C16 C13 C15 C14 C11 C12 R3 R4 R14 R34 R35 R36 R37 R38 R39 R40 R41 R42 R43 R44 R45 R46 R47 R48 R49

GAIN

1 10

U1 U2 U3 U4 U5 U6 U7 U8

ADC

VCC

2/SDA

3/SCL

4

5

6

7/CS1_B

8/CS0_B

9/SDI

10/SDO

11/SCK

12/PWM0

13/PWM1

14/TX

15/RX

16

17

18

19

20

21

22

23

24

25

26

27

ID_SD

ID_SC

GND

GPIO

SPI

5V

GND

0 1

PWM

RX

TX

VCC

GND

UART

SCL

SDA

VCC

GND

I2C

R46 R47

5V

3.3V

SEL_VCC_UART

R48 R49

5V

3.3V

SEL_VCC_I2C

KEY