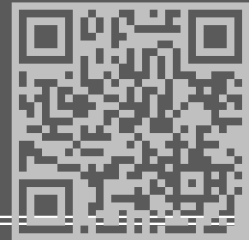


SI LAB
Silizium Labor Bonn
LF SFF AC/DC Rev. 1.0
AC DC



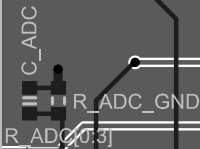
CON1

PW_BIAS

BACK_BIAS

DIODE_HV

R_PIX_INPUT



NWELL_RING LOGIC NWELL

PIXEL OUT0 PIXEL OUT1 PIXEL OUT2 PIXEL OUT3 PIXEL SUM

0	0	0	0	0	0
1	1	1	1	1	1
2	2	2	2	2	2
3	3	3	3	3	3
4	4	4	4	4	4
5	5	5	5	5	5
6	6	6	6	6	6
7	7	7	7	7	7
8	8	8	8	8	8
9	9	9	9	9	9
10	10	10	10	10	10
11	11	11	11	11	11
12	12	12	12	12	12
13	13	13	13	13	13
14	14	14	14	14	14
15	15	15	15	15	15

OUT0
ADCLEMO

OUT1
ADCLEMO

OUT2
ADCLEMO

OUT3
ADCLEMO

SUM
ADCLEMO

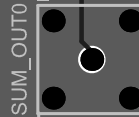
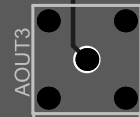
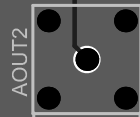
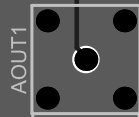
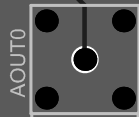
R_AOUT0

R_AOUT1

R_AOUT2

R_AOUT3

R_SUMOUT



PIXEL Arrangement

3	7	11	15
2	6	10	14
1	5	9	13
0	4	8	12

