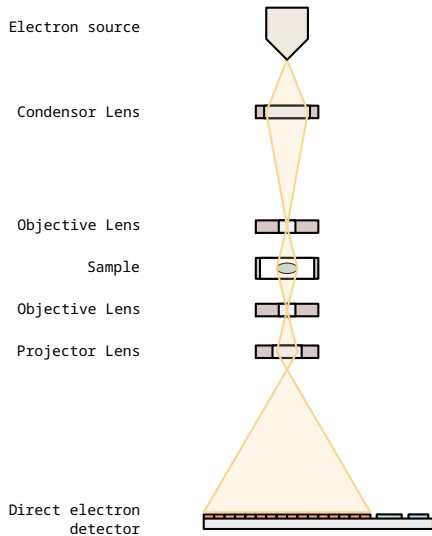


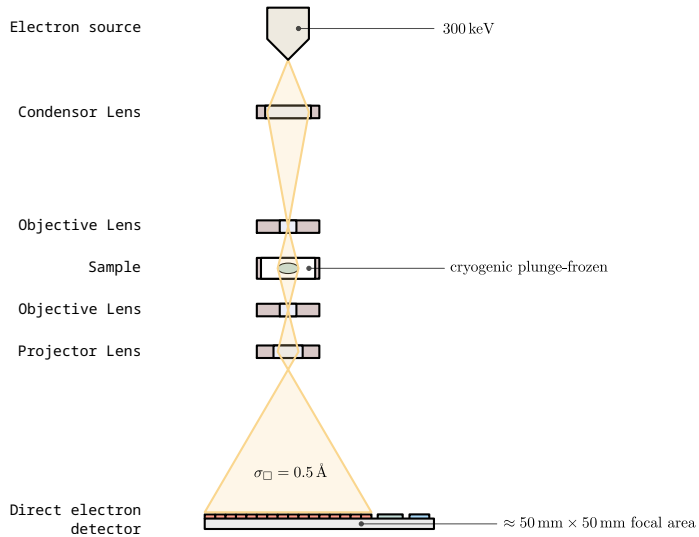
Data converters for high frame rate imaging detectors

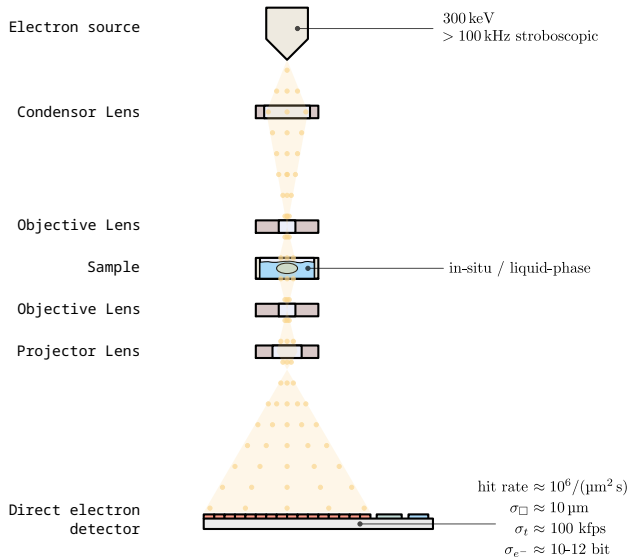
Kennedy Caisley¹, Hans Krüger¹, Jochen Dingfelder¹, Bart Dierickx²

1. University of Bonn, DE

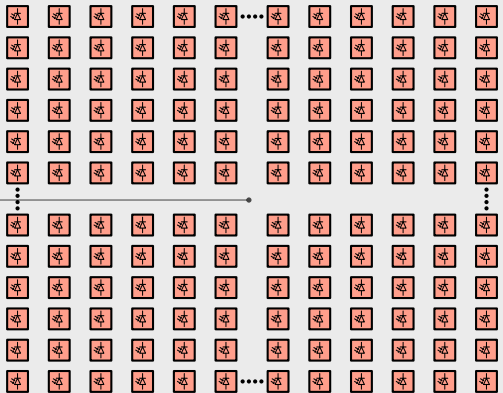
2. Caeleste, Mechelen, BE







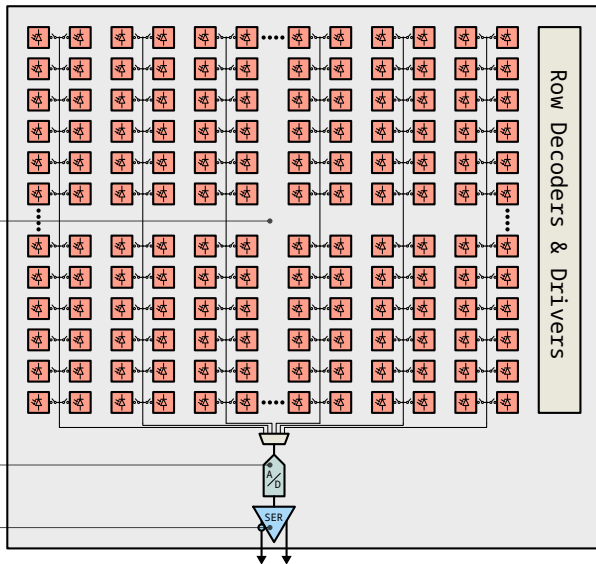
Row Decoders & Drivers



$\approx 600 \mu\text{m}^2$ reticle limit
 $\approx 15 \mu\text{m}$ pitch
 ≈ 1 Mpixel array
2-side buttable

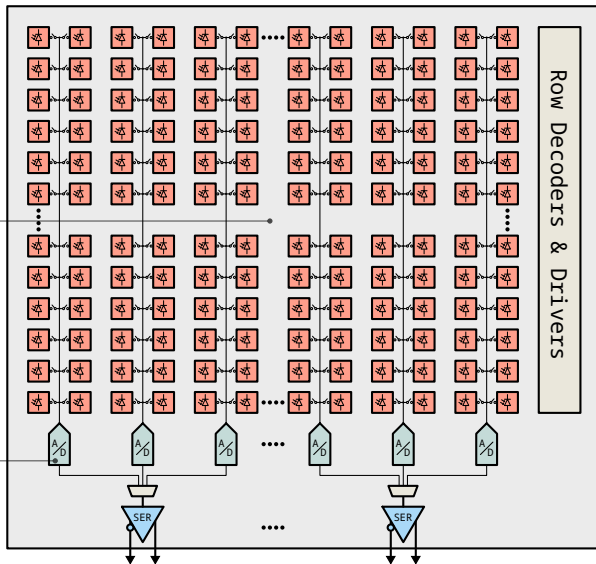
$\approx 600 \mu\text{m}^2$ reticle limit
 $\approx 15 \mu\text{m}$ pitch
 ≈ 1 Mpixel array
2-side buttable

$\approx 60 \text{ mm}^2$
 $\approx 1 \text{ W}$
10-12 bit
 $\approx 100 \text{ Gbps}$
 1 Tb/s



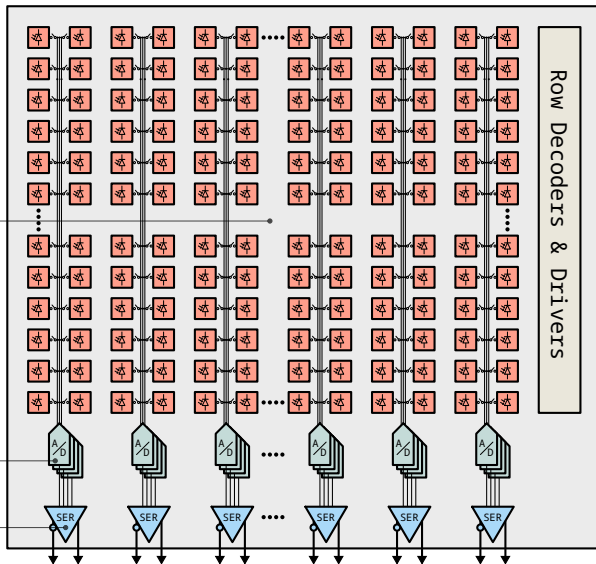
$\approx 600 \mu\text{m}^2$ reticle limit
 $\approx 15 \mu\text{m}$ pitch
 ≈ 1 Mpixel array
2-side buttable

$\approx 120\,000 \mu\text{m}^2$
 ≈ 1 mW
10-12 bit
 ≈ 200 Msp/s

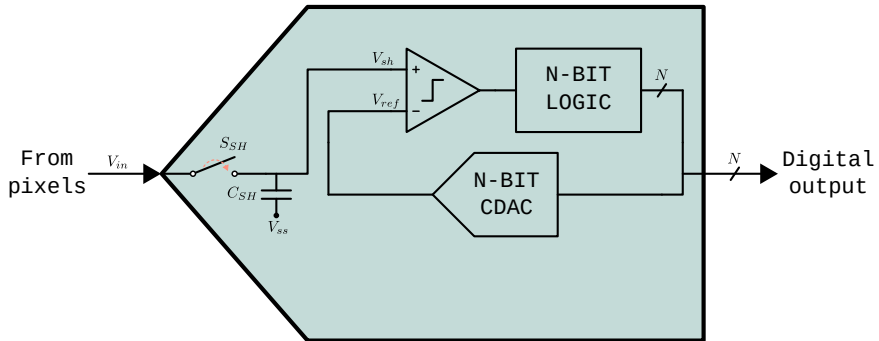


$\approx 600 \mu\text{m}^2$ reticle limit
 $\approx 15 \mu\text{m}$ pitch
 ≈ 1 Mpixel array
2-side buttable

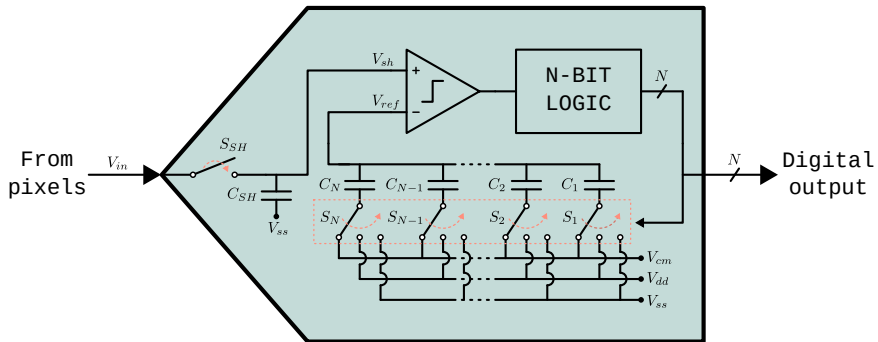
$\approx 7500 \mu\text{m}^2$
 $\approx 100 \mu\text{W}$
10-12 bit
 ≈ 10 Msp/s
5 Gb/s



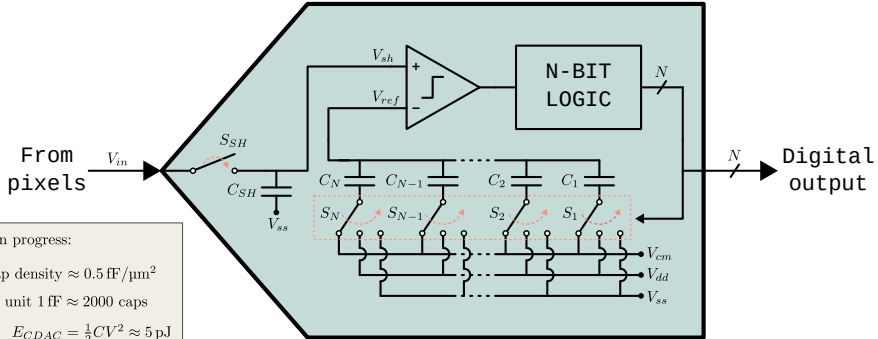
SAR ADC



SAR ADC



SAR ADC



Work in progress:

MOM cap density $\approx 0.5 \text{ fF}/\mu\text{m}^2$

@ unit $1 \text{ fF} \approx 2000 \text{ caps}$

$$E_{CDAC} = \frac{1}{2} CV^2 \approx 5 \text{ pJ}$$

Non binary-weighting