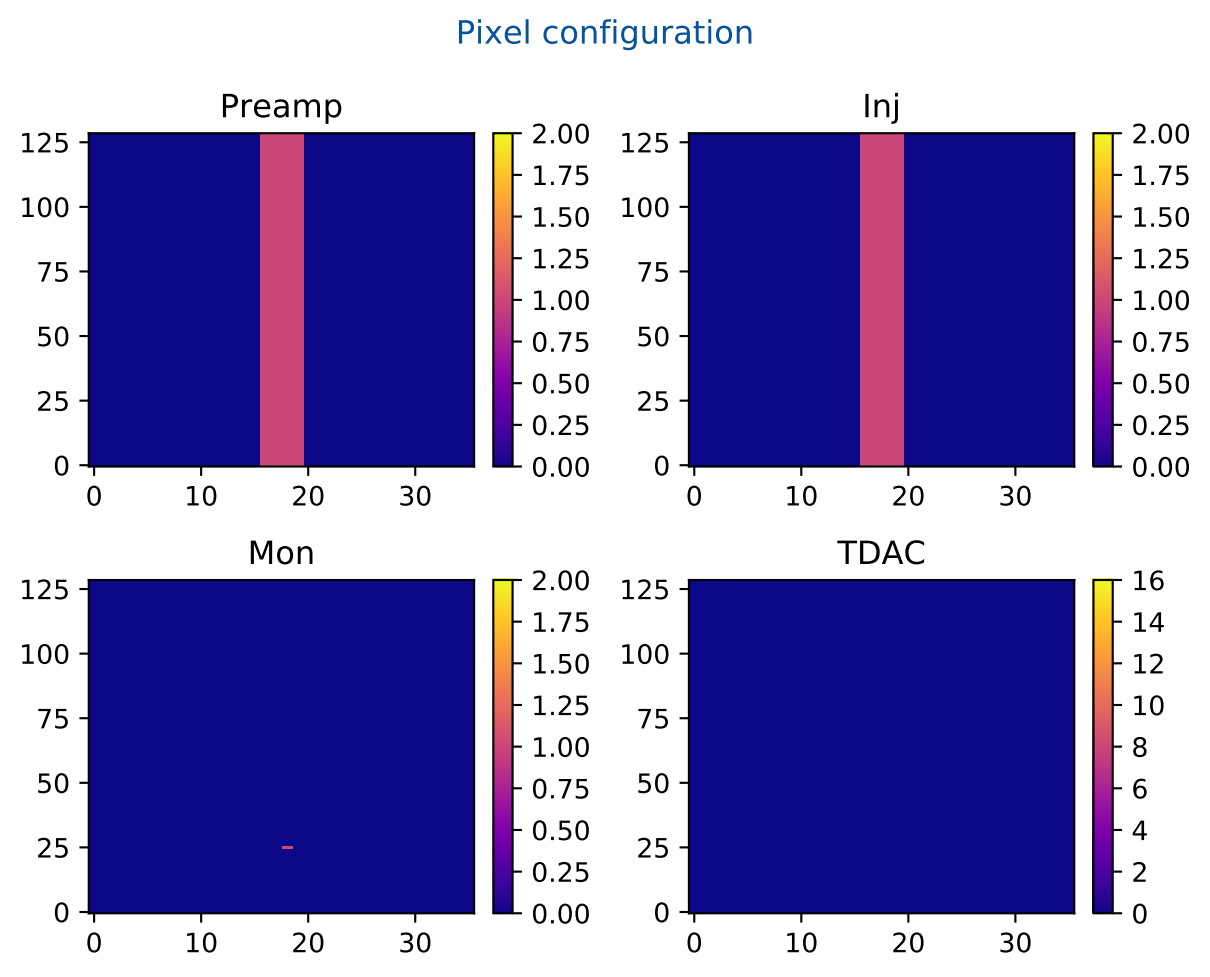
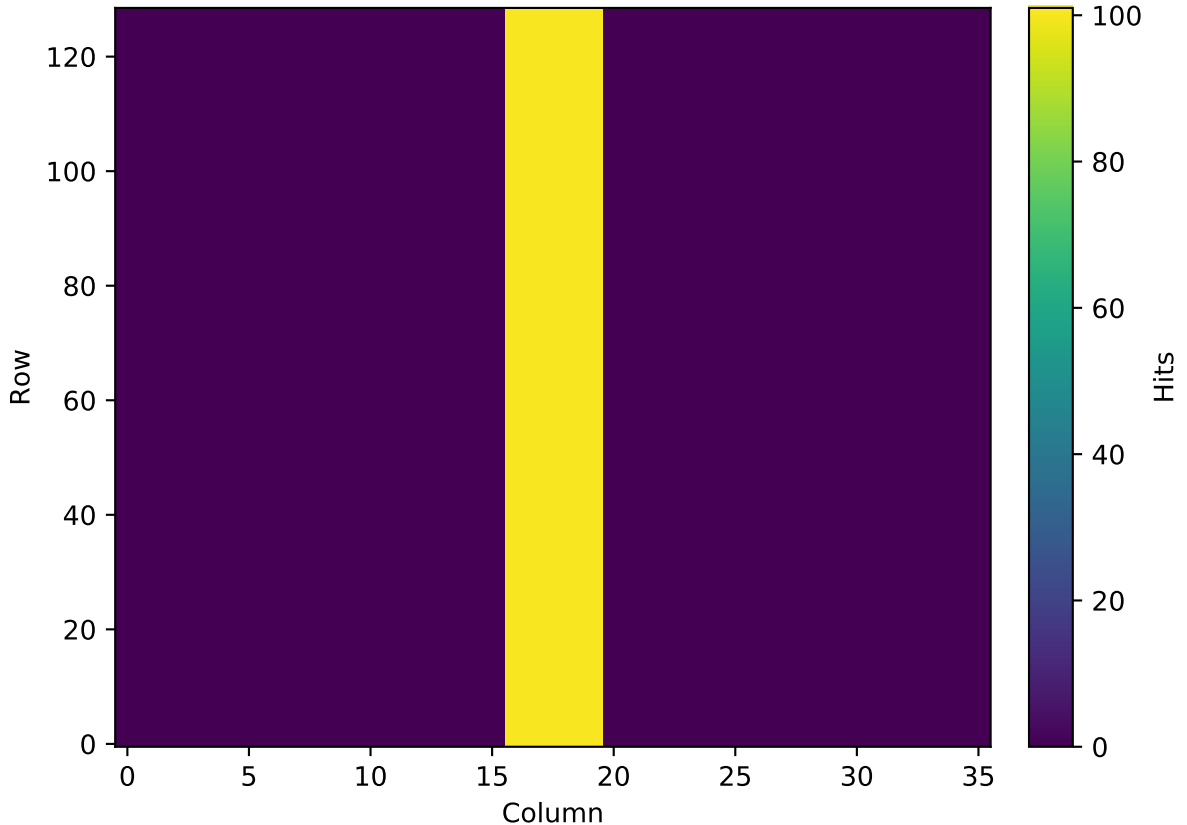
Chip configuration

Param	Value	Param	Value	Param	Value
BLRes	32	VCascC[V]	0.79974355	VPLoad	11
BL[V]	0.750341831166	VCascC[mA]	0.00150668956605	Vbias_CS	32
BL[mA]	-0.0290859152379	VCascCset	0.8	Vfs	32
BLset	0.75	VCascN[V]	0.40083772064	Vsf_dis1	10
IBOTA	32	VCascN[mA]	-0.0649969924948	Vsf_dis2	20
IComp	32	VCascNset	0.4	Vsf_dis3	30
ILVDS	32	VDDA[V]	1.79389031936	inj_delay	5000
INJ_HIset	1.8000000000000003	VDDA[mA]	4.11210213818	inj_n	100
INJ_LOset	0.1	VDDAset	1.8	inj_width	5000
LSBdacL	32	VDDD[V]	1.795278124		
NTC[V]	0.0451240976316	VDDD[mA]	1.99788805211		
NTC[mA]	0.00468387075	VDDDset	1.8		
NTCset	5	VDD_BCID_BUFF[V]	1.69367936911		
PBias[V]	1.82364750071	VDD_BCID_BUFF[mA]	6.47736804075		
PBias[mA]	-0.000619123864204	VDD_BCID_BUFFset	1.7		
PBiasset	0.5	VPC[V]	1.49956992545		
TH[V]	0.831178568516	VPC[mA]	0.100395843308		
TH[mA]	-0.0517904415911	VPCset	1.5		
THset	0.83	VPFB	32		
VAmp	32	VPFoll	12		


Pixel configuration
Preamp
Inj
Mon
TDAC
0
10
20
30
0
25
50
75
100
125
0.00
0.25
0.50
0.75
1.00
1.25
1.50
1.75
2.00
0
2
4
6
8
10
12
14
16


Hit Occupancy (Σ = 51600.0)
Row
Column
Hits
0
20
40
60
80
100
120
0
5
10
15
20
25
30
35