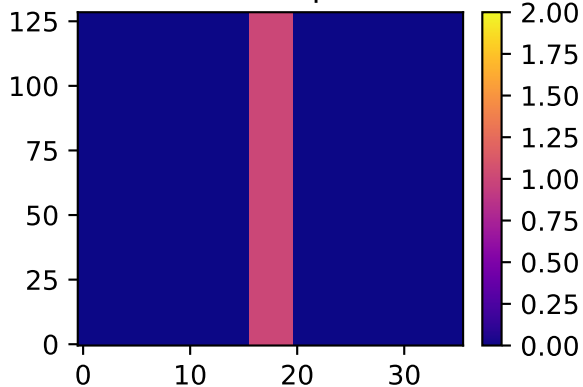


# Chip configuration

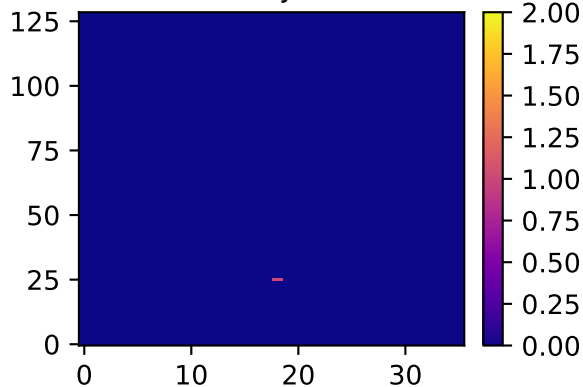
Param	Value	Param	Value	Param	Value
BLRes	32	VCascC[V]	0.79974355	VPLoad	11
BL[V]	0.750341831166	VCascC[mA]	-0.0934160325527	Vbias_CS	32
BL[mA]	-0.0591747724086	VCascCset	0.8	Vfs	32
BLset	0.75	VCascN[V]	0.40083772064	Vsf_dis1	10
IBOTA	32	VCascN[mA]	-0.0259988091076	Vsf_dis2	20
IComp	32	VCascNset	0.4	Vsf_dis3	30
ILVDS	32	VDDA[V]	1.79189372455		
INJ_Hiset	0.4	VDDA[mA]	7.2212525814		
INJ_LOset	0.1	VDDAset	1.8		
LSBdacl	45	VDDD[V]	1.794279776		
NTC[V]	0.0421243156842	VDDD[mA]	3.29651532563		
NTC[mA]	0.0051886184	VDDDset	1.8		
NTCset	5	VDD_BCID_BUFF[V]	1.68968554764		
PBias[V]	1.82114963245	VDD_BCID_BUFF[mA]	12.0438562526		
PBias[mA]	-0.000619123864204	VDD_BCID_BUFFset	1.7		
PBiasset	0.5	VPC[V]	1.49956992545		
TH[V]	0.770252841579	VPC[mA]	0.100395843308		
TH[mA]	-0.00995971664146	VPCset	1.5		
THset	0.769	VPFB	32		
VAmp	32	VPFoll	12		

## Pixel configuration before tuning

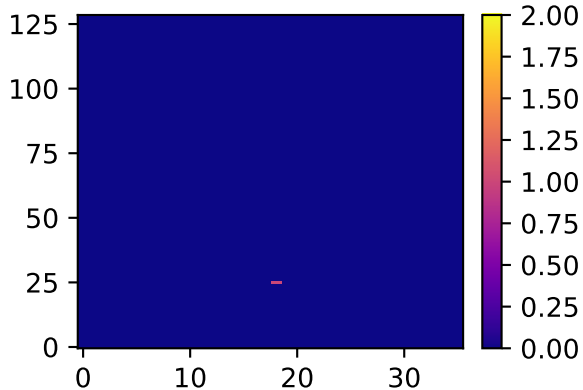
Preamp



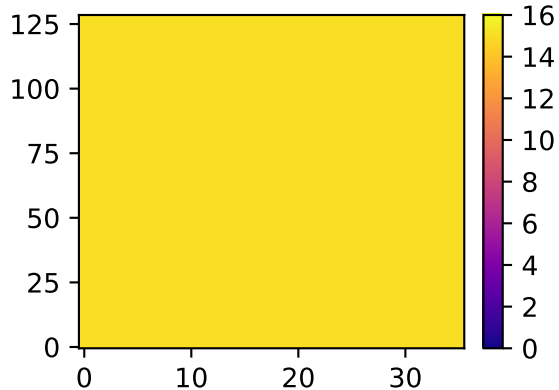
Inj



Mon

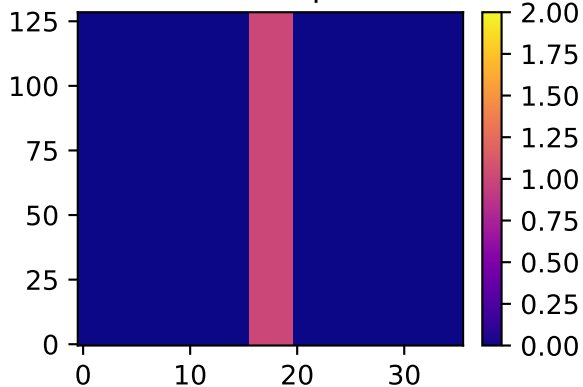


TDAC

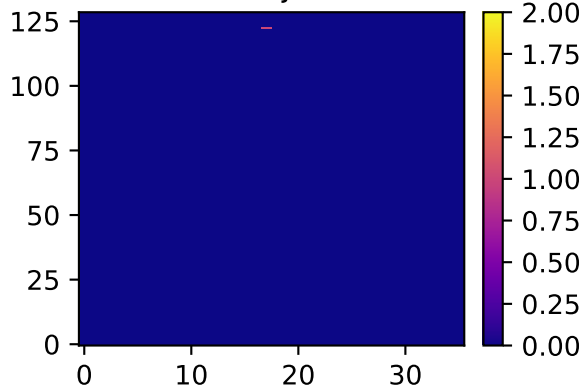


## Pixel configuration

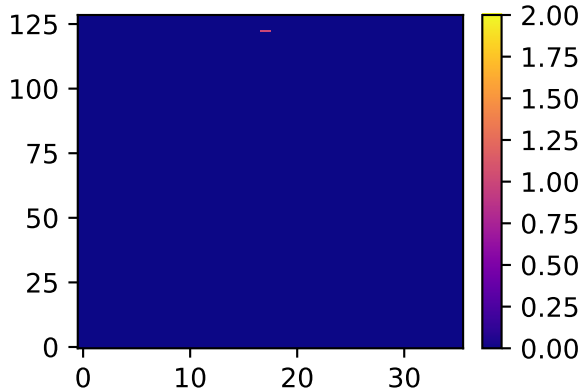
Preamp



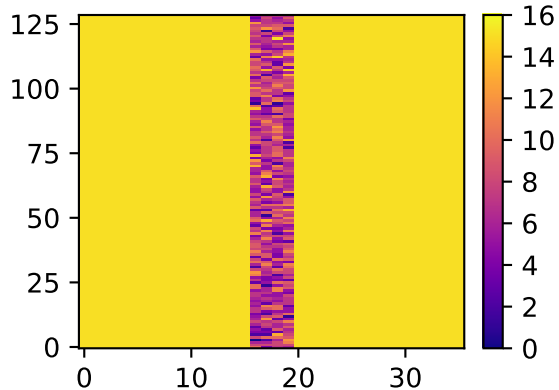
Inj



Mon



TDAC



TDAC distribution

