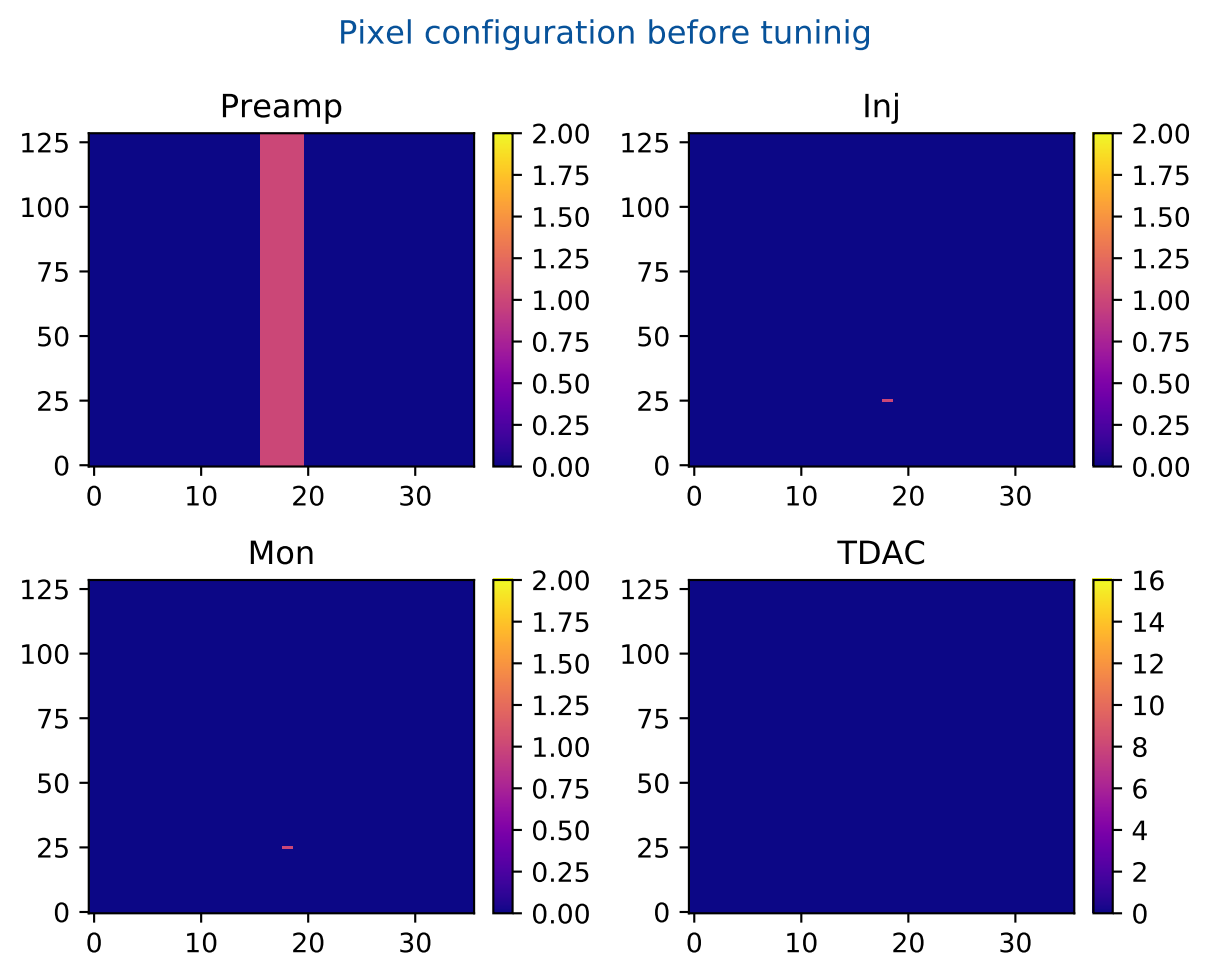
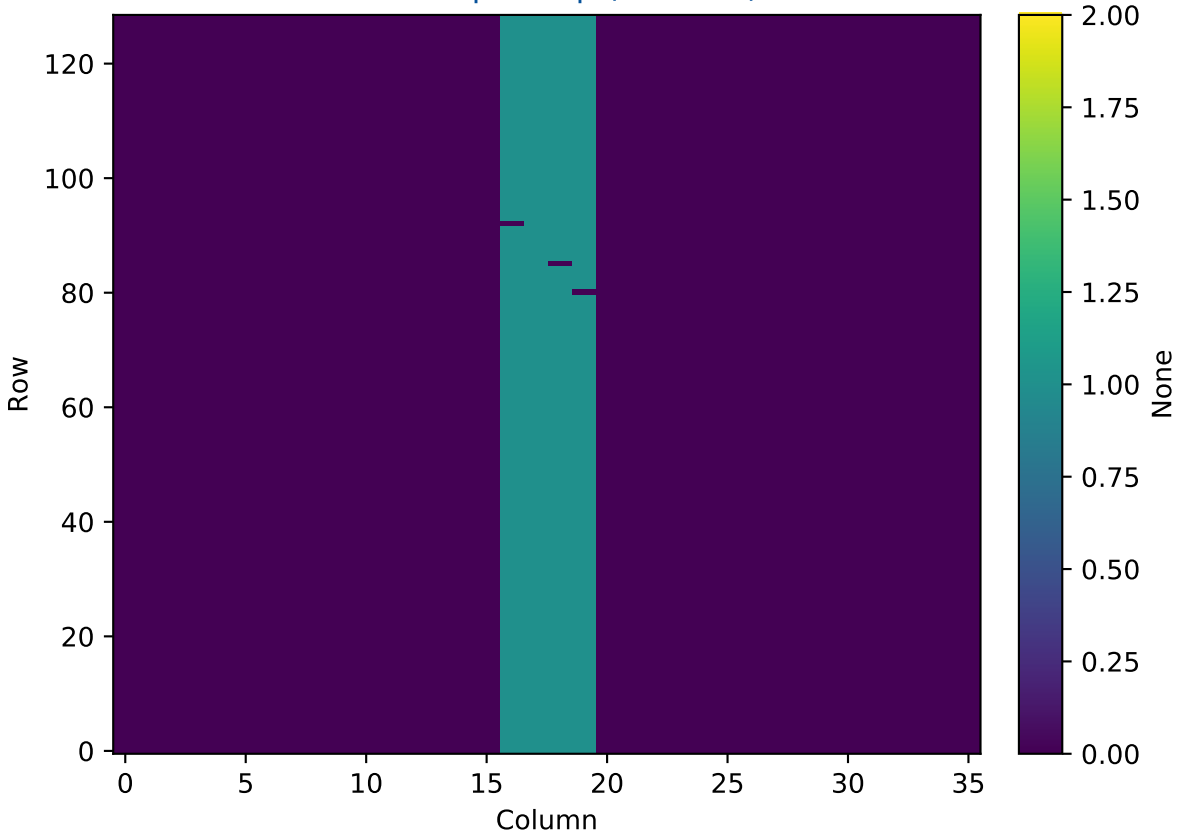
Chip configuration

Param	Value	Param	Value	Param	Value
BLRes	32	VCascC[V]	0.79974355	VPLoad	11
BL[V]	0.750341831166	VCascC[mA]	0.0241073376896	Vbias_CS	32
BL[mA]	-0.209117577309	VCascCset	0.8	Vfs	32
BLset	0.75	VCascN[V]	0.40083772064	Vsf_dis1	10
IBOTA	32	VCascN[mA]	-0.0319985296287	Vsf_dis2	20
IComp	32	VCascNset	0.4	Vsf_dis3	30
ILVDS	32	VDDA[V]	1.79289202196		
INJ_HIset	0.5	VDDA[mA]	5.71682494758		
INJ_LOset	0.1	VDDAset	1.8		
LSBdacL	32	VDDD[V]	1.795278124		
NTC[V]	0.0436242066579	VDDD[mA]	2.49736008039		
NTC[mA]	0.0051886184	VDDDset	1.8		
NTCset	5	VDD_BCID_BUFF[V]	1.69367936911		
PBias[V]	1.82364750071	VDD_BCID_BUFF[mA]	6.47736804075		
PBias[mA]	-0.000619123864204	VDD_BCID_BUFFset	1.7		
PBiasset	0.5	VPC[V]	1.49956992545		
TH[V]	0.791726663368	VPC[mA]	0.100395843308		
TH[mA]	-0.0333650032204	VPCset	1.5		
THset	0.7905	VPFB	32		
VAmp	32	VPFoll	12		


Pixel configuration before tuninig
Preamp
Inj
Mon
TDAC
0
10
20
30
0
25
50
75
100
125
0.00
0.25
0.50
0.75
1.00
1.25
1.50
1.75
2.00
0
2
4
6
8
10
12
14
16


Enabled preamp (Σ = 513)
Row
Column
None
0
20
40
60
80
100
120
0
5
10
15
20
25
30
35
0.00
0.25
0.50
0.75
1.00
1.25
1.50
1.75
2.00