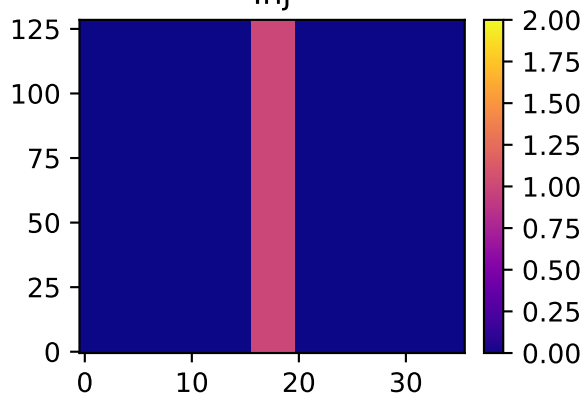
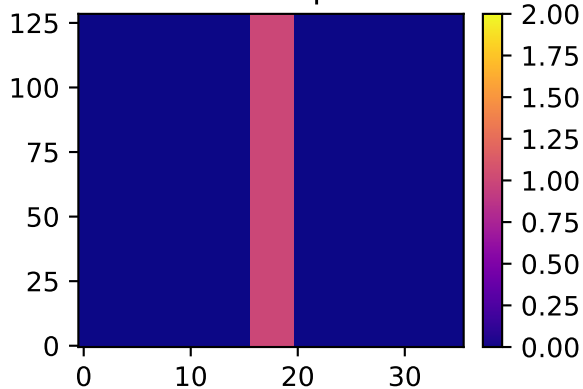


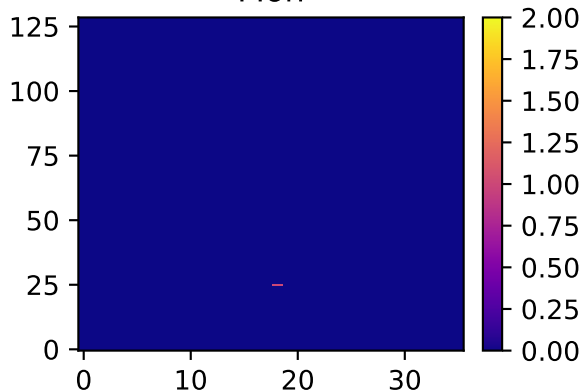
Chip configuration before scan

Param	Value	Param	Value	Param	Value
BLRes	32	VCascC[V]	0.79974355	VPLoad	11
BL[V]	0.750341831166	VCascC[mA]	0.00853800231559	Vbias_CS	32
BL[mA]	-0.110827310552	VCascCset	0.8	Vfs	32
BLset	0.75	VCascN[V]	0.40083772064	Vsf_dis1	10
IBOTA	32	VCascN[mA]	-0.0514976213223	Vsf_dis2	20
IComp	32	VCascNset	0.4	Vsf_dis3	30
ILVDS	32	VDDA[V]	1.79389031936	inj_delay	5000
INJ_Hlset	1.5	VDDA[mA]	4.11210213818	inj_n	100
INJ_LOset	0.1	VDDAset	1.8	inj_width	5000
LSBdacL	32	VDDD[V]	1.796276472		
NTC[V]	0.0471239522632	VDDD[mA]	1.99788805211		
NTC[mA]	0.0051886184	VDDDset	1.8		
NTCset	5	VDD_BCID_BUFF[V]	1.69367936911		
PBIAS[V]	1.82364750071	VDD_BCID_BUFF[mA]	6.47736804075		
PBIAS[mA]	-0.000619123864204	VDD_BCID_BUFFset	1.7		
PBIASset	0.5	VPC[V]	1.49956992545		
TH[V]	0.81120292034	VPC[mA]	0.100395843308		
TH[mA]	-0.0124496407456	VPCset	1.5		
THset	0.81	VPFB	32		
VAmp	32	VPFoll	12		

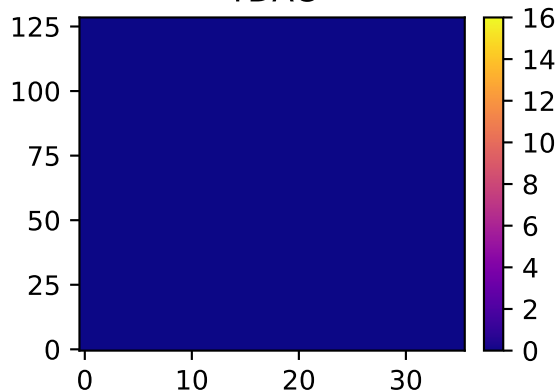
Preamp Pixel configuration before scan Inj



Mon



TDAC



Hit Occupancy ($\Sigma = 51600.0$)

