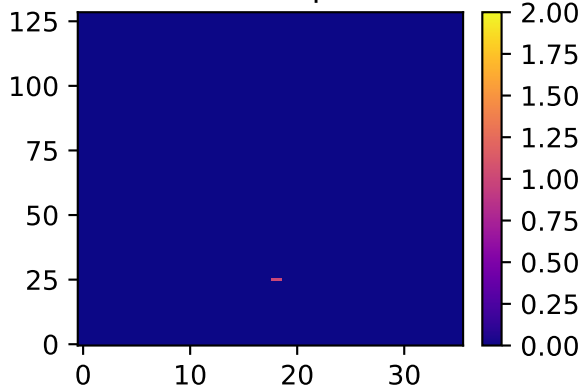


Chip configuration

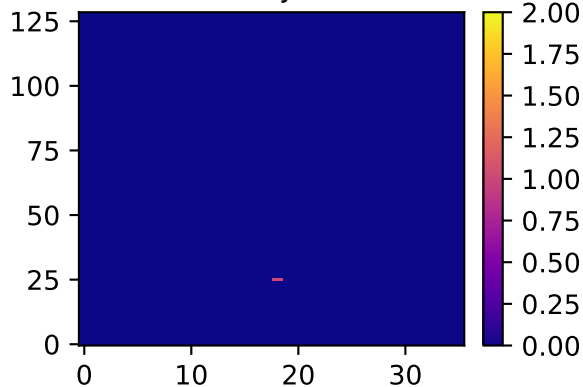
Param	Value	Param	Value	Param	Value
BLRes	32	VCascC[V]	0.79974355	VPLoad	11
BL[V]	0.750341831166	VCascC[mA]	0.0145648418152	Vbias_CS	32
BL[mA]	0.045634746736	VCascCset	0.8	Vfs	32
BLset	0.75	VCascN[V]	0.40083772064	Vsf_dis1	10
IBOTA	32	VCascN[mA]	-0.0319985296287	Vsf_dis2	20
IComp	32	VCascNset	0.4	Vsf_dis3	30
ILVDS	32	VDDA[V]	1.79389031936	inj_delay	5000
INJ_Hiset	2.850000000000001	VDDA[mA]	4.01180696259	inj_n	100
INJ_LOset	0.1	VDDAset	1.8	inj_width	5000
LSBdaCL	32	VDDD[V]	1.796276472		
NTC[V]	0.0461240249474	VDDD[mA]	1.99788805211		
NTC[mA]	0.00468387075	VDDDset	1.8		
NTCset	5	VDD_BCID_BUFF[V]	1.69467782447		
PBias[V]	1.82364750071	VDD_BCID_BUFF[mA]	6.47736804075		
PBias[mA]	-0.00112305991013	VDD_BCID_BUFFset	1.7		
PBiasset	0.5	VPC[V]	1.49956992545		
TH[V]	0.821190744428	VPC[mA]	0.100395843308		
TH[mA]	-0.00448188361234	VPCset	1.5		
THset	0.82	VPFB	32		
VAmp	32	VPFoll	12		

Pixel configuration

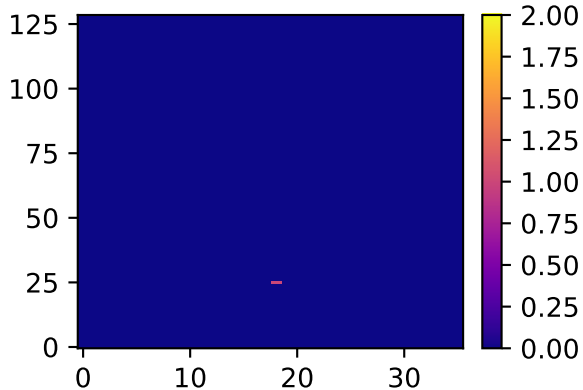
Preamp



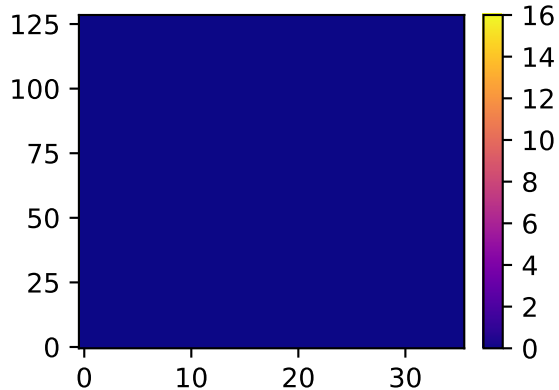
Inj



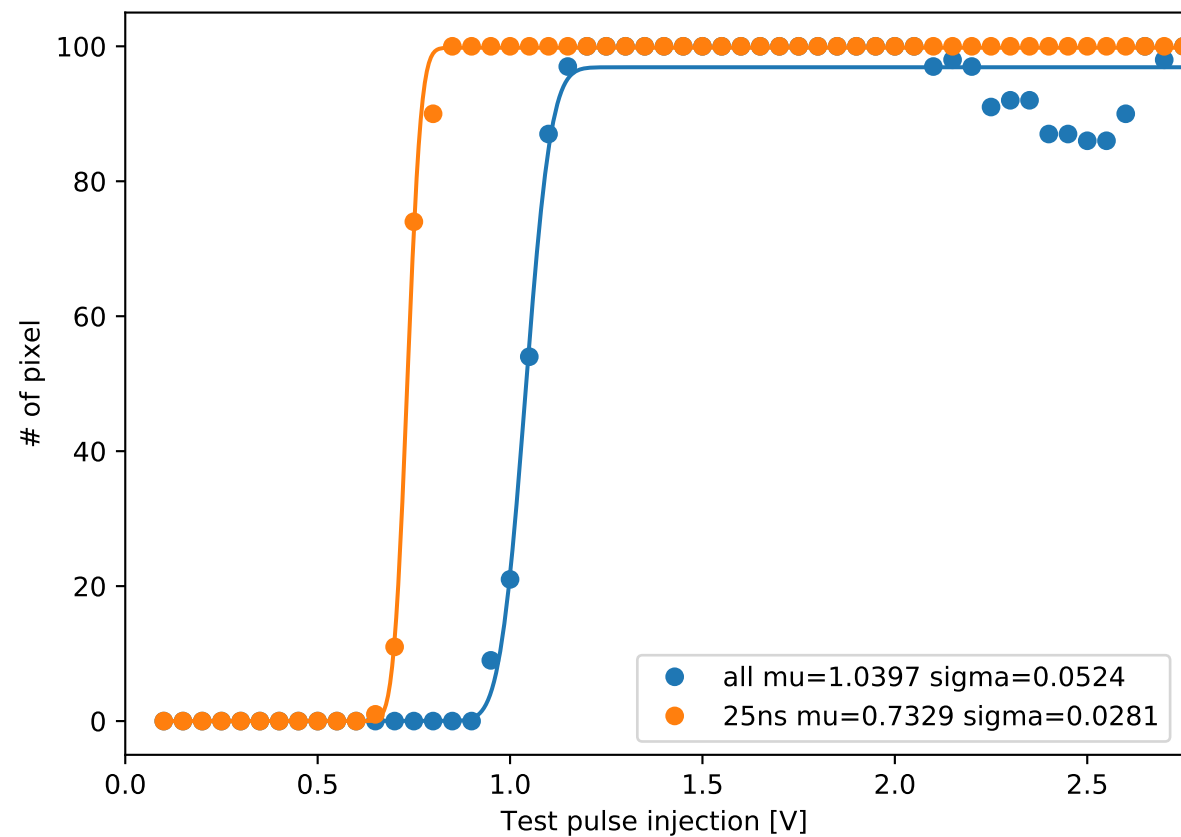
Mon



TDAC

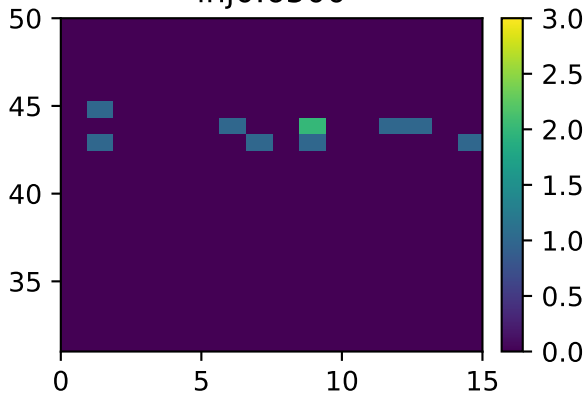


Phase=12 LE=41

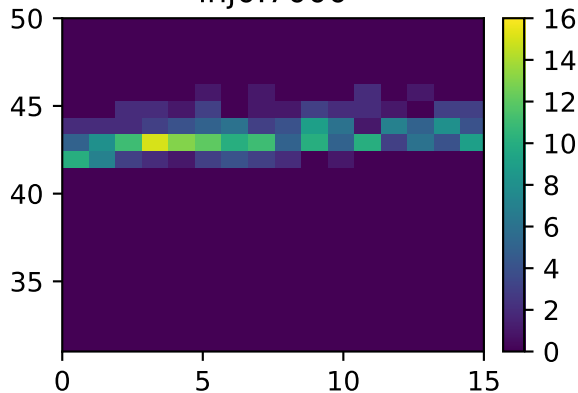


Pixel configurations

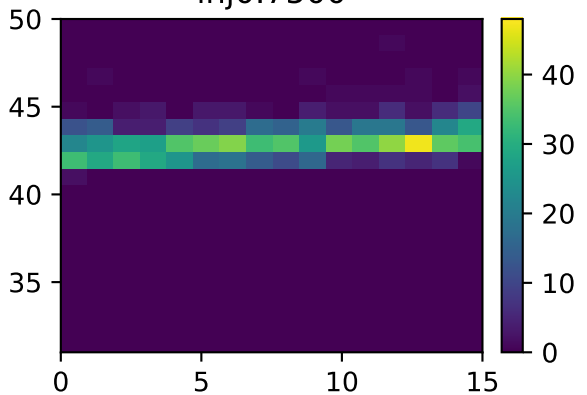
inj0.6500



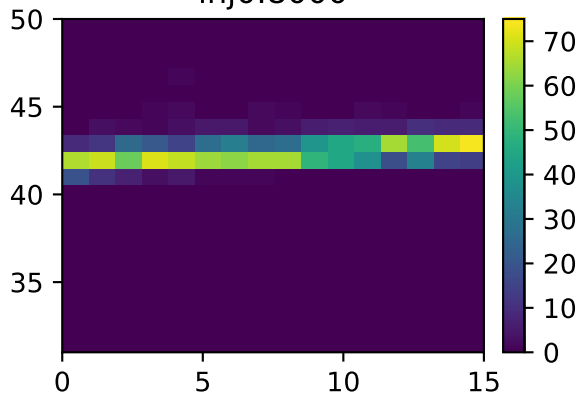
inj0.7000



inj0.7500

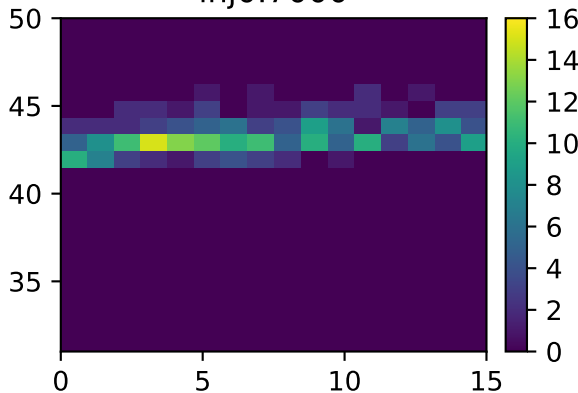


inj0.8000

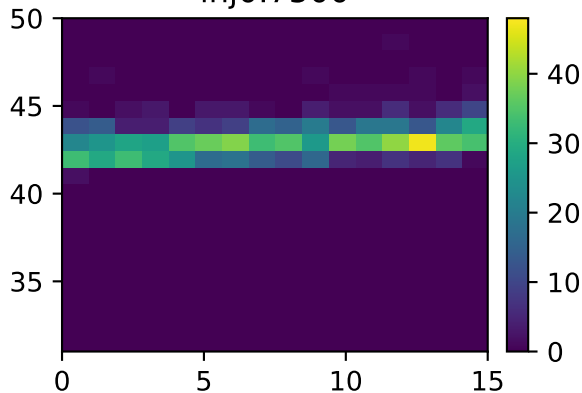


Pixel configurations

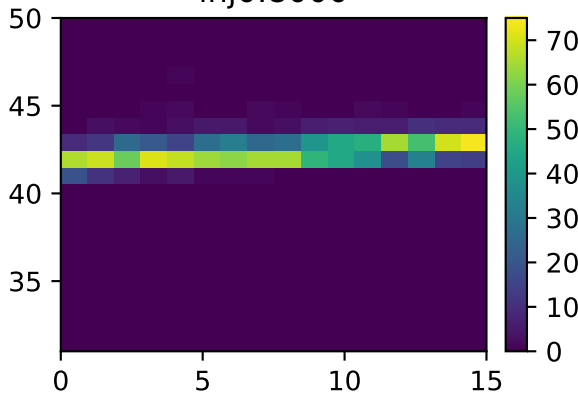
inj0.7000



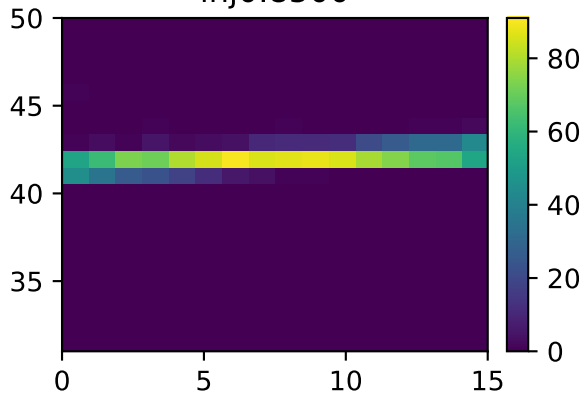
inj0.7500



inj0.8000

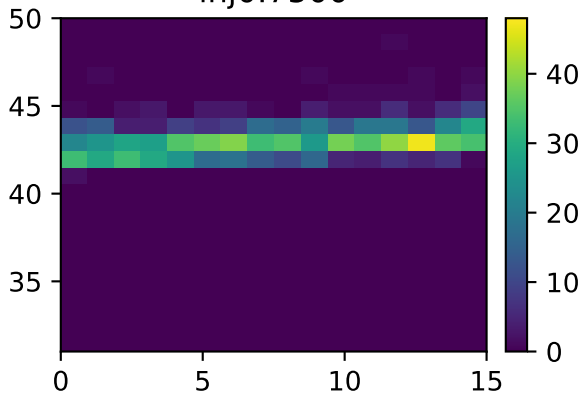


inj0.8500

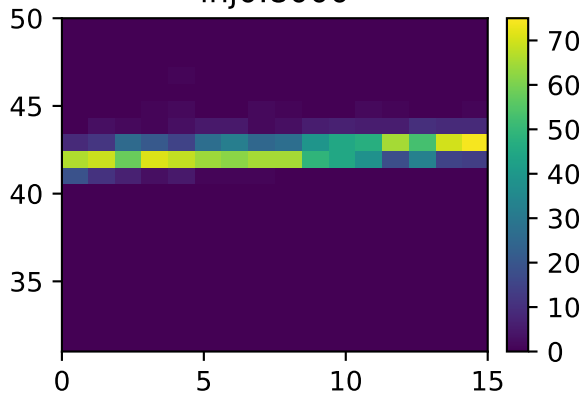


Pixel configurations

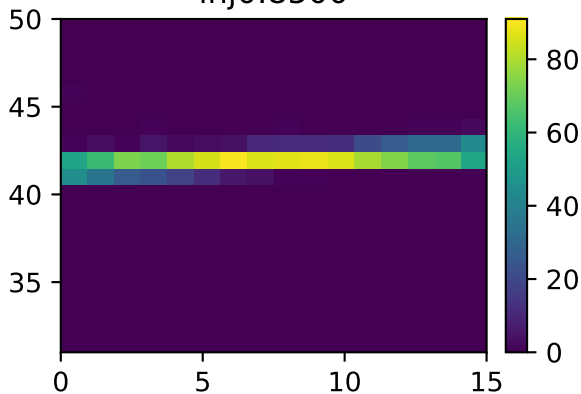
inj0.7500



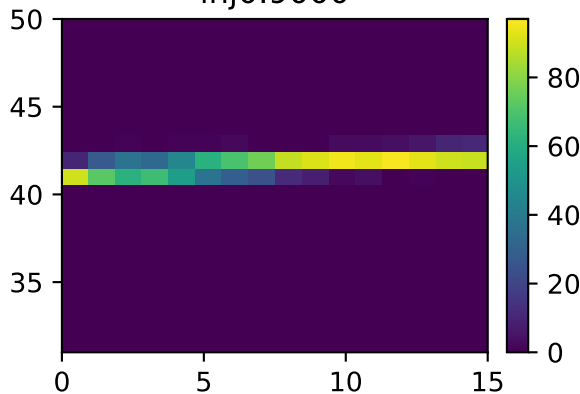
inj0.8000



inj0.8500

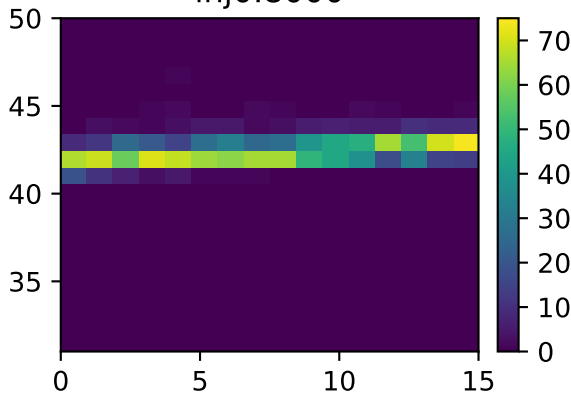


inj0.9000

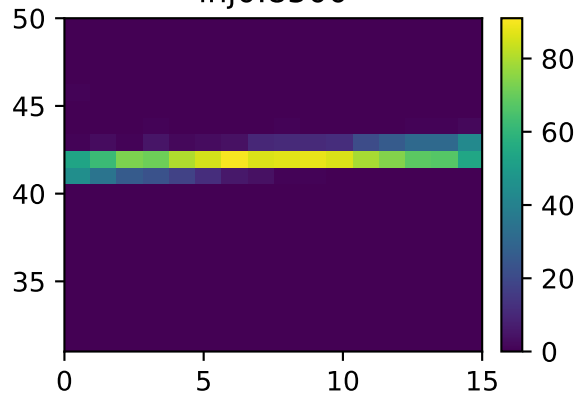


Pixel configurations

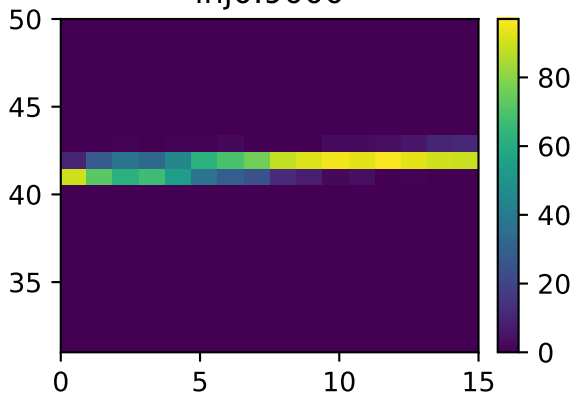
inj0.8000



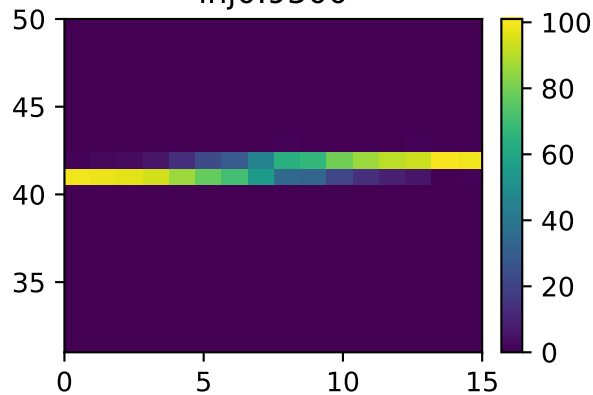
inj0.8500



inj0.9000

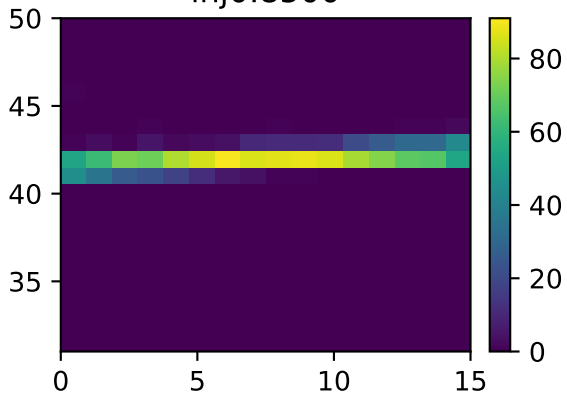


inj0.9500

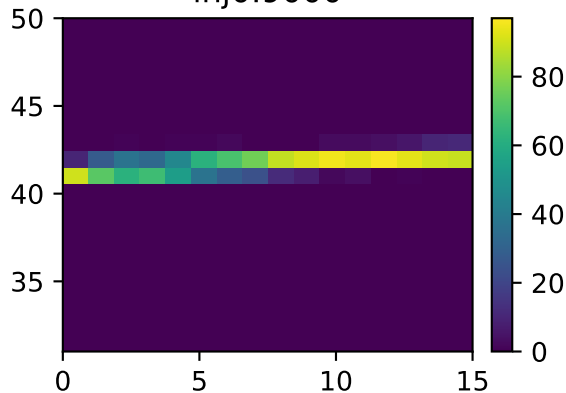


Pixel configurations

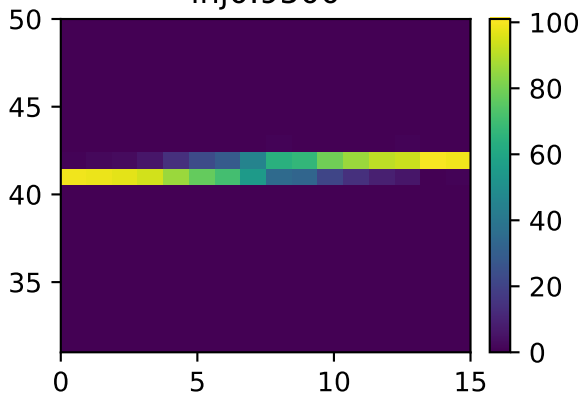
inj0.8500



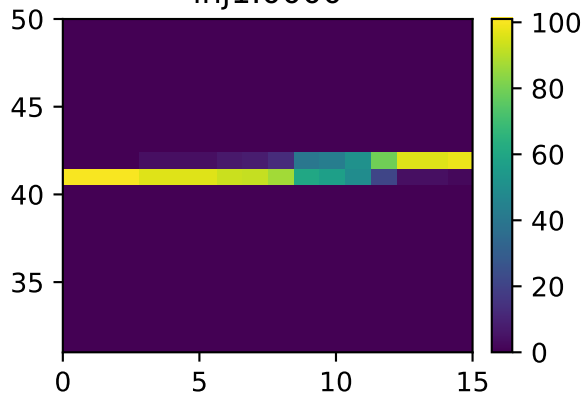
inj0.9000



inj0.9500

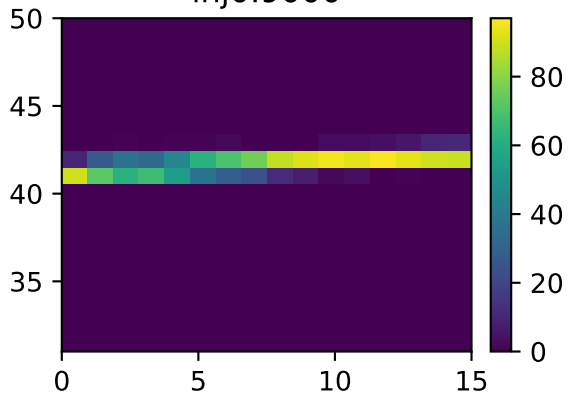


inj1.0000

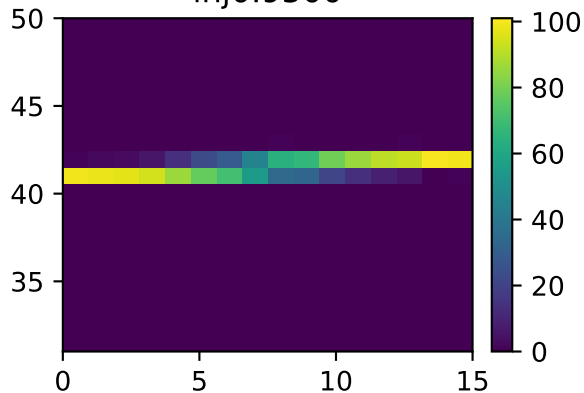


Pixel configurations

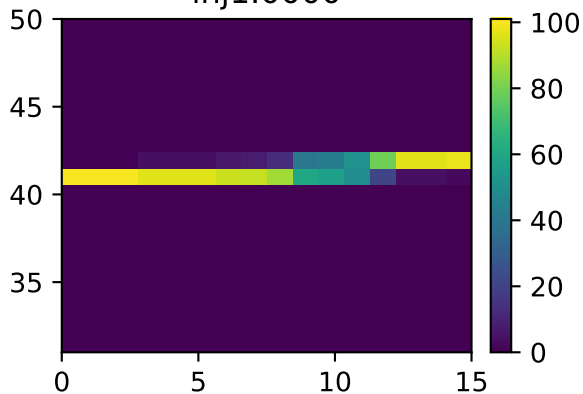
inj0.9000



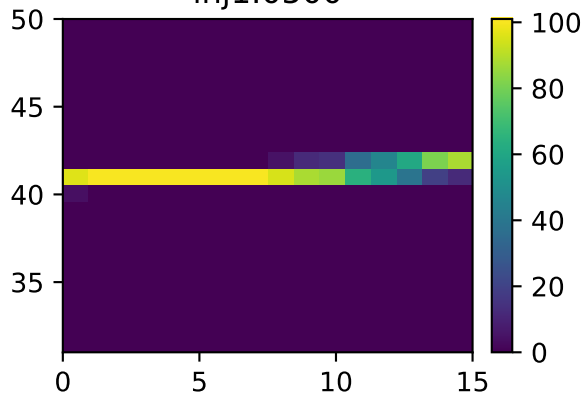
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inj1.0000

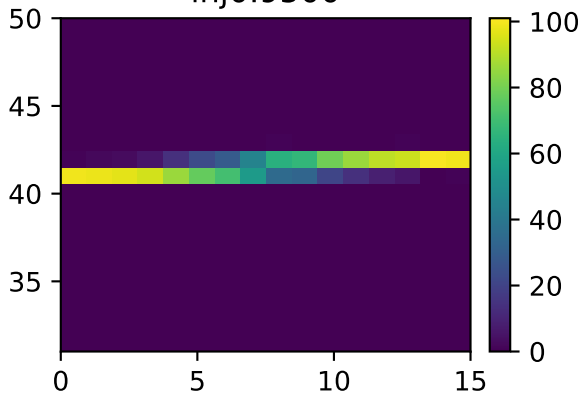


inj1.0500

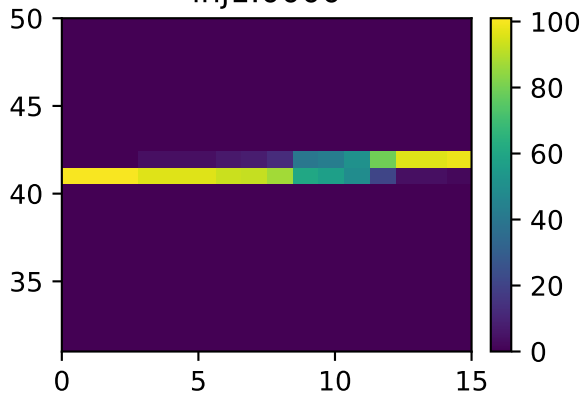


Pixel configurations

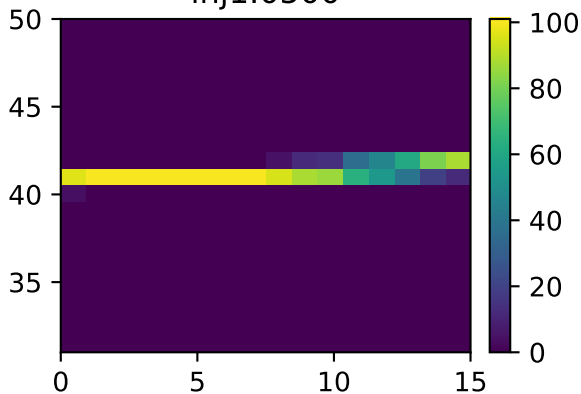
inj0.9500



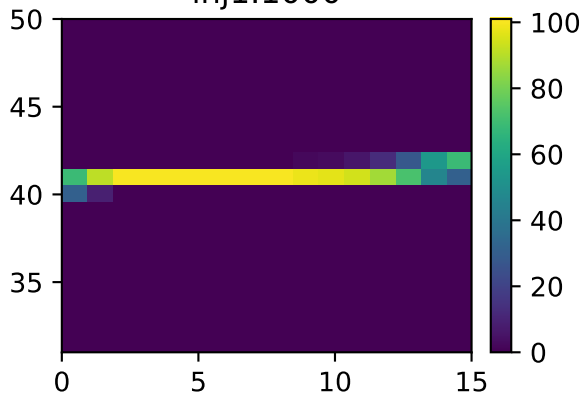
inj1.0000



inj1.0500

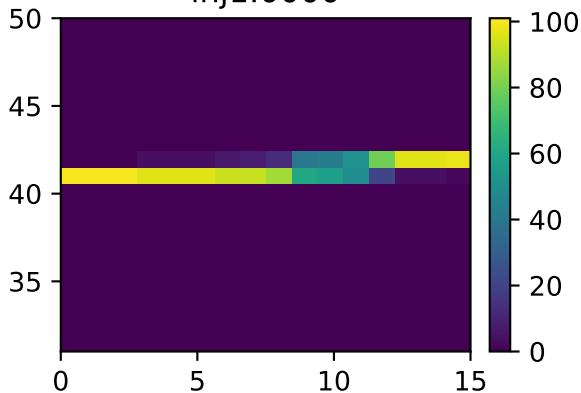


inj1.1000

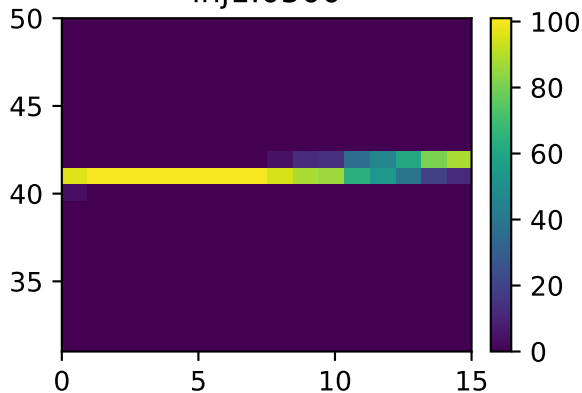


Pixel configurations

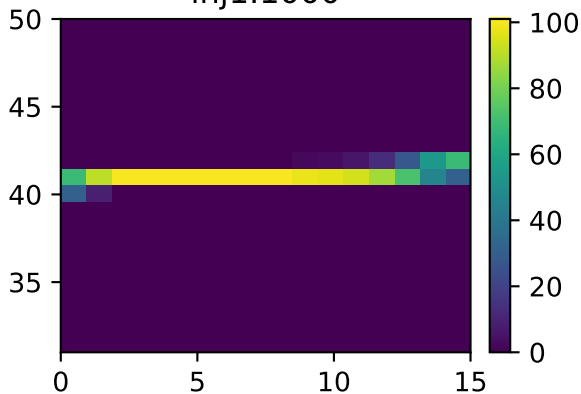
inj1.0000



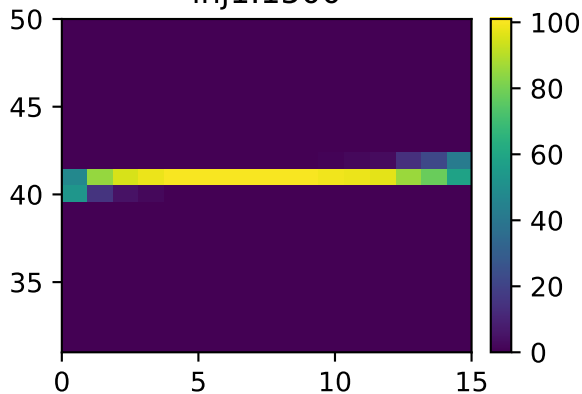
inj1.0500



inj1.1000

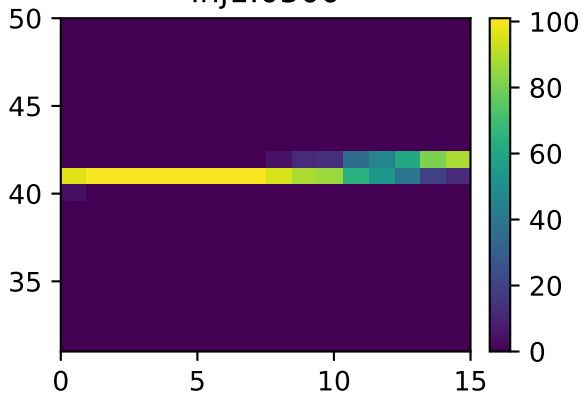


inj1.1500

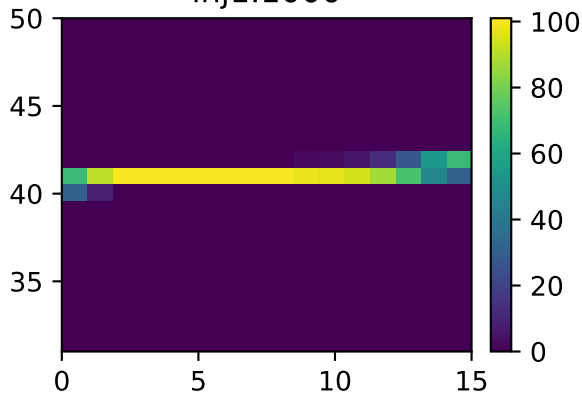


Pixel configurations

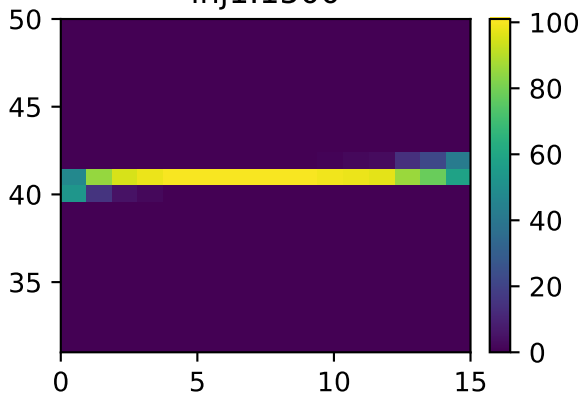
inj1.0500



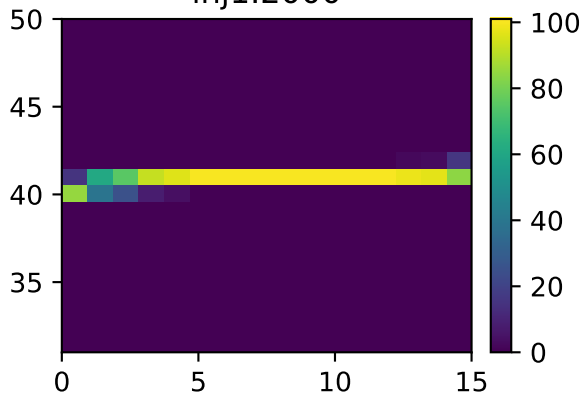
inj1.1000



inj1.1500

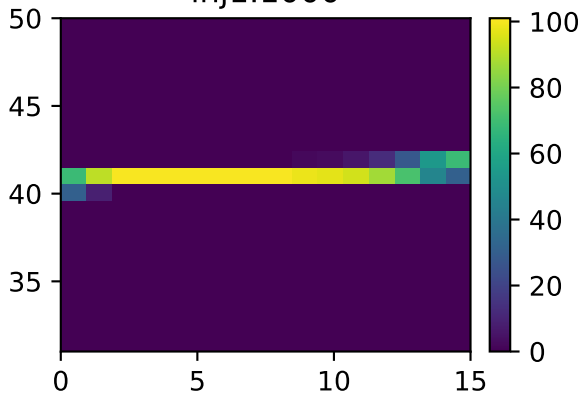


inj1.2000

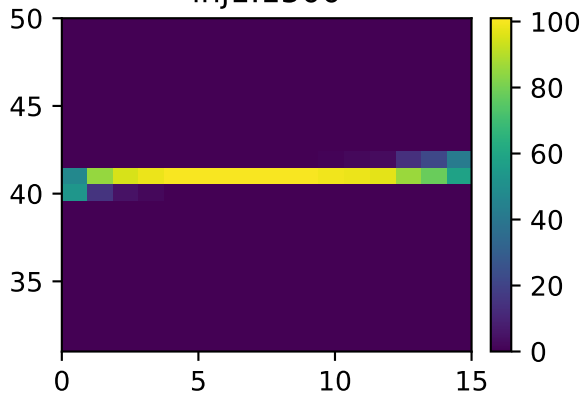


Pixel configurations

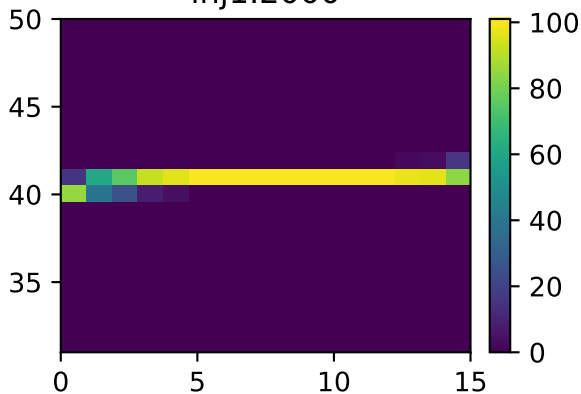
inj1.1000



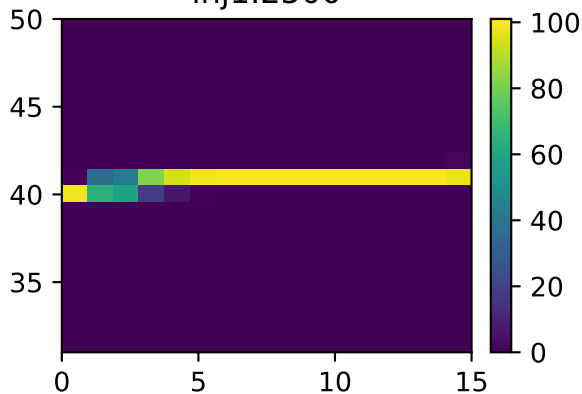
inj1.1500



inj1.2000

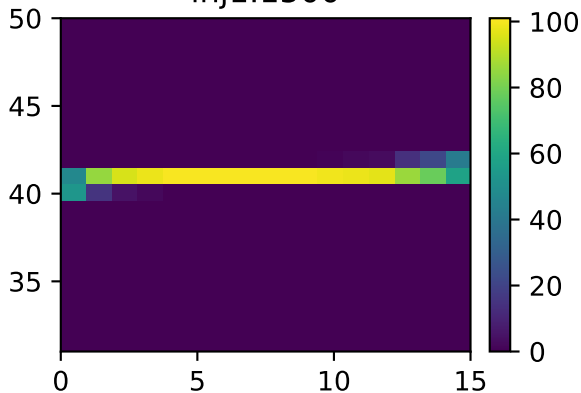


inj1.2500

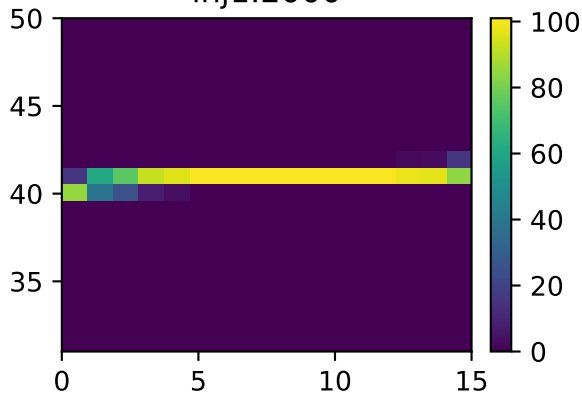


Pixel configurations

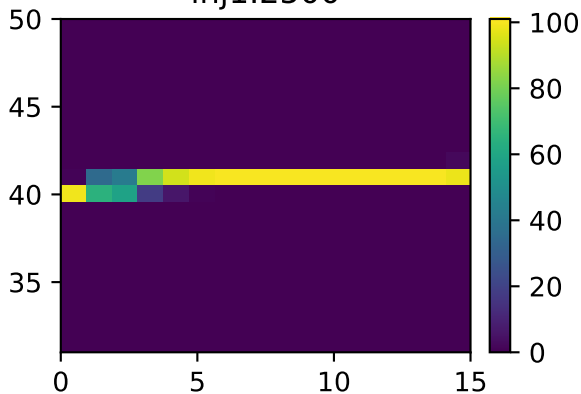
inj1.1500



inj1.2000



inj1.2500



inj1.3000

