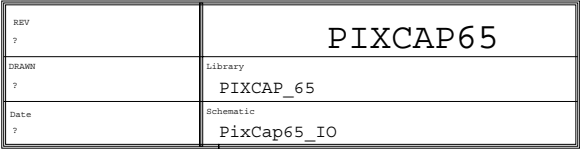
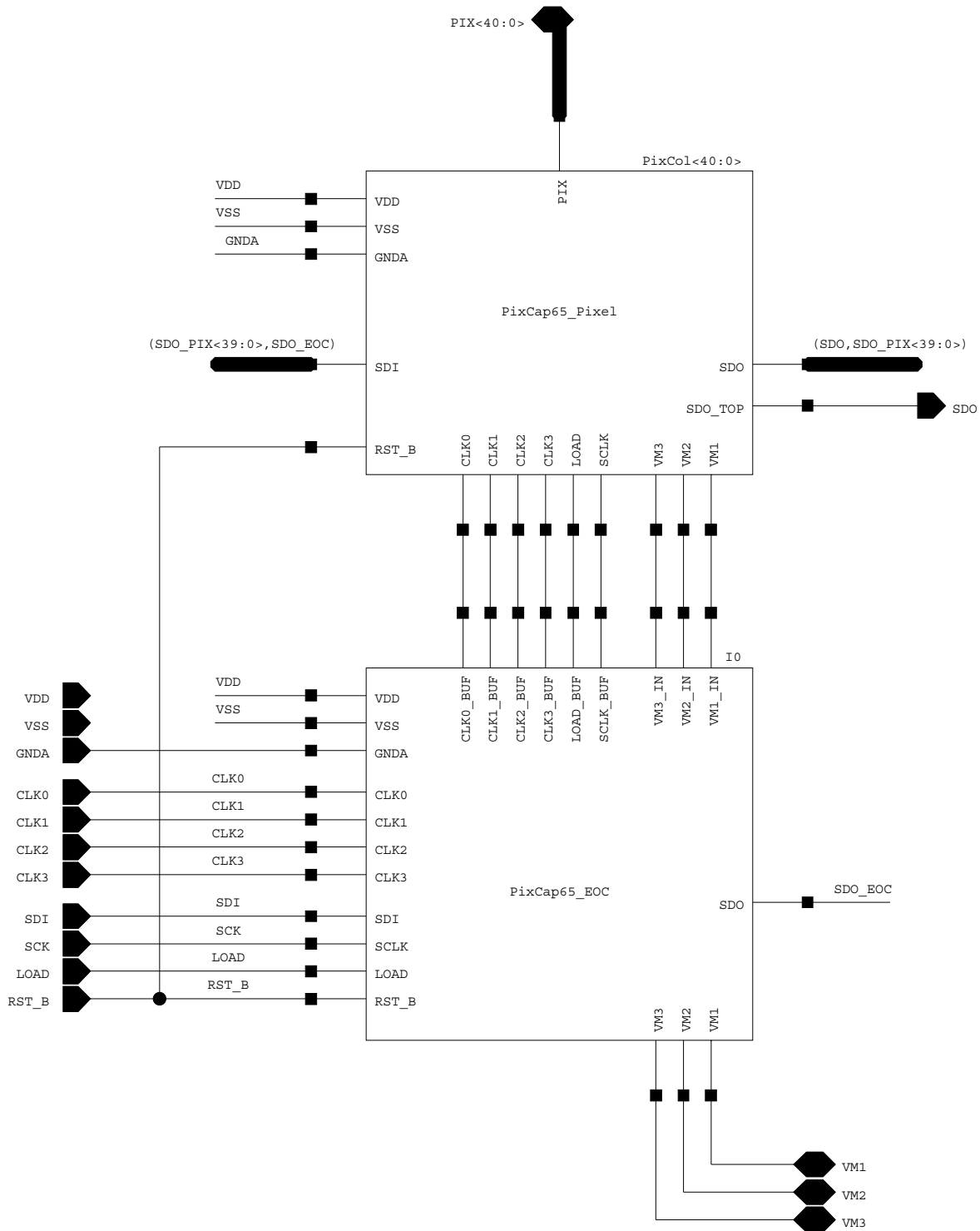


Calibration and test capacitors in row 0

REV ?	PIXCAP65
DRAWN ?	Library PIXCAP_65
Date ?XCAP	Schematic PixCap65_Top

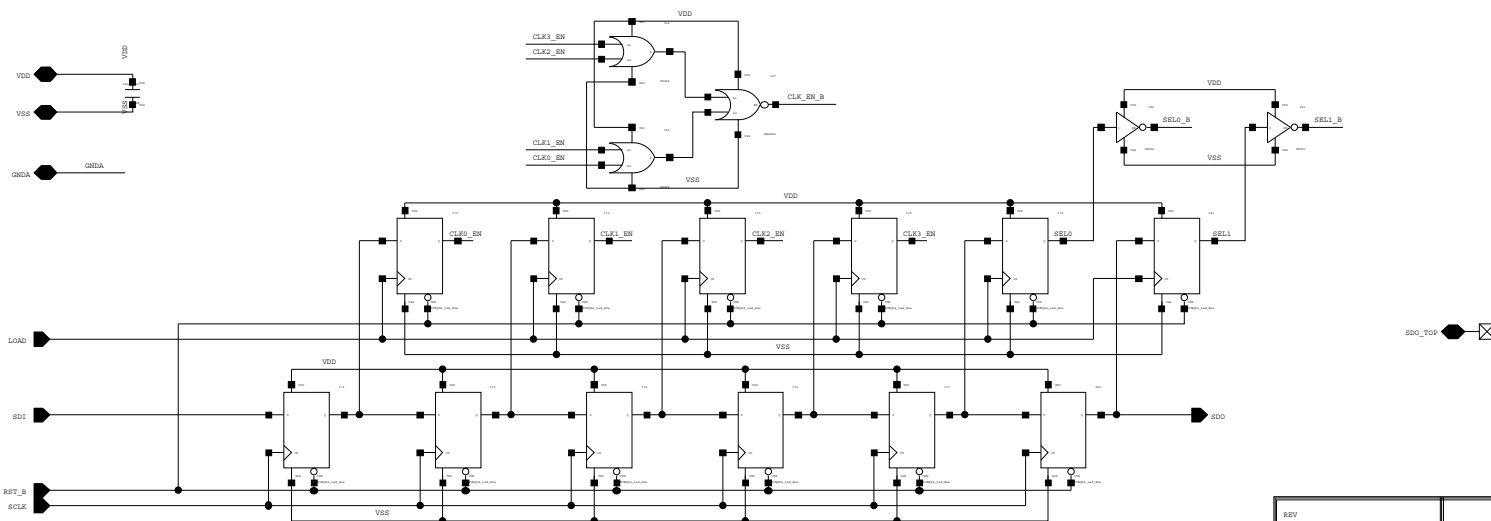




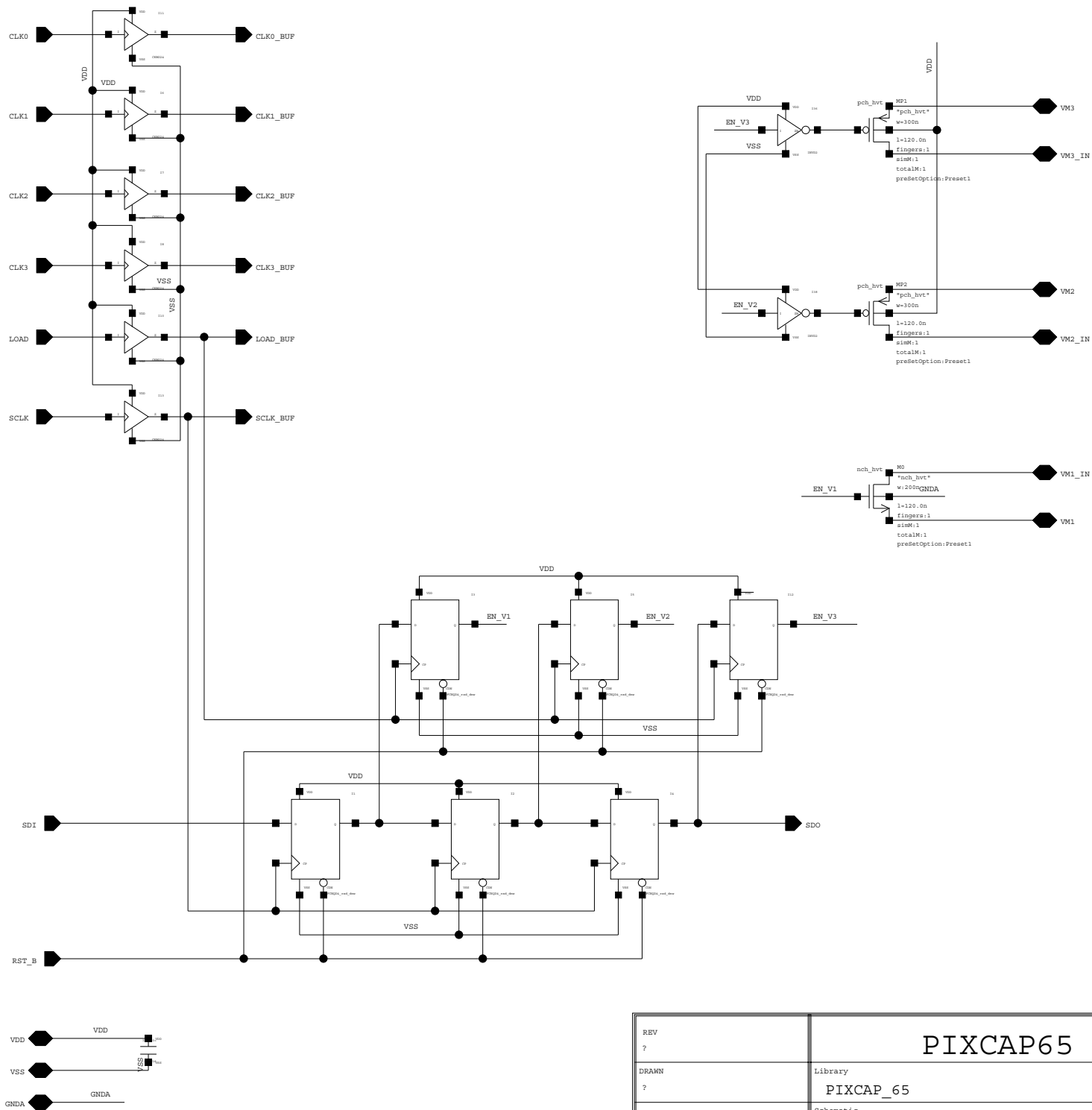
REV ?	PIXCAP65
DRAWN ?	Library PIXCAP_65
Date ?	Schematic PixCap65_Column

```
SEL[1:0]      Static bias
0 0          GND
0 1          VM1
1 0          VM2
1 1          VM3

CLK_EN[3:0]   No clock enabled, static bias active
0 0 0 0
x x x x      CLK0 enabled (PMOS to GND), static bias off
x x x x      CLK1 enabled (PMOS to VM1), static bias off
x x x x      CLK2 enabled (PMOS to VM2), static bias off
x x x x      CLK3 enabled (PMOS to VM3), static bias off
```



REV ?	PIXCAP65
DRAWN ?	Library PIXCAP_65
Date ?	Schematic PixCap65 Pixel



REV	PIXCAP65
?	
DRAWN	Library
?	PIXCAP_65
Date	Schematic
?	PixCap65_EOC

