Embedded System Design
Practice 2

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Necessary file download

- **download “embedded.zip” from LMS**

- **List of files**
  - PL2303_Prolific_DriverInstaller (Windows)
  - toolchain-s5pc1xx.tar.gz
  - u-boot-1.3.4.tar.gz
  - vpos.bin
  - vpos.zip

Contents

1. U-Boot booting
2. Install TFTP, ARM Cross compiler
3. U-Boot porting
4. VPOS booting

U-BOOT

Bootloader

- **What is bootloader?**
  - Initialize system hardware
  - Load OS image into memory and branch to OS startup routine
  - The first program that runs when the system is powered on

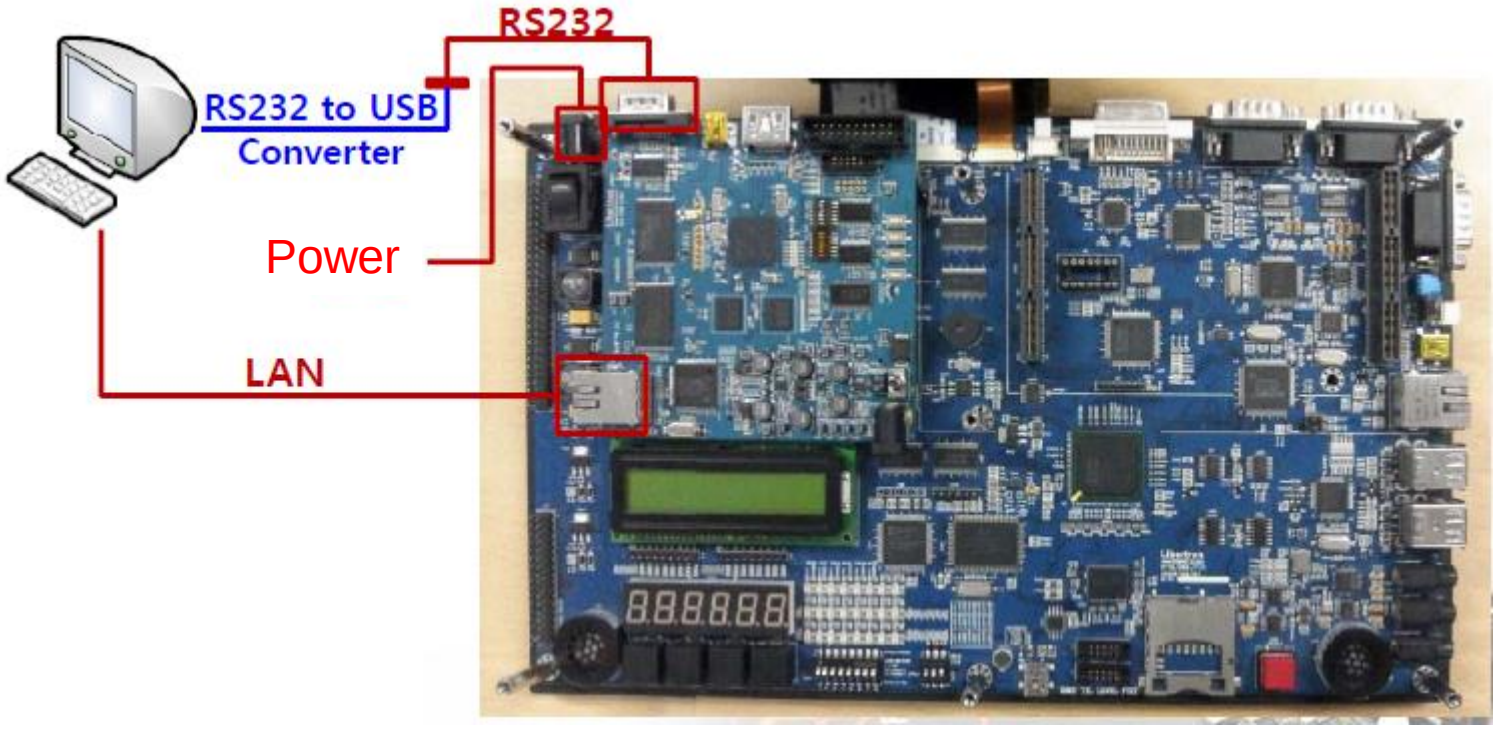
- **Location of the bootloader**
  - Generally, the physical address 0
  - Located in static memory such as ROM, Flash ROM, SRAM
- **Features of the bootloader**
  - Memory initialization
  - Hardware initialization

U-Boot

- **What is U-Boot?**
  - Universal Bootloader
  - Bootloader for embedded boards based on PowerPC and ARM
  - Widely used for bootloader of Linux on embedded board
- **Advantages**
  - Ported to various platforms (ARM, MIPS, x86, etc.)
  - Clean code, good structure
  - Easy configuration

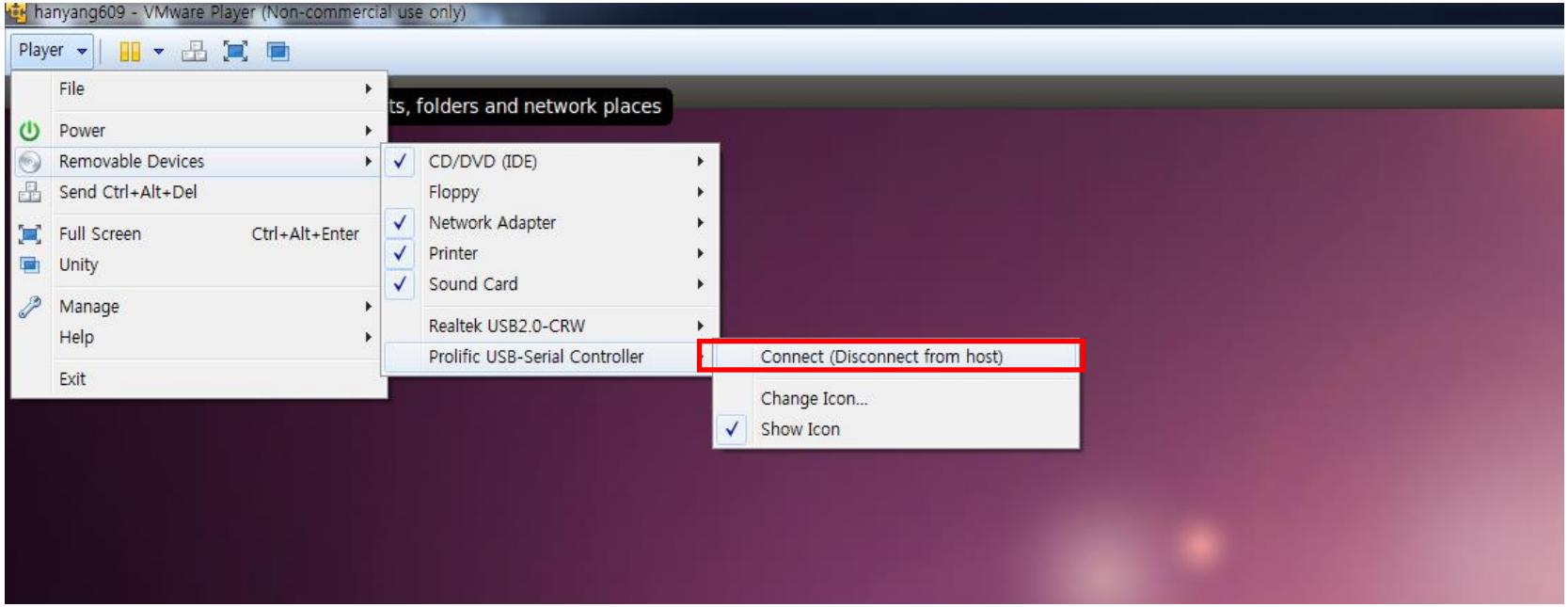
Serial Cable Connection

- **Serial connection**
  - Connect power cable to board
  - Connect serial cable to board and pc using converter



Serial Cable Connection

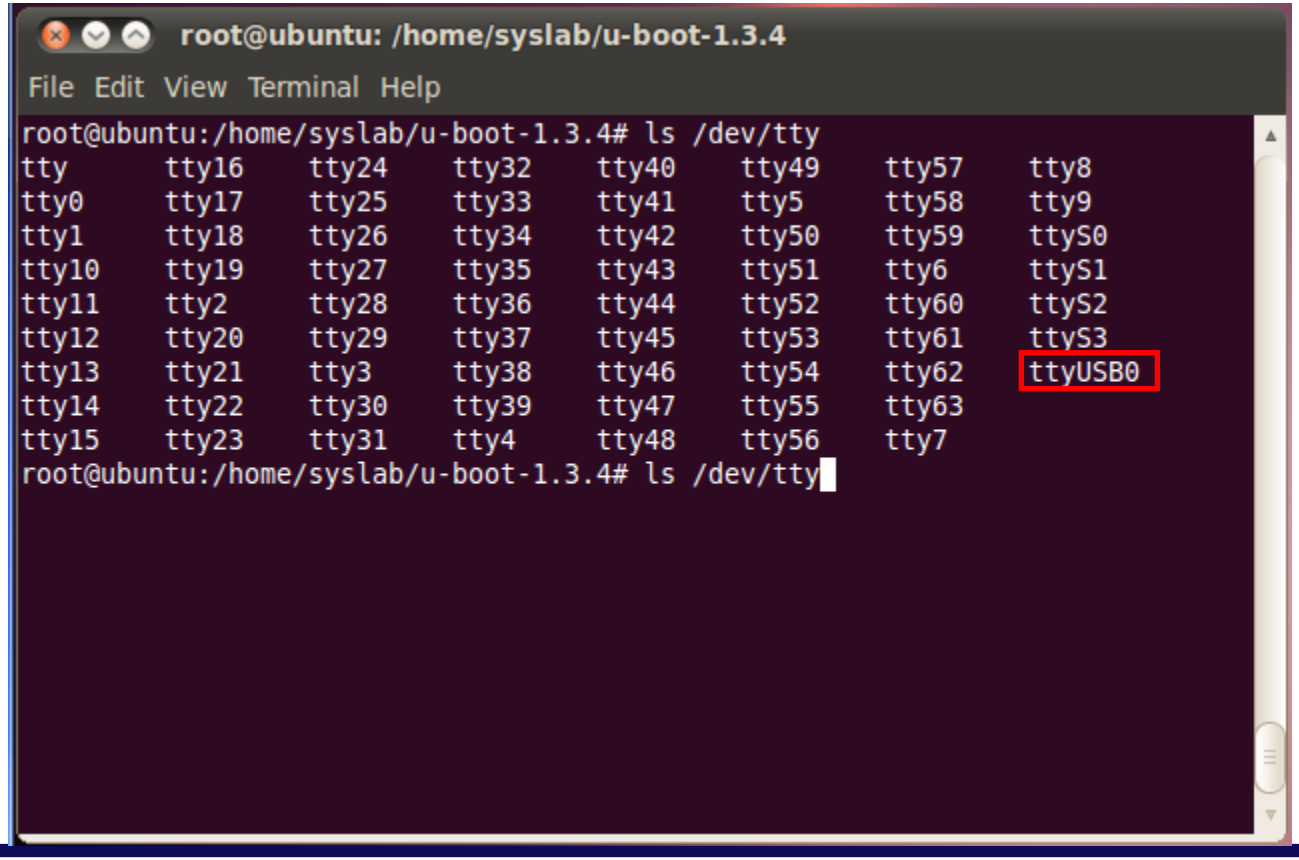
- **Player → Removable Devices**
  - Check ‘Prolific USB-Serial Controller → Connect



Serial Cable Connection

- **Verify that the board is connected**

  >> ls /dev/tty + Tap twice



Install USB to Serial Driver

- **Install USB to Serial Driver (except linux)**

- **Windows**
  - Install using PL2303_Prolific_DriverInstaller_v1.10.0
- **MAC OS**
  - Search PL-2303 Mac driver → Download and install

- **Reboot after installation**

Minicom

- **Install minicom**

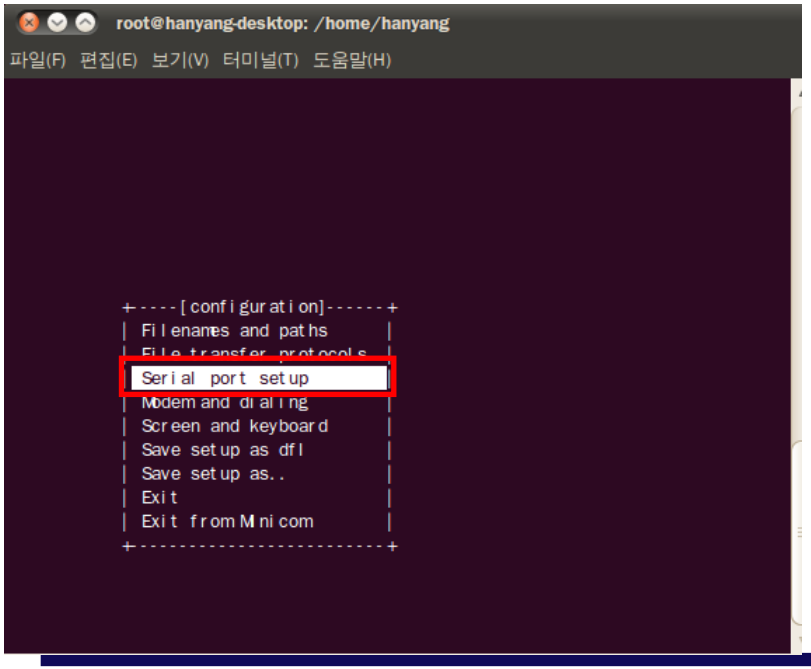
  >> apt-get install minicom

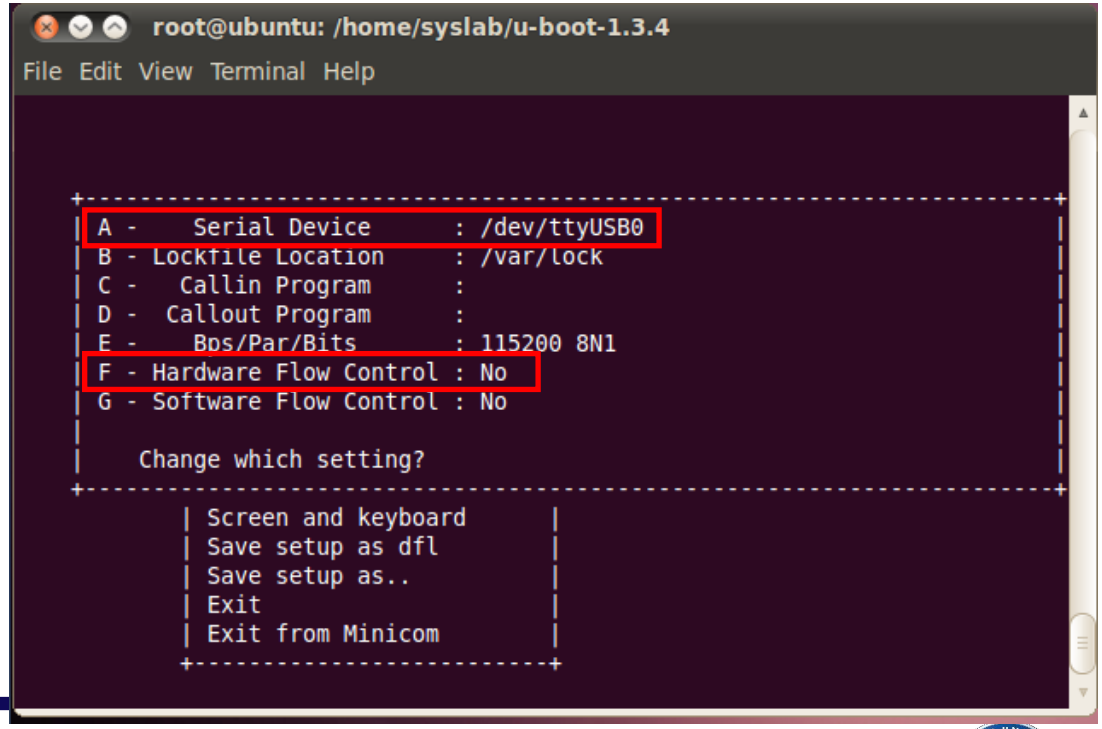
Minicom

- **Minicom settings (be sure to use ‘root’ account)**

  >> minicom -s

  – In Serial port setup, change the ‘Serial Device’ and ‘Hardware Flow Control’ as follows (go to the corresponding item by pressing the keyboard ‘a’ and ‘f’ keys)

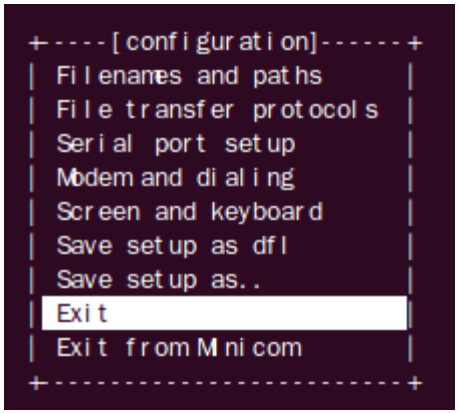




Minicom

- **Minicom settings**
  - Save minicom settings with ‘Save setup as dfl’

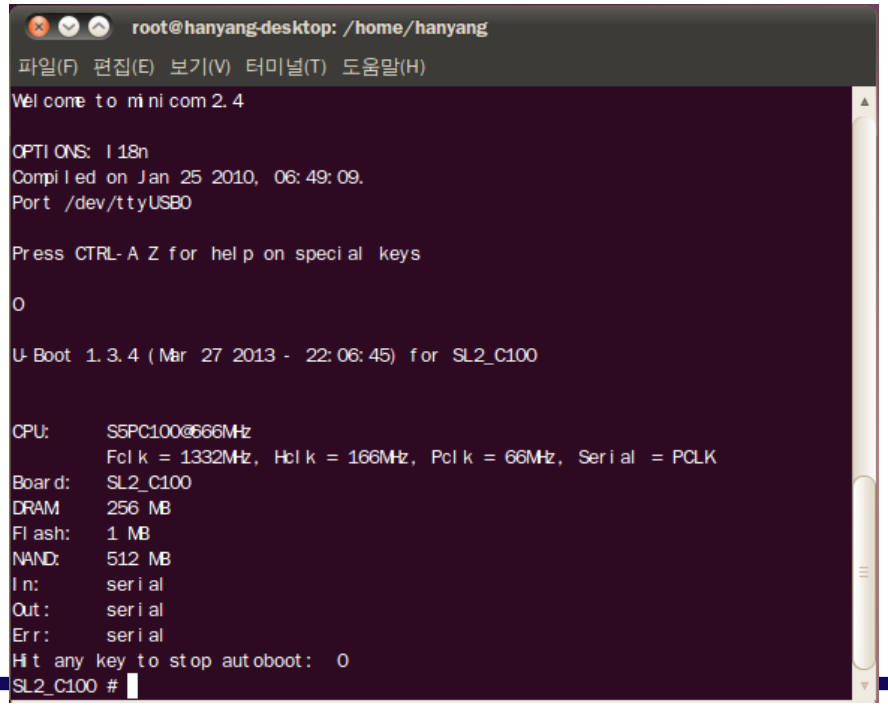
- **Connect to board with minicom**
  (1) >> minicom
  (2) >> minicom –s and ‘Exit’



U-boot

- **Power on the board and press the ‘Enter’ key**

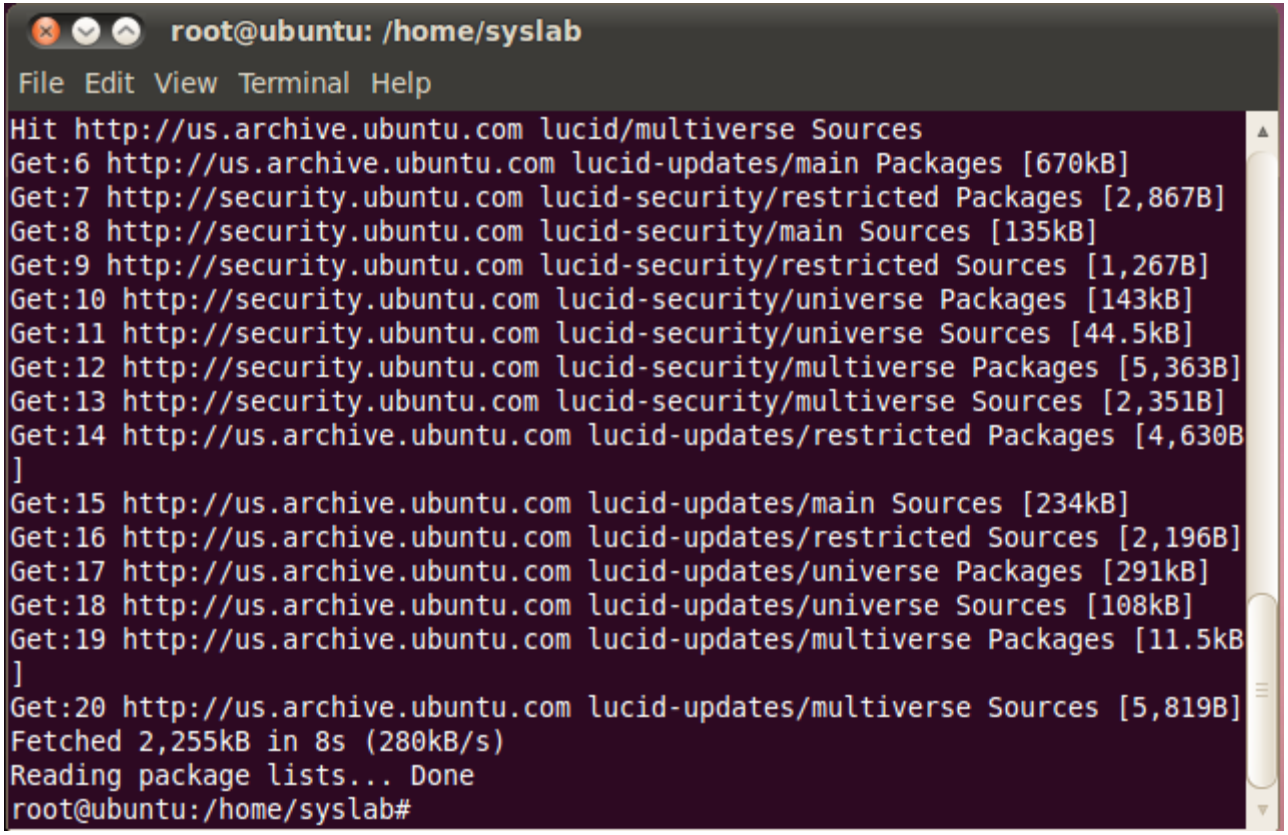
- **If the following screen appears, the board connection succeeds.**
  - If the screen is not displayed, press the reset button to reset



INSTALL TFTP, ARM CROSS COMPILER

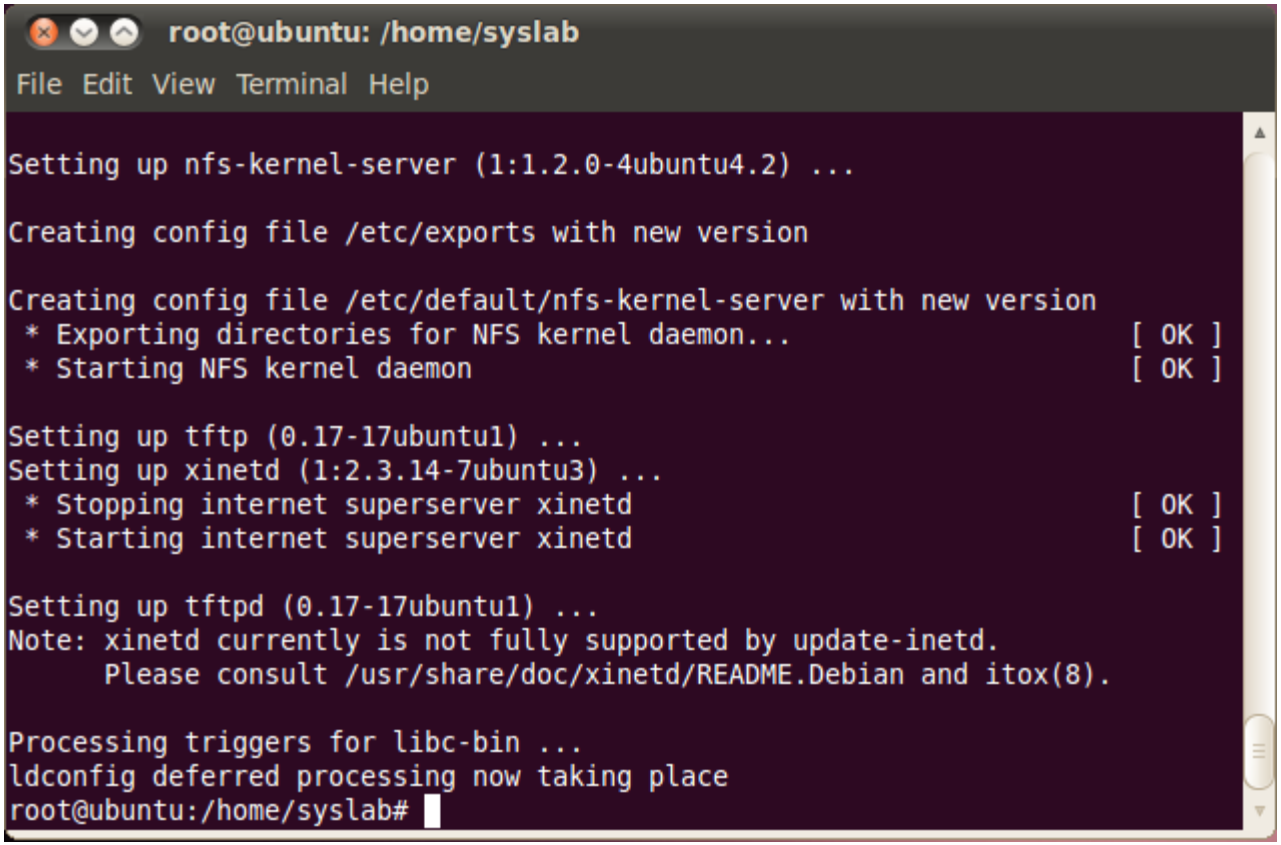
Get Update Files

>> apt-get update



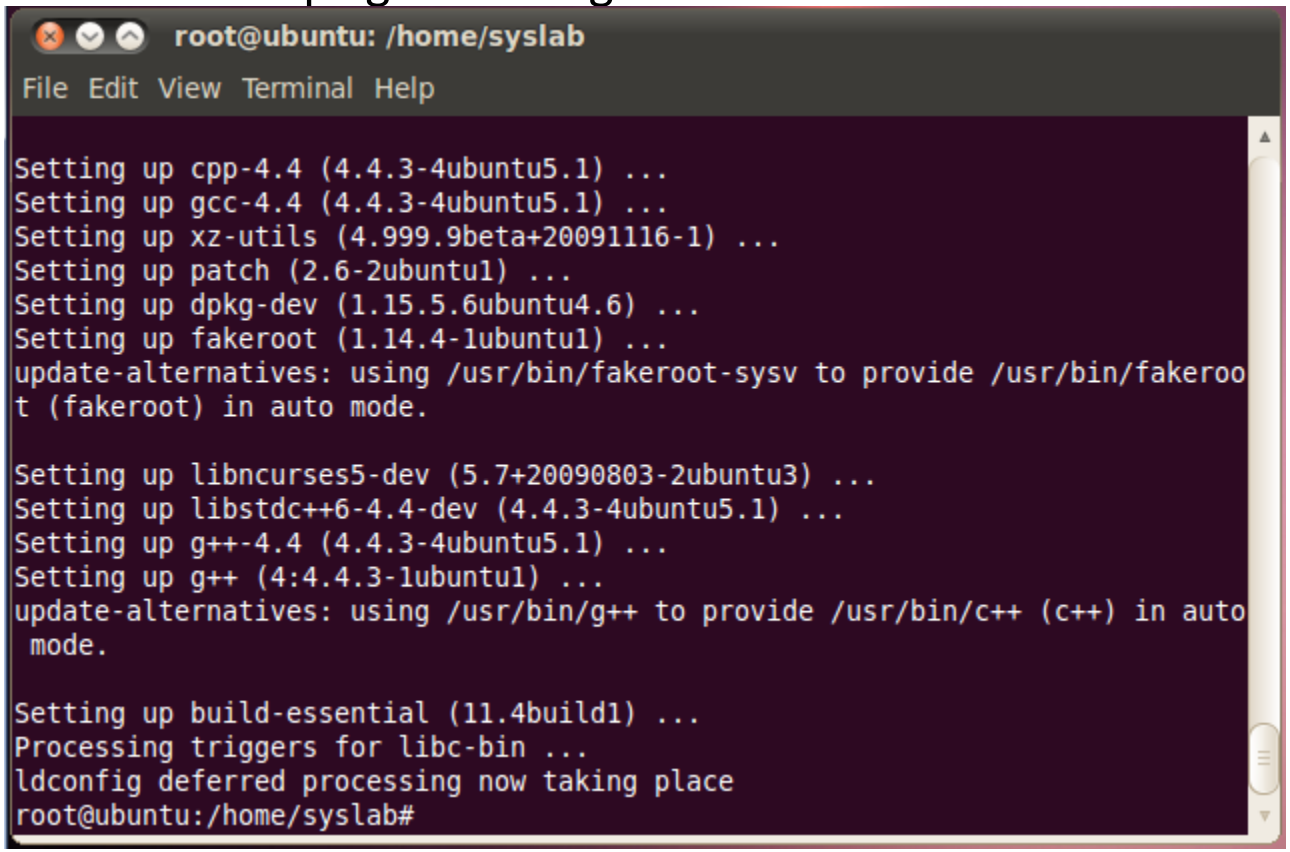
Install and Set up a Linux-based Development Environment

- **Install package**
  - >> apt-get install nfs-kernel-server tftp tftpd xinetd



Install and Set up a Linux-based Development Environment

- >> apt-get install libncurses5 libncurses5-dev build-essential
- For 64bit >> apt-get install gcc-multilib



TFTP

- **What is TFTP?**
  - Trivial File Transfer Protocol
  - Protocols for transferring files like FTP
    - Transfer in a simpler way than FTP
  - Mainly used as operating system upload on embedded systems

+ Easy to implement

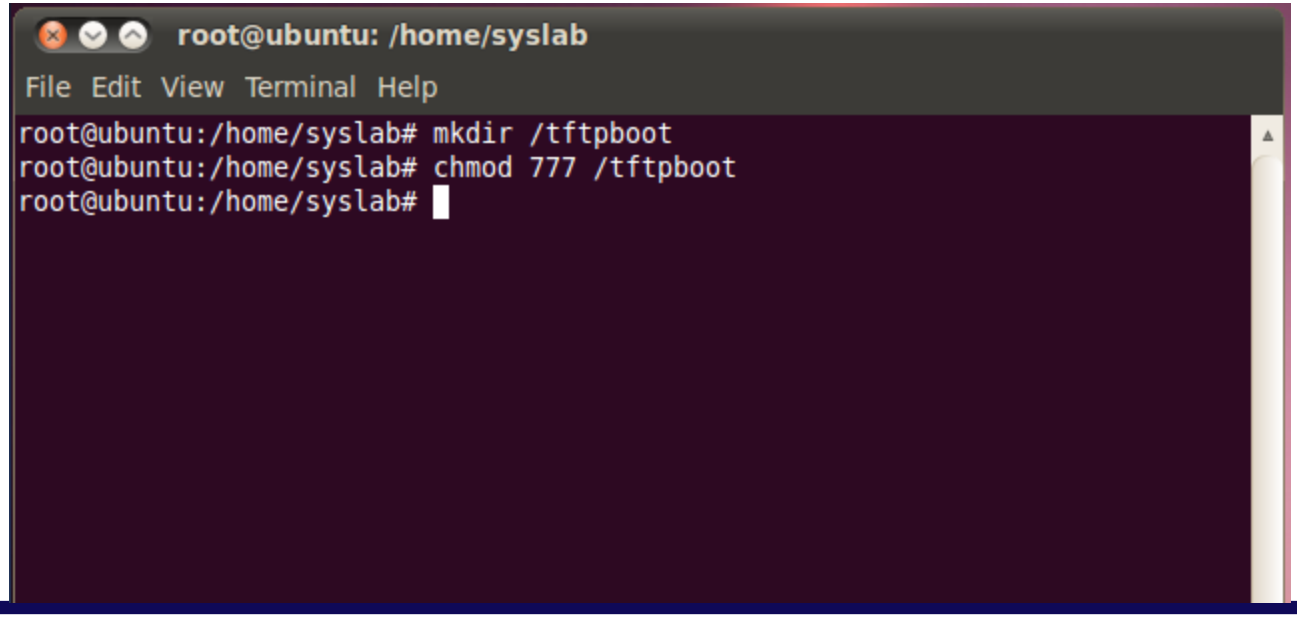
- Data may be lost during data transfer

TFTP Setting

- **TFTP Setting**
  - Make tftpboot folder

    >> mkdir /tftpboot
  - Setting tftpboot folder permissions

    >> chmod 777 /tftpboot

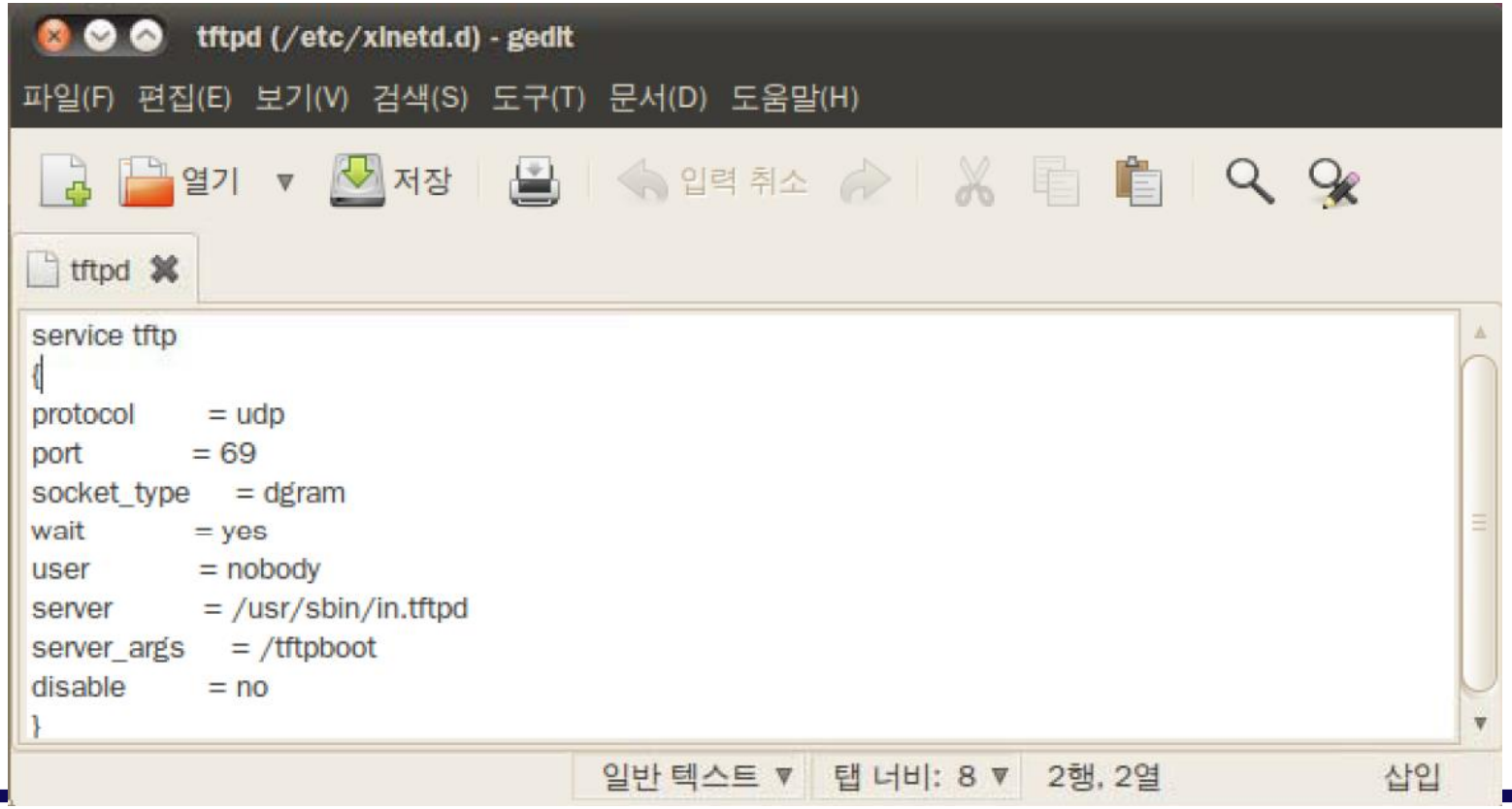


TFTP Setting

- **TFTP Preferences**

  >> gedit /etc/xinetd.d/tftpd

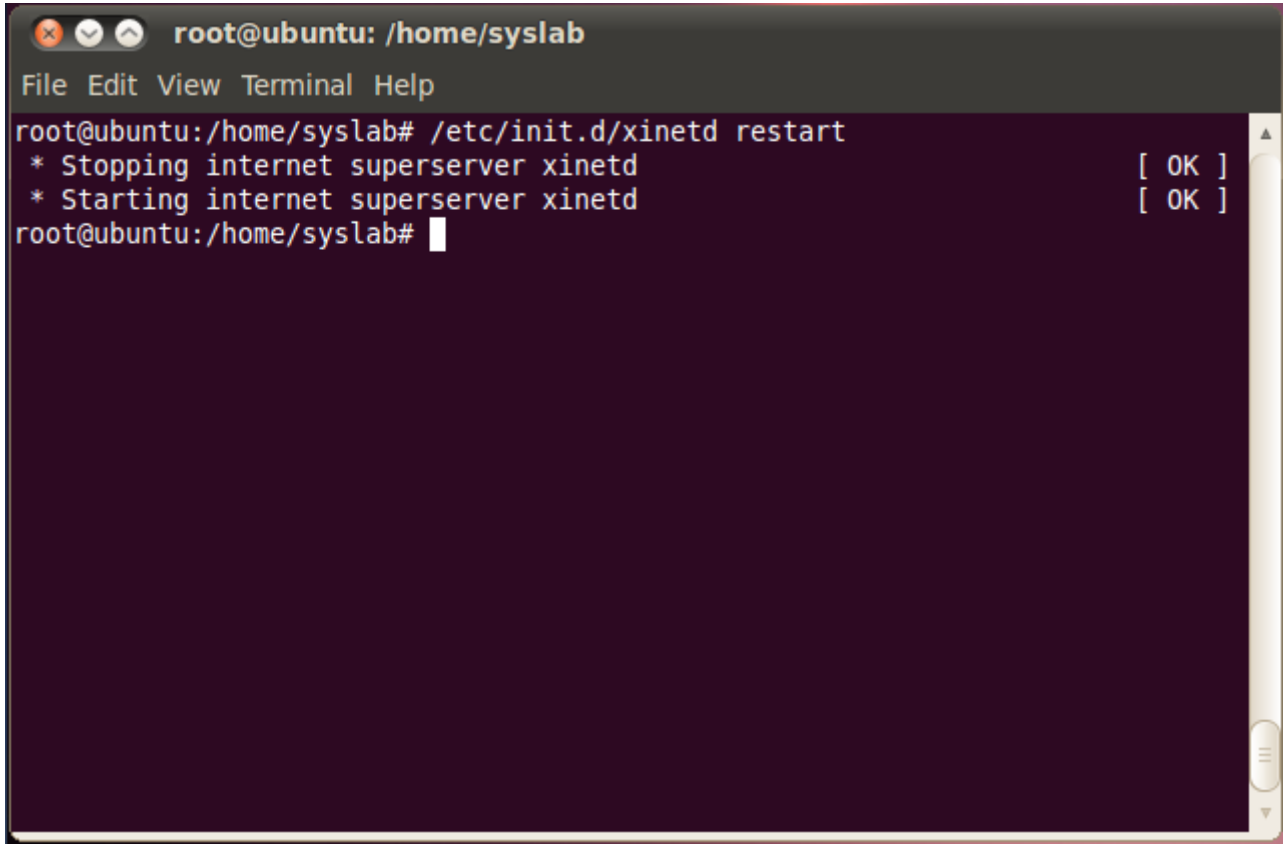
  – Edit and save as shown below



TFTP Setting

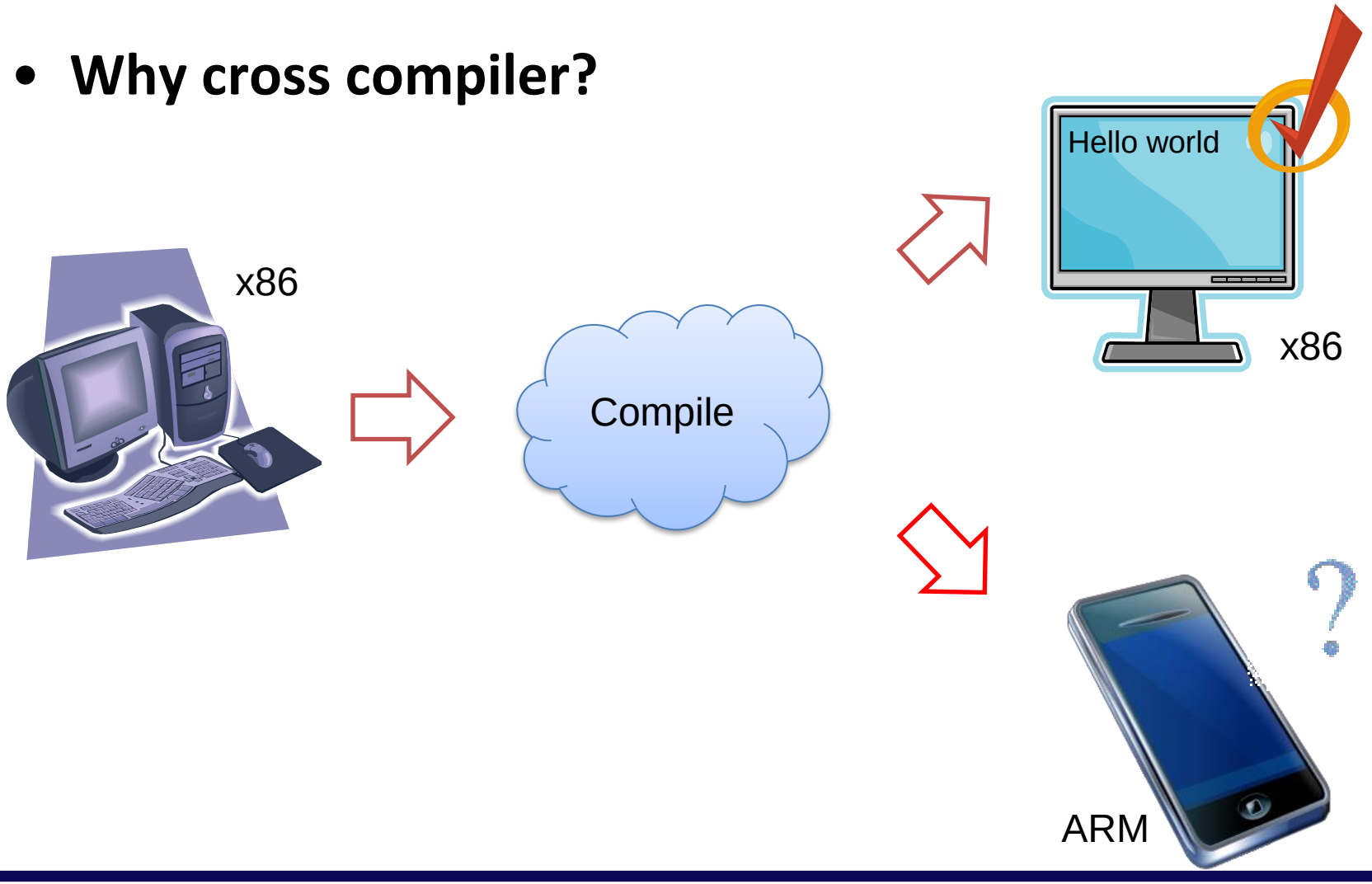
- **Xinetd restart**

  >> /etc/init.d/xinetd restart



Cross Compiler

- **Why cross compiler?**



Cross Compiler

- **Need for cross compiler**
  - On normal computer systems (x86), compiling and execution are done on the same machine (x86)
  - On embedded systems (ARM), compilation is performed on the host computer (x86) and executed in ARM systems.
    - Common compiler translates original codes to x86 machine language and can not run on ARM
- **What is cross compiler?**
  - Rather than translating the program into the machine language of the computer on which the compiler is running, compiling into machine language for different models
- **Toolchain**
  - Compilation environment of host computer for software development of desired embedded system
  - Includes cross compiler

Install ToolChain

- **Install ToolChain**

  >> mv toolchain-s5pc1xx.tar.gz /opt

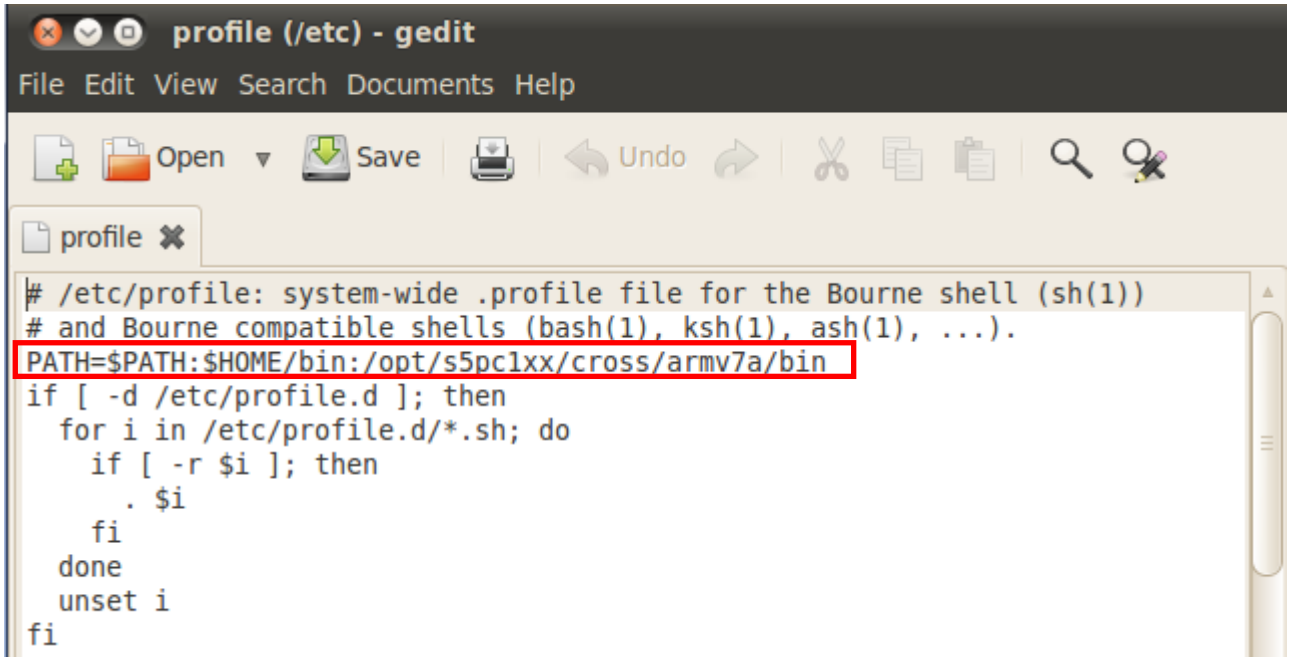
  >> tar -xvf toolchain-s5pc1xx.tar.gz

Install ToolChain

- **Set cross compiler environment variables**

  >> gedit /etc/profile

  – Add PATH=$PATH:$HOME/bin:/opt/s5pc1xx/cross/armv7a/bin

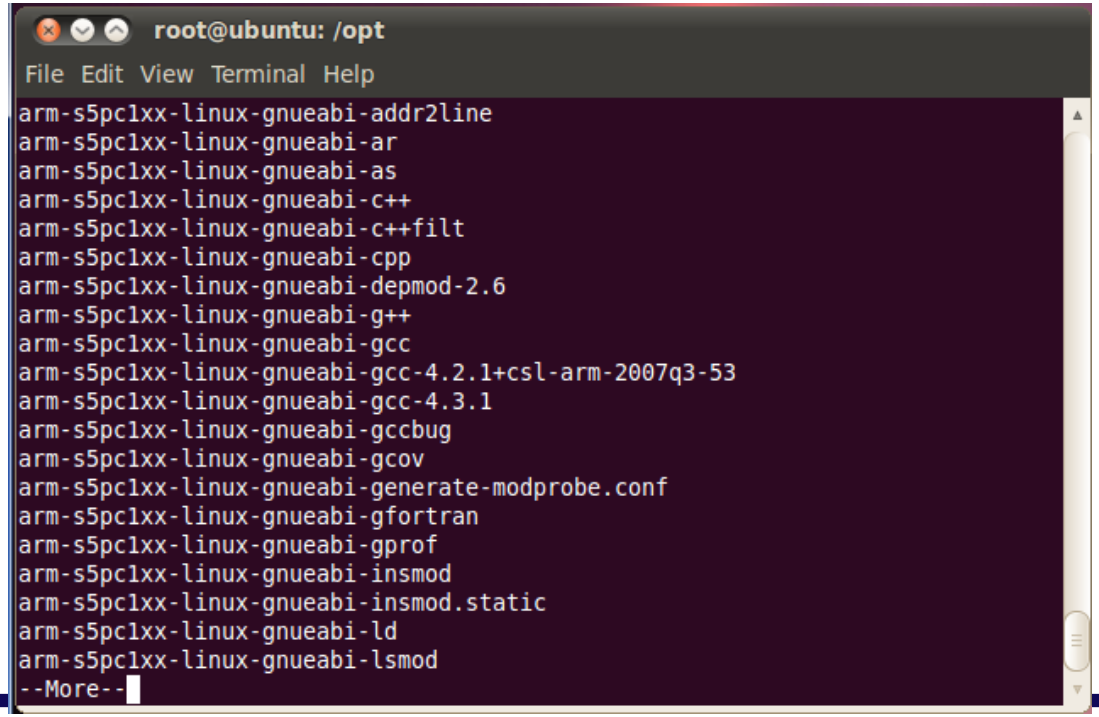


Install ToolChain

- **Apply the modification**

  >> source /etc/profile

- **Confirm cross compiler is applied**
  - Type ‘arm’ in the shell and press the Tap key twice



U-BOOT PORTING

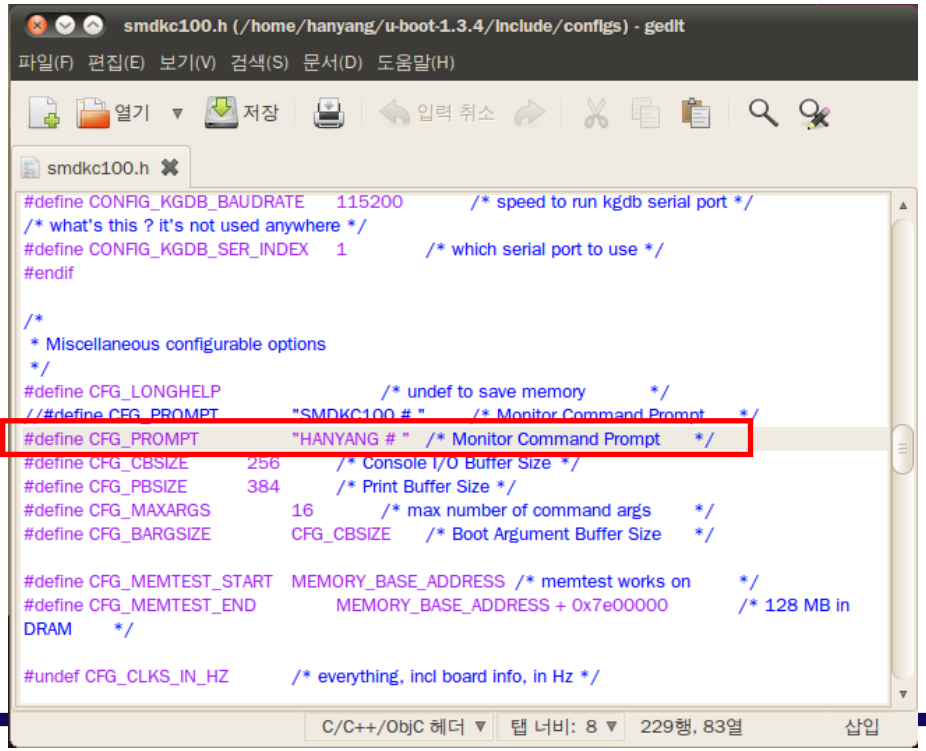
U-boot Compilation

- **Modify the code to make sure U-boot is installed correctly**

  >> gedit /home/hanyang/u-boot-1.3.4/include/configs/smdkc100.h

- **Modify the string of CFG_PROMPT on line 229**

  # define CFG_PROMPT “SL2_C100 #” ➔ “Student ID #”



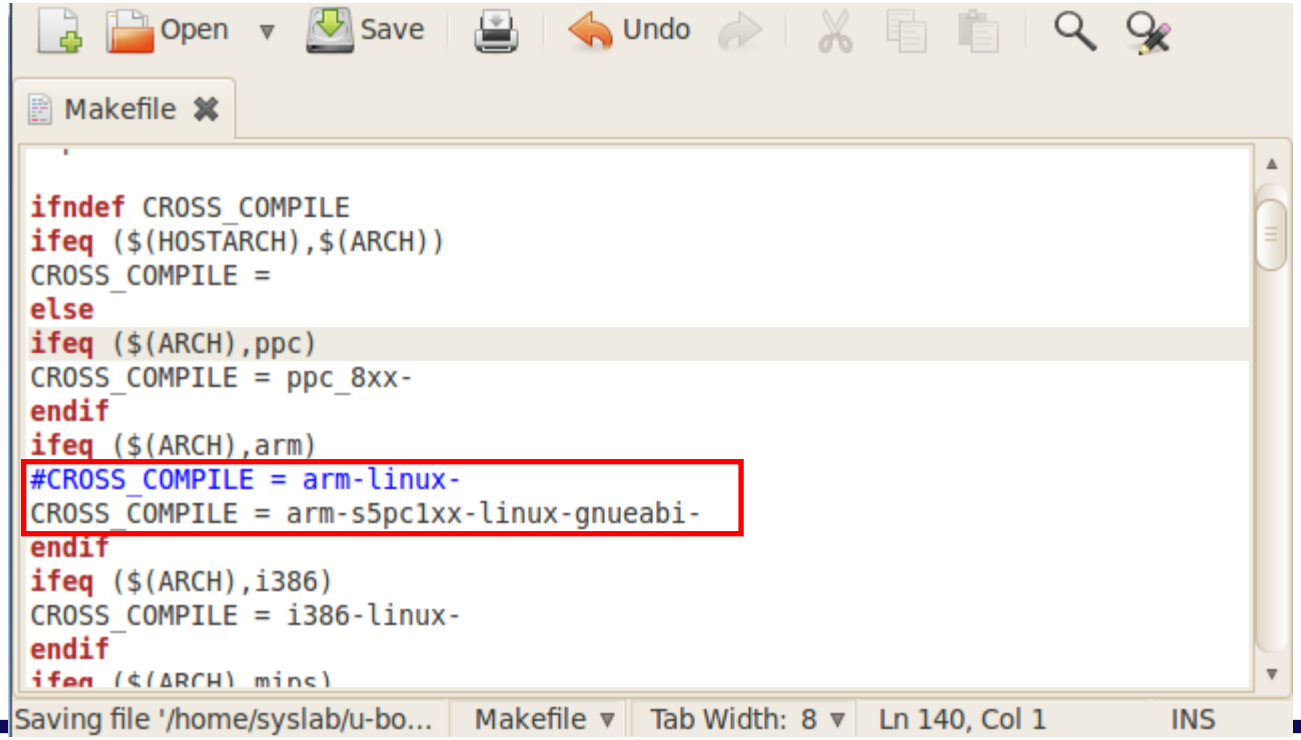
U-boot Compilation

- **Confirm Makefile**

  >> gedit /home/hanyang/u-boot-1.3.4/Makefile

  – Comment #CROSS_COMPILE = arm-linux- on line 144

  [using 'arm-s5pc1xx-linux-gnueabi-' cross compiler]



U-boot Compilation

- **U-boot make**

  >> cd /home/hanyang/u-boot-1.3.4

  >> source /etc/profile

  >> make clean

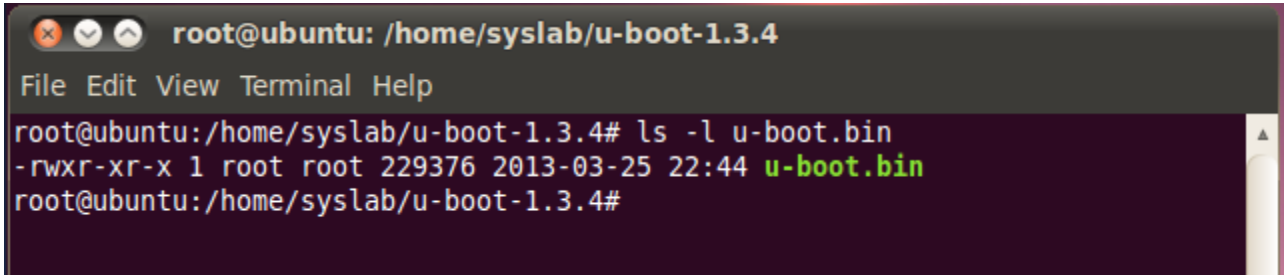
  >> make clobber

  >> make smdkc100_config

  >> make

- **Ensure the the U-boot.bin file is created**

  >> ls –l u-boot.bin



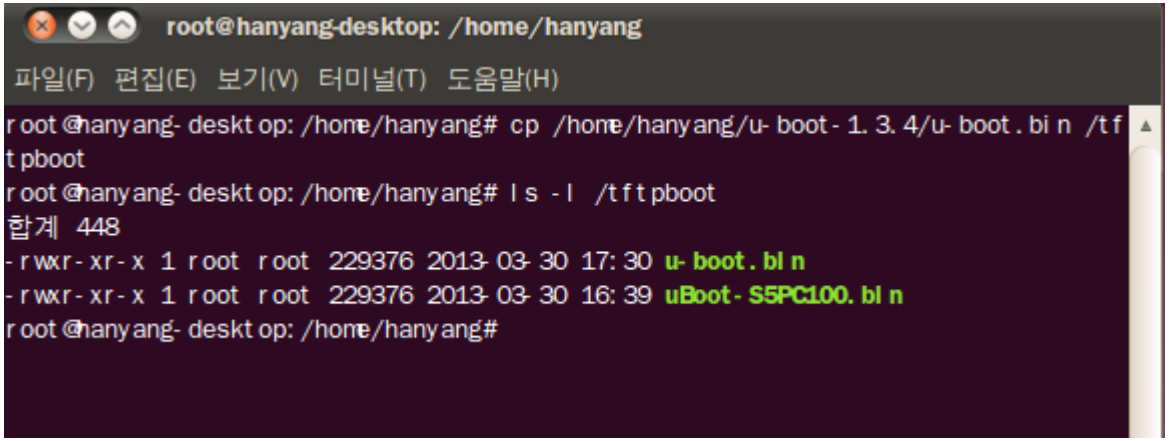
U-boot Compilation

- **Copy u-boot.bin file to the /tftpboot folder**

  >> cp /home/hanyang/u-boot-1.3.4/u-boot.bin /tftpboot

- **Verify that the u-boot.bin file has been copied**

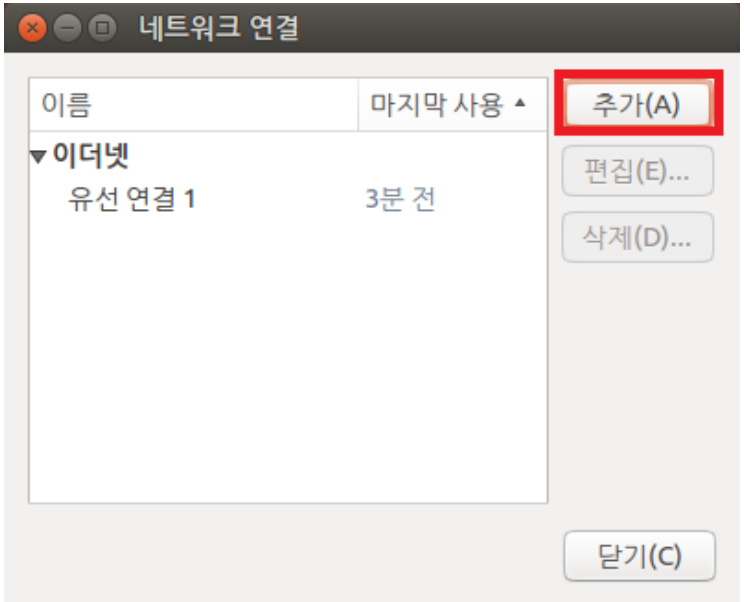
  >> ls –l /tftpboot



Network Settings

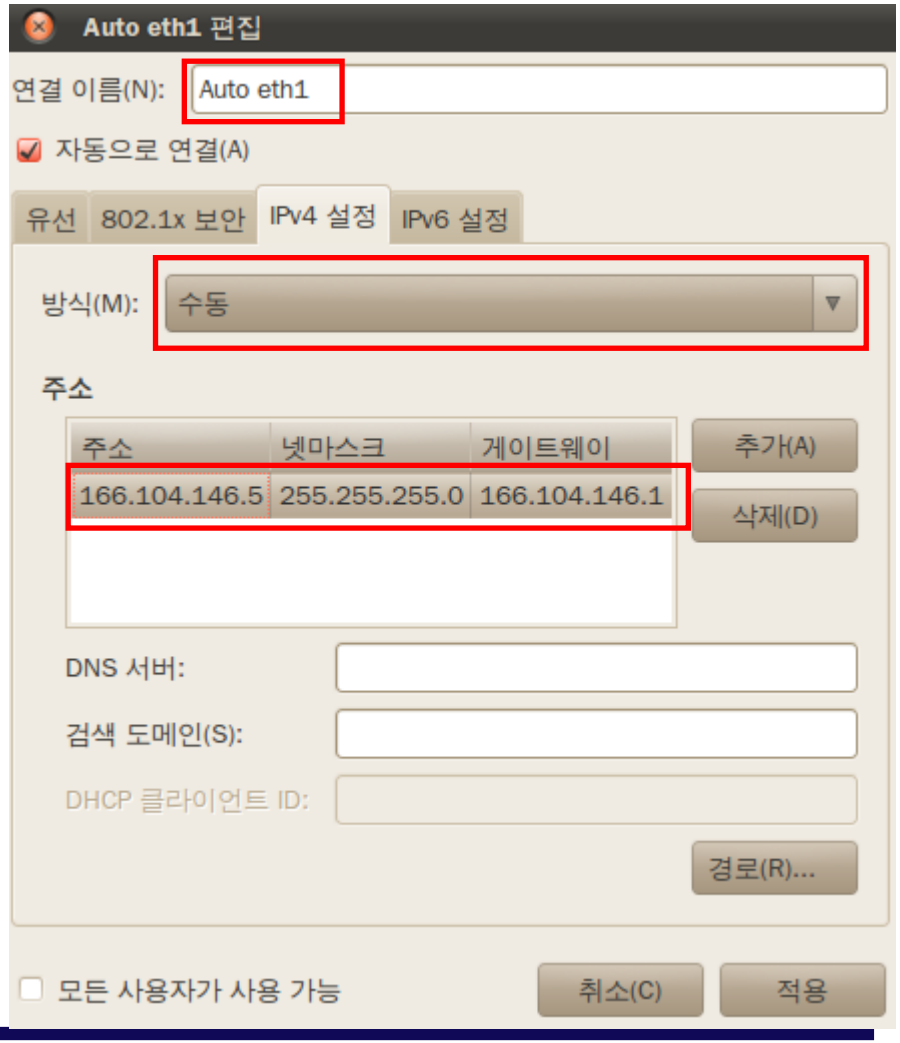
- **Network settings**
  - Linux IP settings

- **Network Connections**
  - Add -> Ethernet



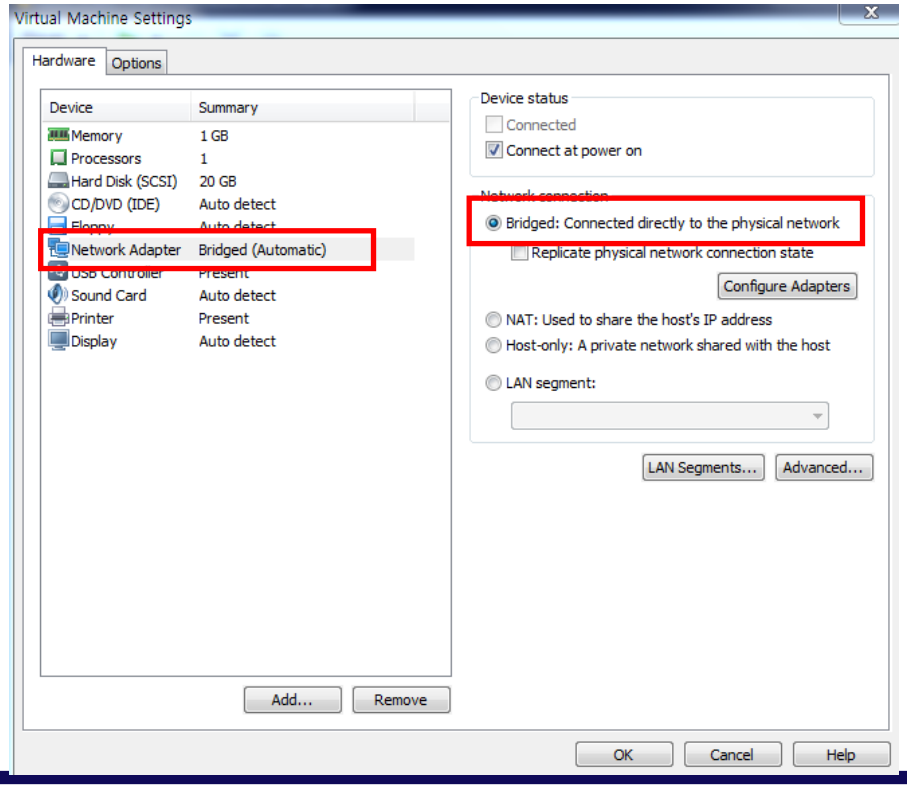
Network Settings

- **Edit Auto eth1**
  - IPv4 Settings
    - Method : Manual
    - Address : 166.104.146.5
    - Netmask : 255.255.255.0
    - Gateway : 166.104.146.1
  - Click ‘Save…’



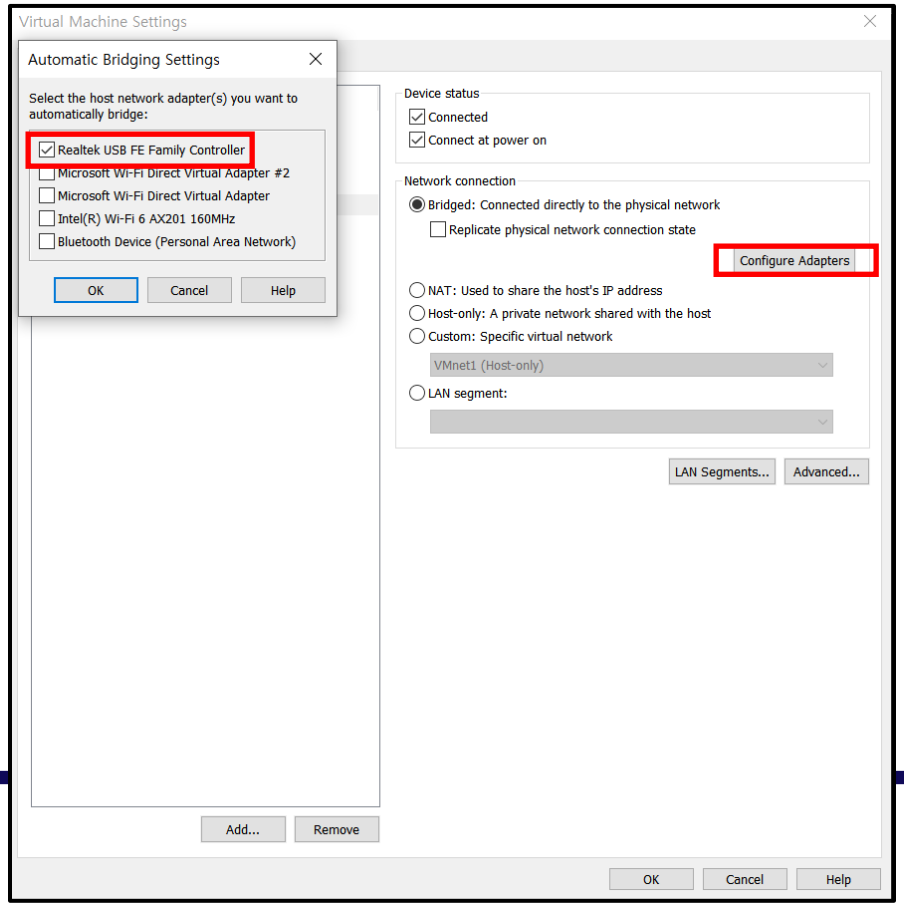
Network Settings

- **Network settings of virtual machine**
  - Shutdown linux
  - Hardware tab→ Network Adapter → Bridged



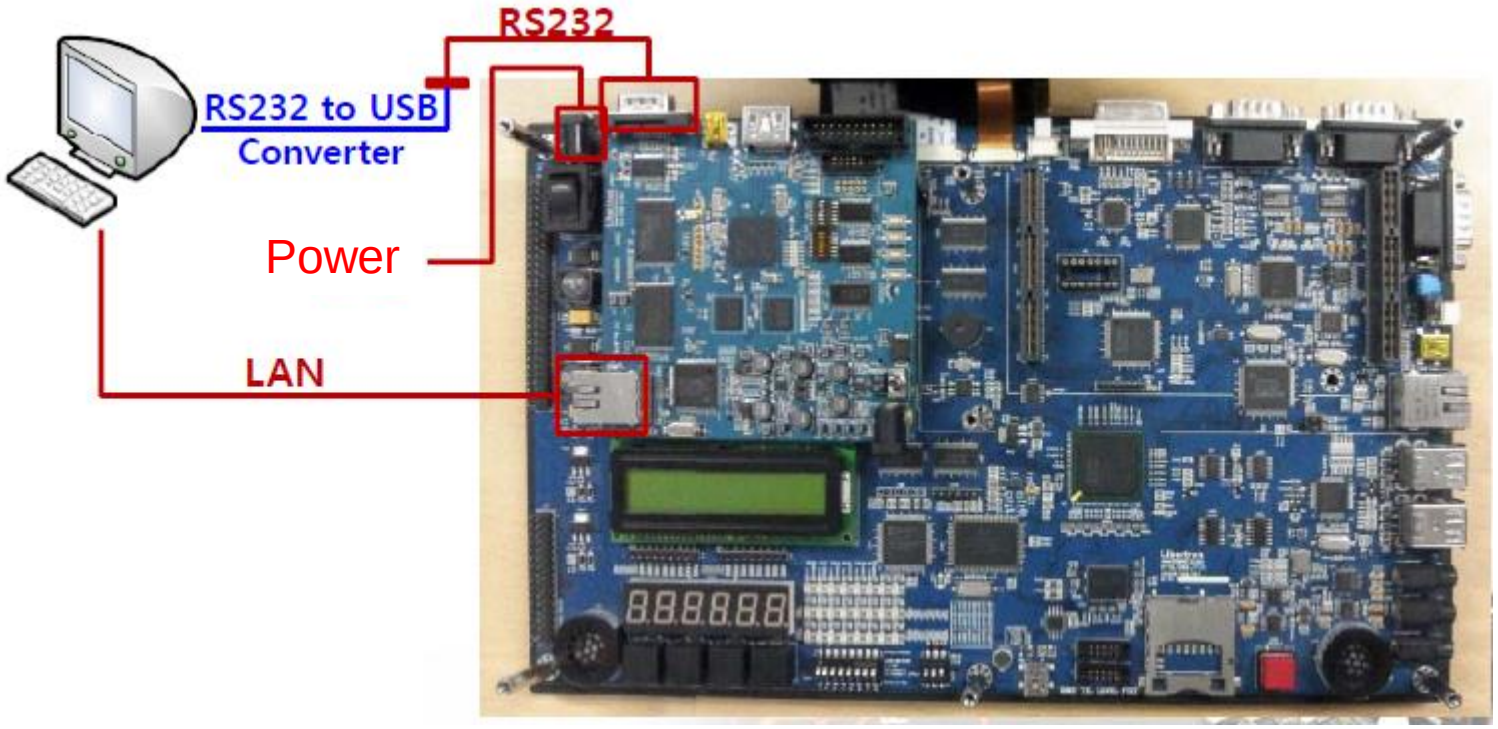
Network Settings

- **Network settings of virtual machine**
  - Configure Adapters
  - Check only ethernet adapters (not WIFI, Bluetooth)



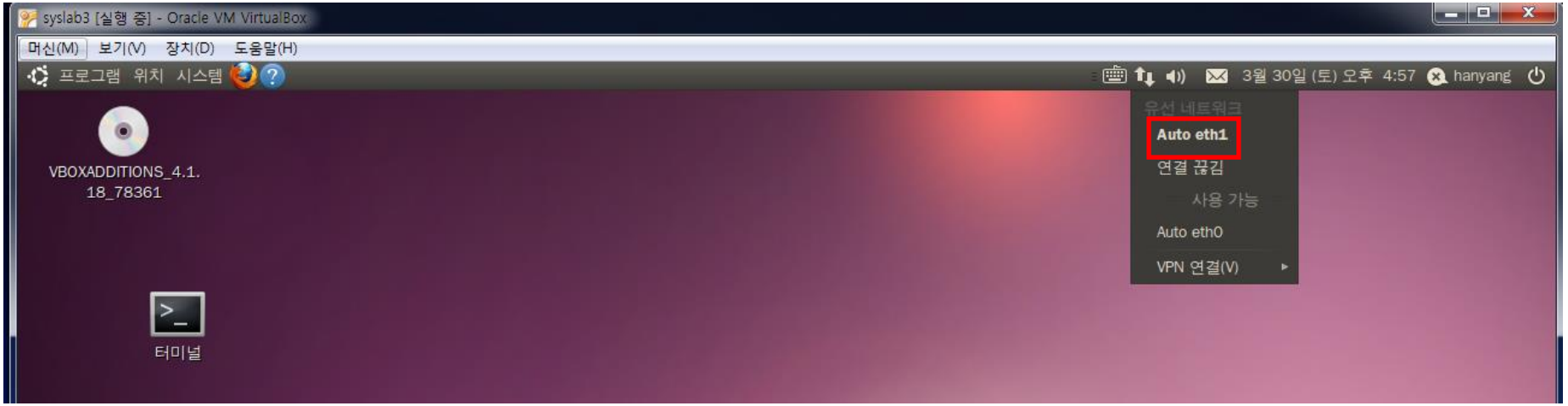
Network Connection

- **Network connection**
  - Connect LAN to pc



Network Connection

- **Connect the network to 'Auto eth1' after reboot Linux**



U-boot Porting

- **Network preferences**
  - Use the following U-boot command to set the IP of the host PC and the IP of the target board

    # setenv ipaddr 166.104.146.10 : save target board IP address

    # setenv serverip 166.104.146.5 : linux IP address

    # setenv gatewayip 166.104.146.1 : gateway address

    # setenv netmask 255.255.255.0 : netmask address

- **Save preferences**

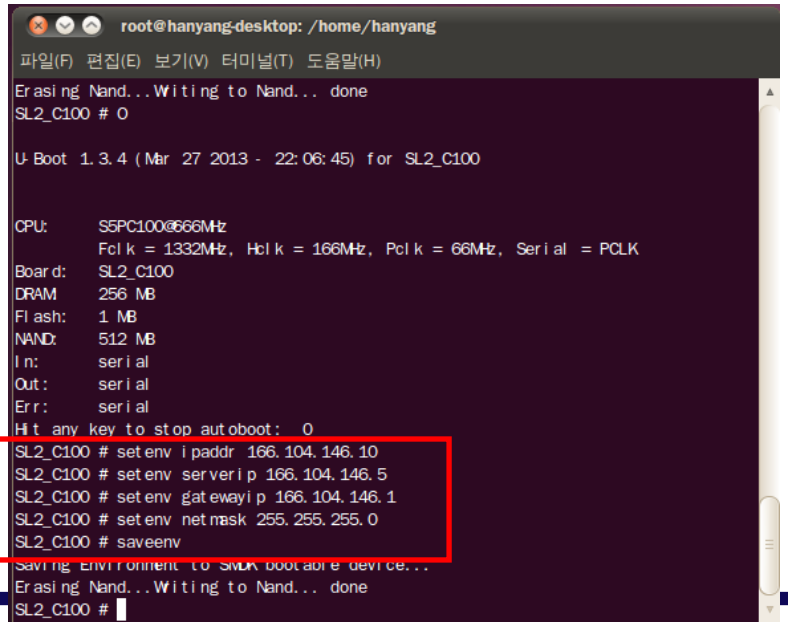
  # saveenv

- **Check network settings**

  # printenv

- **Check connection**

  # ping 166.104.146.5



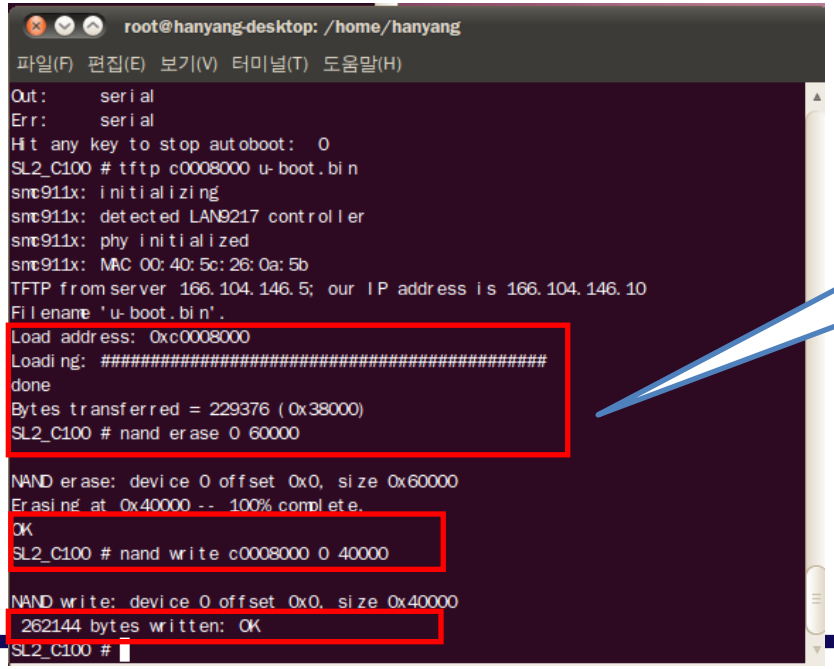
U-boot Porting

- **U-boot write using TFTP**
  - Write u-boot to nand with tftp using Network

    # tftp c0008000 u-boot.bin

    # nand erase 0 60000

    # nand write c0008000 0 40000



Make sure that
Load, Erase, and Write
operations are correct!

U-boot Porting

- **Reset board and press ‘Enter’**
  - Check if it has been changed to “Student ID #“
- **U-boot write complete**

VPOS BOOTING

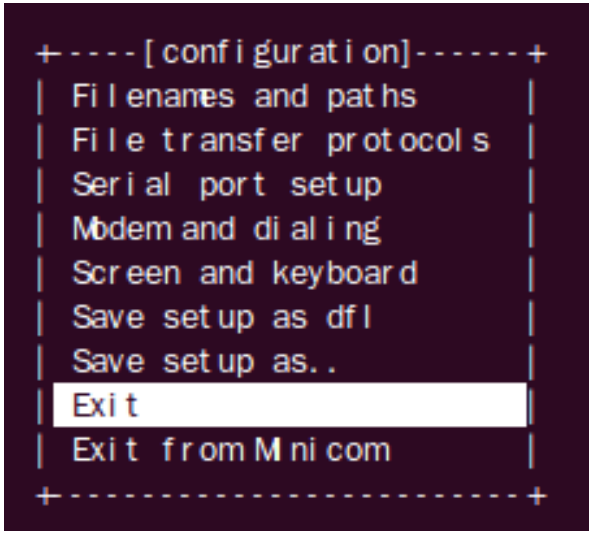
Prepare vpos Booting

- **Copy vpos.bin to /tftpboot**
  - Copy vpos.bin to /tftpboot

    >> cp vpos.bin /tftpboot

Minicom

- **Connect to Board with minicom**
  - (1) >> minicom
  - (2) >> minicom –s and ‘Exit’



Download VPOS Kernel (not using NAND)

- **In this practice, we write it directly to SDRAM without being stored in NAND**

- **Kernel write using TFTP**
  - Write vpos kernel to SDRAM with tftp using network

    ```
    # tftp c0008000 vpos.bin
    ```

  - Booting command

    ```
    # bootm c0008000
    ```

Download VPOS kernel (using NAND)

- **Kernel write using TFTP**
  - Write vpos kernel to nand with tftp using network

    # tftp c0008000 vpos.bin

    # nand erase 80000 400000

    # nand write c0008000 80000 400000

    # setenv bootcmd nand read c0008000 80000 300000\;bootm c0008000

    # saveenv

  - Booting command

    # boot

VPOS Booting

- **VPOS boot screen**

```
Board:    SL2_C100
DRAM      256 MB
Flash:    1 MB
NAND:     512 MB
In:       serial
Out:      serial
Err:      serial
Hit any key to stop autoboot:  0
HANYANG # boot

NAND read: device 0 offset 0x80000, size 0x300000
 3145728 bytes read: OK
Boot with zImage

Starting kernel ...


*****************************************************************************
*  QURIX version 3.0    xx/10/2012                                          *
*****************************************************************************

Race condition value = 1191214

Shell>
```

ASSIGNMENT

Assignment

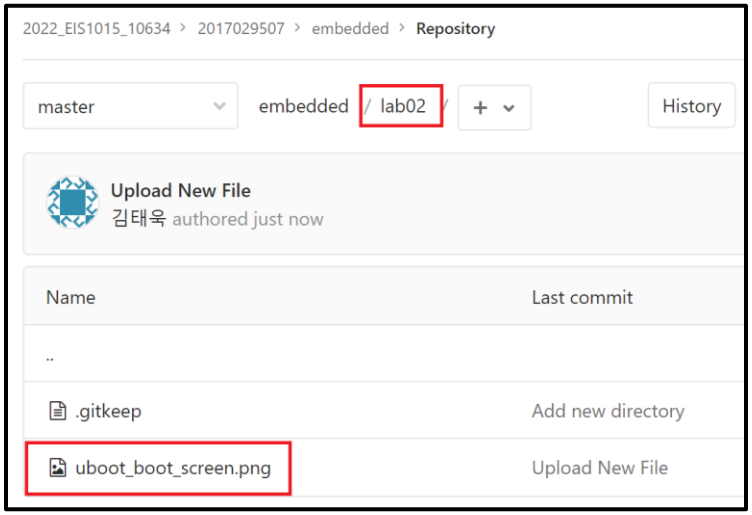
Upload modified u-boot boot screen on your gitlab

```
U-Boot 1.3.4 (Mar  9 2022 - 19:36:05) for SL2_C100


CPU:     S5PC100@666MHz
         Fclk = 1332MHz, Hclk = 166MHz, Pclk = 66MHz, Serial = PCLK
Board:   SL2_C100
DRAM:    256 MB
Flash:   1 MB
NAND:    512 MB
In:      serial
Out:     serial
Err:     serial
Hit any key to stop autoboot:  0
2022xxxxxx #
2022xxxxxx #
2022xxxxxx #
2022xxxxxx #
2022xxxxxx #
```

example image file to upload

(Student ID must be shown)



create “lab02” directory and

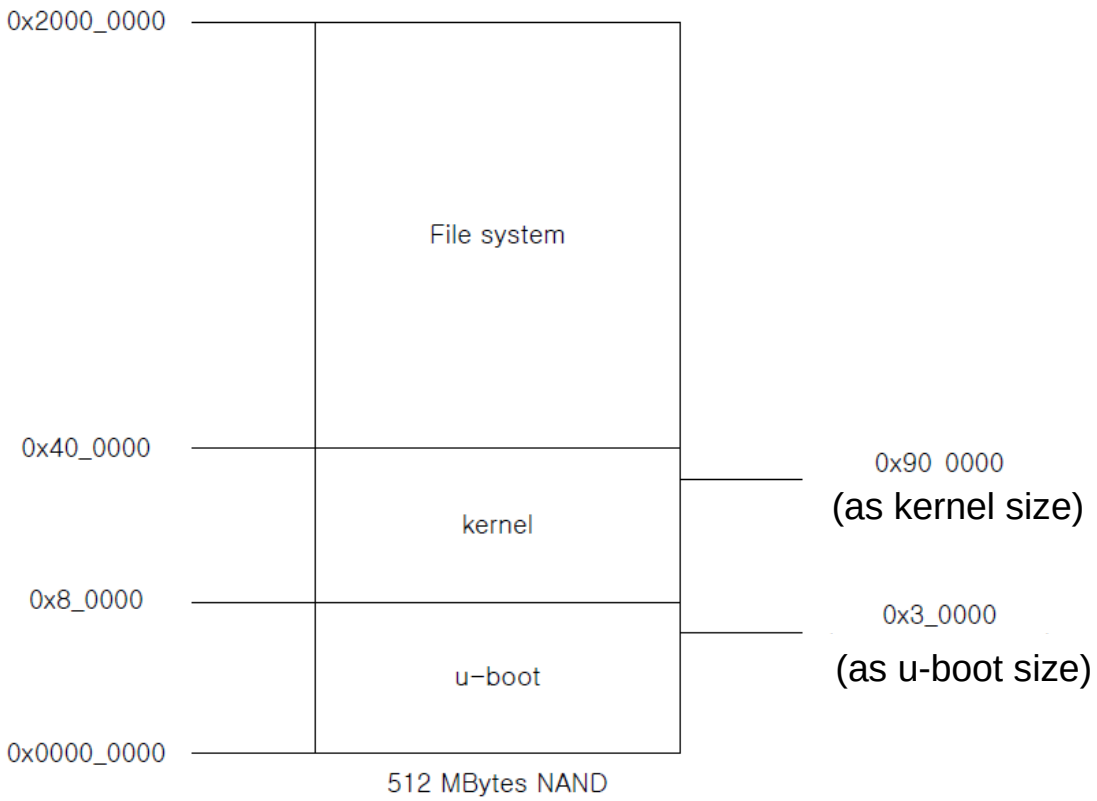
upload uboot screen file

Deadline : 3/18 11:59pm

APPENDIX

Location of U-boot and Kernel in NAND Flash

- **NAND Flash Address Map of SYS-LAB II board**



Location of U-boot and Kernel in mDDR

- **What is mDDR?**
  - Double Data rate synchronous DRAM for mobile computers
  - Low power consumption than conventional DDR SDRAM

- **U-boot**
  - Physical address : 0x27e00000
  - Virtual address : 0xc7e00000

- **Kernel**
  - Physical address : 0x20008000
  - Virtual address : 0xc0008000

U-Boot Command

- **What is the meaning of “tftp c0008000 file_name”**
  - Write a file at 0xc0008000 of SDRAM
  - If only u-boot is booted, kernel memory space is empty
    So we can temporarily write data to that empty memory space
  - Since u-boot uses MMU, it accesses memory with virtual address

- **What is the meaning of “nand write c0008000 0 40000”**
  - Write the data in 0xc0008000 of SDRAM to 0x0 of NAND Flash
  - U-boot or Vpos are temporarily stored in 0xc0008000 of SDRAM
    So, we write data to the NAND Flash to store data permanently

Thank you