Optimizing PageRank Algorithm

CSC 485C/586C - Interim Report 1

Kevin(Zhe) Chen Yiming Sun Xiangwen Zheng Feb 23, 2020

Introduction

Given a huge set of website data, it can be very difficult for one to retrieve the webpage with the desired media contents. If we treat such web search problems as a simple graph problem, the task would be to find the centrality of the graph network according to one's interests. PageRank is an algorithm used by Google Search to rank websites in their search engine results. PageRank was named after Larry Page, one of the founders of Google. PageRank is a way of measuring the importance of website pages [1]. Also as Google has described, PageRank works by counting the number and quality of links to a page to determine a rough estimate of how important the website is. The underlying assumption is that more important websites are likely to receive more links from other websites. PageRank is a classical graph ranking algorithm that could not only be applied to website search engines, but also applicable on many other applications such as to evaluate the "importance" of a person in a social network, or to evaluate the weight of a package in software dependency networks, etc.

The existing algorithm we found has several problems: the running time for a large dataset can be improved and the cache hit rate is low. The area that will be investigated is the data structure using and the algorithms. The existing algorithm utilizes an array of struct (AoS) to restore the data, and some of the attributes (properties) of the objects are not even frequently used. Also, the tiling technique is possible to be implemented in the main algorithm to maximize the cache hit rates when the code is iterating through the nodes.

The reason that most drives us to dive into this problem is the popularity and impact on today's browsers and search engines. Once the number of pages increases massively, the more time and memory will be taken. Our purpose is to optimize it as much as possible.

Algorithm Description

In this project report, we will be focusing on the simplified version of the PageRank algorithm. Figure 1 represents the relationships between several web pages using a simple directed graph with nodes being the web pages and edges being the citation relationships. In the given graph, node A is pointing to node B and node C,

node B is pointing to node D, node C is pointing to node A, node B, and node D. In a practical context, the content of nodes will contain sufficient information about the pages, which will not be considered in our case due to a simplified introduction.

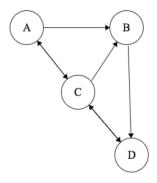


Figure. 1 simple graph example

A naive formula that was consistently used in PageRank algorithm:

$$PR(A) = \frac{PR(T1)}{C(T1)} + ... + \frac{PR(Tn)}{C(Tn)}$$

where PR(A) indicates the PageRank score of node A, T1...Tn are the set of nodes that directly point to node A, C(A) refers to the number of outgoing pointers from node A.

At the initial state, the page score will be 1/n for every page node, where n is the number of the page nodes. The score indicates the rank of the page thus gives importance and priority to each page node. In the next iteration, we need to consider how many nodes are pointing to the current node. For node A, there is only pointed by one node which is node C. The PageRank score of node C from the last iteration is $\frac{1}{4}$, and node C has three outgoing pointers. The PageRank score of node A will be calculated as:

$$PR(A) = \frac{1/4}{3} = \frac{1}{12}$$

Using the same methodology, scores after the second iteration for nodes A, B, C, D are $\frac{1}{12}$, $\frac{2.5}{12}$, $\frac{4.5}{12}$, $\frac{4}{12}$ respectively. The number of iterations varies depending on the problem's context and practical situation. When the PageRank scores do not change significantly overall, the iteration can be stopped and finished. The process should stop under a normal circumstance when PageRank score differences are smaller than 0.001.

The simplified implementation of the PageRank algorithm does not cover the Table 1 shows the result after three iterations:

Iteration	0	1	2	PageRank
A	1/4	1/12	1.5/12	1
В	1/4	2.5/12	2/12	2
С	1/4	4.5/12	4.5/12	4
D	1/4	4/12	4/12	3

Table. 1 results after three iterations

After introducing the fundamental idea of this algorithm, we are going to utilize it in a more complicated context. Here is the pseudocode of this algorithm:

```
for iteration in range of num_iteration:
    if the iteration is the first one:
        set all the (current) scores to 1/n;
    else:
        set all the previous scores with corresponding scores;
        set all the scores to 0;
        for node A in all the nodes:
            for node B in node A's predecessors:
            A.score += B.prev score / B.num successors;
```

In the provided example, the iteration number is constant and large (e.g. 500 or above). In real-world problems, the iteration will be done until the results (PageRank score) roughly converge to a number. The reason in our case we did not do this way is because we want to compare the difference of performance and the perf results when the iteration number is different.

The input can be fairly simple, we choose the edges list contained in the text file to be our input format with a list of two-column number sets separated by space where the first column represents the source nodes, and the second column represents the target nodes that are pointed. The output is the list of page nodes along with their final PageRank scores. Figure 2 is an example of a small size test file. Frankly, the order of the nodes automatically sorted is ascendingly.

```
1
    0 1
 2
    1 2
 3
    1 8
 4
    2 3
 5
    2 13
    3 4
 6
 7
    3 16
8
    4 5
9
    5 6
10
    5 19
11
    6 7
    6 20
12
```

Figure. 2 simple test file example

In terms of storing and structuring the input data, AoS is the data structure being used. Firstly, an object called Node is used to contain the page information,

which includes id (page ID), a node vector called nodesFrom that contains a list of nodes point to this node, an integer type called countTo that counts the number of this node is pointing to, a variable called scorePrev that stores the PageRank score of this node from the previous iteration, and the variable score that stores score in current iteration. The following is how the array of the structure looks like:

Experiment Setup

In order to test the algorithm for its accuracy and correctness, only the time of the algorithm part will be measured, instead of including the measurement of reading files and storing dataset. Additionally, several different datasets with different cases necessary. For example, ordered data, unordered data, dangling type data, and sink type data. Firstly, a fair large test file is necessary for stress tests. Therefore,

we planned to generate around twenty different text test files with different sizes of nodes inside from 5 to 40,000 nodes. At the current stage, the focus was put on a 10,000-nodes and a 50,000-nodes dataset, both with 500 iterations. All the connections and directions between nodes are totally random. The test files with small sizes will make sure that the algorithm is correct and doing the right things, and the content of the text files are easy to be changed for testing in different cases. As we observed, the running time and cache hit results can be varied if the test files are small. Therefore, performing in a larger dataset is crucial for visualizing the difference before and after optimization. In the Linux-Perf[3] command, the following two figures (Figure 3 and Figure 4) are generated respectively for AoS and SoA. For our experiment, instruction per cycle, branch miss rate and L1-cache miss rate are the focus.

```
124.40 msec task-clock
                                            0.935 CPUs utilized
        2 context-switches
                                            0.016 K/sec
                                            0.000 K/sec
        0
               cpu-migrations
                                        #
       365
               page-faults
                                            0.003 M/sec
               cycles
468,918,147
                                            3.770 GHz
                                                                          (48.82%)
296,173,339
               instructions
                                            0.63 insn per cycle
                                                                          (61.68%)
75,956,895
               branches
                                        # 610.604 M/sec
                                                                           (61,68%)
 4,469,435
               branch-misses
                                        # 5.88% of all branches
                                                                          (61,68%)
99.741.314
               L1-dcache-loads
                                       # 801.802 M/sec
                                                                          (54.39%)
20,598,975
               L1-dcache-load-misses
                                      # 20.65% of all L1-dcache hits
                                                                          (25.46%)
14,034,584
               LLC-loads
                                        # 112.821 M/sec
                                                                          (25.72%)
    22,435
               LLC-load-misses
                                        # 0.16% of all LL-cache hits
                                                                          (37.27\%)
0.133052256 seconds time elapsed
0.116719000 seconds user
0.008049000 seconds svs
```

Figure. 3 AoS (Baseline) Perf result

```
70.11 msec task-clock
                                             0.864 CPUs utilized
               context-switches
                                        #
                                             0.071 K/sec
                                             0.000 K/sec
               cpu-migrations
       306
               page-faults
                                        #
                                             0.004 M/sec
252,824,111
                cycles
                                             3.606 GHz
                                                                           (51.60%)
344,384,679
               instructions
                                             1.36 insn per cycle
                                                                           (64.54%)
 52,049,147
               branches
                                                                           (64.54%)
                                        # 742.349 M/sec
 2.317.033
               branch-misses
                                        # 4.45% of all branches
                                                                           (65.80%)
 96,485,671
               L1-dcache-loads
                                        # 1376.124 M/sec
                                                                           (48,40%)
 8,405,182
               L1-dcache-load-misses
                                            8.71% of all L1-dcache hits
                                                                           (22.80%)
 1,099,125
               LLC-loads
                                        # 15.676 M/sec
                                                                           (22.81%)
               LLC-load-misses
                                        # 0.06% of all LL-cache hits
                                                                          (34.76%)
       664
0.081160789 seconds time elapsed
0.058018000 seconds user
0.012432000 seconds sys
```

Figure. 4 SoA Perf result

For the future experiment, special cases of the dataset will be tested for more details and purposes. The first special case is called dangling node case, which means there are some nodes in the dataset that are not pointing to any other nodes. The second special case is called the sink node case, which means there are some nodes in the dataset that are not pointed by other nodes.

Single-threaded Optimization

In our results from the baseline, we noticed that the L1 cache miss ratio is significantly high (20%), thus increasing the spatial locality is the priority. In the conventional AoS data structure, the data that we access frequently (i.e. the scores) is not contiguous. As a result, the data that we access next is not likely loaded in the cache line.

So, as our first optimization step, we decided to change the data structure that stores nodes from AoS to the struct of array (SoA). The key is that, even though the access of nodes is random, all the scores are stored in the same array (vector), so it is ensured that we can load

as many scores to the cache as possible. Compared to Code snippet 1 in the Algorithm Description section, Code snippet 2 is the SoA implementation. Some attributes are deleted here because they are not going to be used at all, and it would be a big waste if those irrelevant data is kept operating. Figure 3 and Figure 4 are the Perf results when AoS and SoA are used.

```
struct Nodes
{
     vector< vector< Id > >
nodesFrom;
     vector< Count > countTo;
     vector< Score > score;
     vector< Score > scorePrev;
};

Code snippet. 2 SoA
```

In addition, we have explored alternative way to store the current scores and previous scores with only one vector. For a graph of N nodes, use a vector of size 2*N. For each node with ID = i, the index of the previous score of such node is 2*i, and the index of the current score is 2*i + 1. The purpose is that since we assign the previous scores with the current scores, and the current scores back to 0's in every iteration, putting each current score next to its corresponding should theoretically previous score increase locality. However, the results turn out to be making no difference than the two-vector-score structure, so the idea will not be discussed further.

Overall Comparison

The following table (Table 2) is generated from the Perf results we got. In this case, we compare when the dataset sizes are 10,000 and 50,000. As we observe (from Figure 3 and Figure 4) in the size interval between 10,000 and 50,000. The number of branch misses, the number of cache misses, and runtimes increase almost linearly, which is expected because there is much more work to do as the dataset has greater size. But switching data structure from AoS to SoA significantly improves the code performance. Since every time we ran the perf, the results vary a little bit, so the result we take the average out of ten attempts. The branch miss is not improved much because our branch situation is rare, there are not many chances to improve for this part. Clearly, SoA reduces the cache miss massively, it is over a half less compared to AoS. That is expected for switching AoS to SoA since SoA's feature is maximizing the cache hits and improving the performance. The running time reduced to 1/3, which is also desired.

	AoS		SoA	
	N = 10,000	N = 50,000	N = 10,000	N = 50,000
Instruction / Cycle	0.63	0.57	1.36	1.36
Branch Miss Rate	5.88%	6.39%	4.45%	5.60%
L1 Cache Miss Rate	20.65%	22.29%	8.71%	10.21%
Runtime (microseconds)	98,938	516,765	36,902	183,370

Table. 2 Benchmarking

Diagnostics

From the experimental results, back-end (memory) boundness is the primary cause of latencies. In the beginning stage of our project, we implemented a full Node struct which allows a page node to have complete access to both its incoming and outgoing node lists. However, we identified that some of the data were not accessed by the algorithms at all, hence, we have removed those cold data in order to reduce the memory load. and After implementing our baseline of naive PageRank algorithm, by using both Linux-Perf and our manual inspection, we have noticed that the L1 cache miss ratio to be more than 20%. This is because our code accesses data in different places in the Node vector numerous times during the computation iteration process which further affects the program spatial locality performance.

Limitations and Future works

As we have discussed from the sections above, our works have been proved to generate several observable improvements over the baseline algorithm. However, due to the limited timeframe set on this project, there are several paths we have not yet done but would like to try to improve our current works. Our work tested on very little input data and only mainly focused on the 10,000-node and 50,000-node ones. To enhance our findings and statements, in our next step, we would like to generate and test input sets with differences in the number of nodes/edges and max degree (number of outgoing edges). The current

implementation the PageRank on algorithm is experiencing a technical issue which results in inaccurate PageRank scores due to an edge case regarding dangling nodes that holds no outgoing edges, which makes the score of such node to be not accurate. In order to solve this problem, we plan to apply matrix operation on the PageRank Algorithm introduced by Kenneth Shum [2]. For our existing implementation, there is very little chance we could take the advantage of tiling technique we learned from the class, but this will be discussed more for the future step where we have adopted the matrix version of the implementation of the PageRank algorithm.

References

[1] GeeksforGeeks:

https://www.geeksforgeeks.org/page-rank-algorithm-implementation/. (last visited on 02/22/2020).

[2] Kenneth Shum. Notes on PageRank Algorithm 1 Simplified.

http://home.ie.cuhk.edu.hk/~wkshum/papers/pagerank.pdf . (last visited on 02/23/2020).

[3] Linux-Perf manual.

http://man7.org/linux/man-pages/man1/perf.1.html . (last visited on 02/19/2020).