

Sibgatullin Bulat

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Projects

- **Bachelors first-year projects:**

There is [repository](#) with my first year ILab course projects.

GitHub: Sibonji/iLab-repo .

Also I have done some ASM projects for my informatics MIPT course during second semester. And a tic-tac-toe project on STM32 for STM32 DREC course.

- **Bachelors second-year projects:**

There is [repository](#) with my 3 semester Computer Technologies course. There are 5 projects, written in C and built with Make in this repository.

GitHub: Sibonji/Computer-technologies .

My skills

- **C/C++**

- Basic skills in **ASM**.

- Basic skills in **Python**.

- Good practice skills in and **Make**.

- Good practice skills in **Tex**.

- Intermediate **CPU architecture** knowledge.

- Basic knowledge of **RC circuits**

- B1 **English** level.

Higher education

Second-year bachelors student in MIPT; Department of Radio Engineering and Cybernetics (DREC); Applied physics and mathematics.

Completed additional courses

- Industrial programming course - during bachelors first-year in MIPT.
Lecturer - I. Dedinsky.

- Intel ILAB C++ course - during bachelors second-year in MIPT.
Lecturer - K. Vladimirov.

- Intel ILAB micro architecture course - during bachelors second-year in MIPT.
Lecturer - K. Korolev.

- DREC programming on STM32 microcontrollers course.