



Logic Informatic introduction Lecture 1

DR. Herlina Jayadianti., S.T., M.T

- Herlina Jayadianti
- S1 UPN 1997-2001
- S2 UGM Teknik Elektro 2005-2007
- S3
- UGM Teknik Elektro 2008-2010
- Universidade doMinho Portugal SDI 2010-2013
- University Trento Italy 2013
- UGM Teknik Elektro 2013-2015

SUBJECT

- Proposition logic
 - Expression Rules Logic
 - Sentence and Substitution
 - Method of proof
 - Normal form
 - Calculus Predicate, Representation Sentence
 - Sentence interpretation and validity
 - Valid and Substitution Scheme
 - Introduction of Resolution
 - Application of Logic Information

Evaluation 1 and Evaluation 2

- Evaluation 1 (Class meeting 8)
- Evaluation 2 (Class meeting 16)
- **Open book**



Point

- Homework (10%)
- Presence (10%)
- Evaluation 1 (30%)
- Evaluation 2 (40%)
- Activity (10%)

References

1. Soesianto, F dan Dwijono, D.,2003, "*Logika Matematika untuk Ilmu Komputer*" Yogyakarta : Andi offset
2. ITB bahan ajar – rinaldi munir – matematika diskrit
<http://informatika.stei.itb.ac.id/~rinaldi.munir/Matdis/matdis.htm>
3. Daliyo GP, "*Materi Kuliah Logika Informatika*", Universitas Gadjah Mada, Yogyakarta
4. Manna, Z and Waldinger, R., 1985, "*The logical basis for computer programming*", Addison-Wesley Publishing Company.
5. Arindama Singh , 2004 ; "*Logics For Computer Science* ", Prentice Hall of India
6. Klement, Kevin C. "*Lecturer Notes for Mathematical Logic*" University of Massachusset
7. Huth, Michael, "*Logic in Computer Science*" Cambridge
8. Reeves, Steve and Clarke, Mike, "*Logic for Computer Science*" ,University of London, United Kingdom

Thank you

See you next week