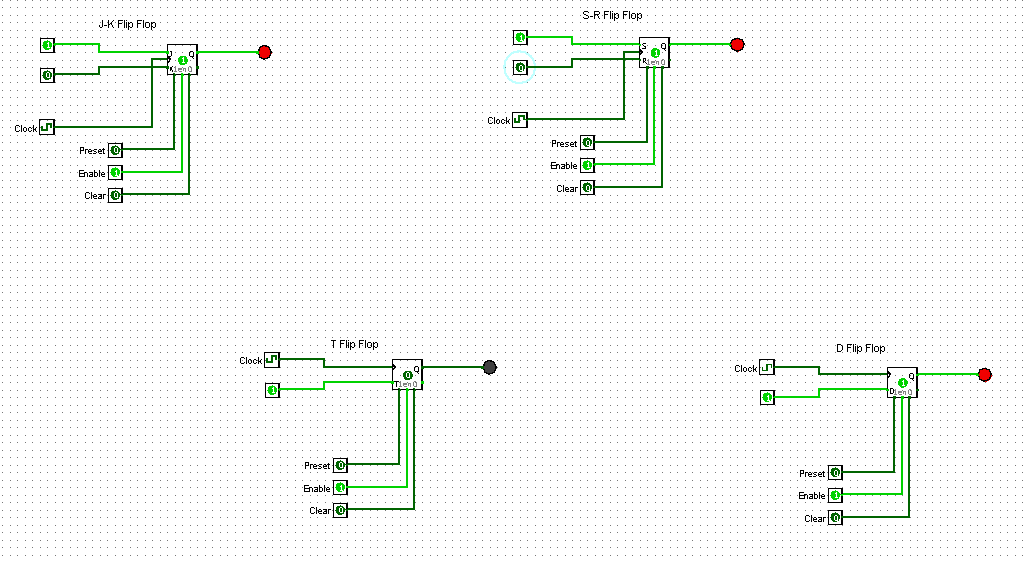
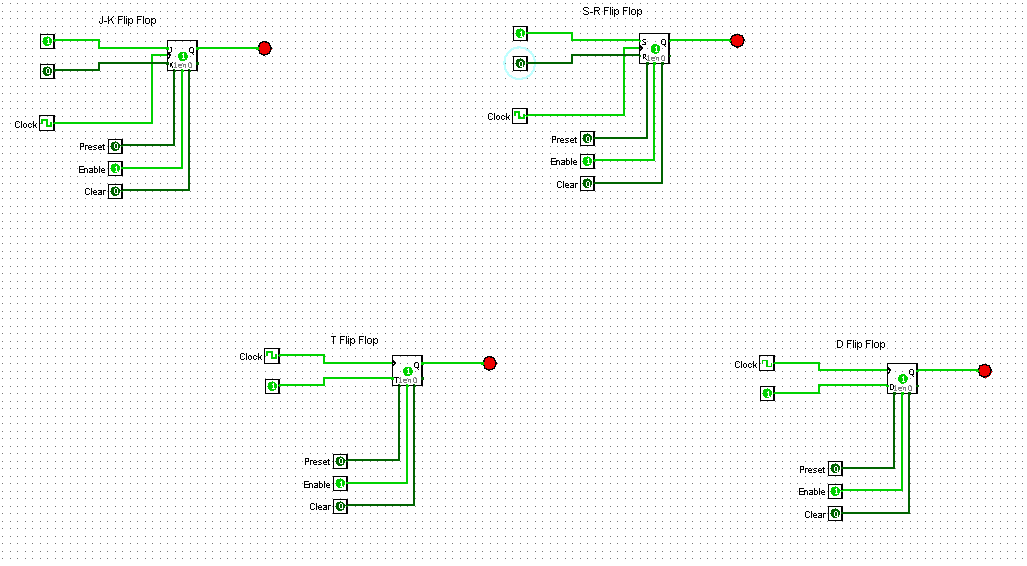
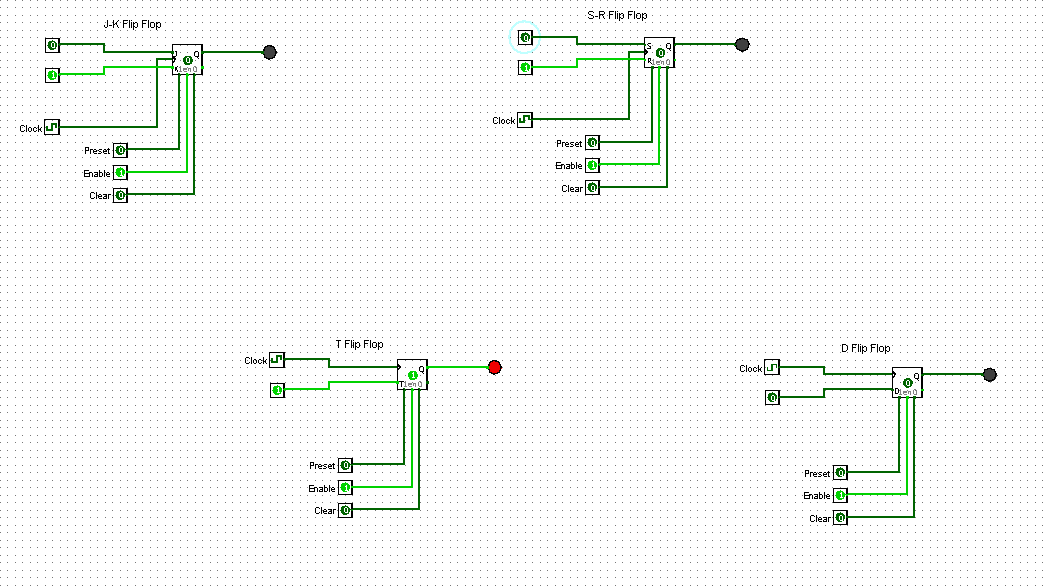
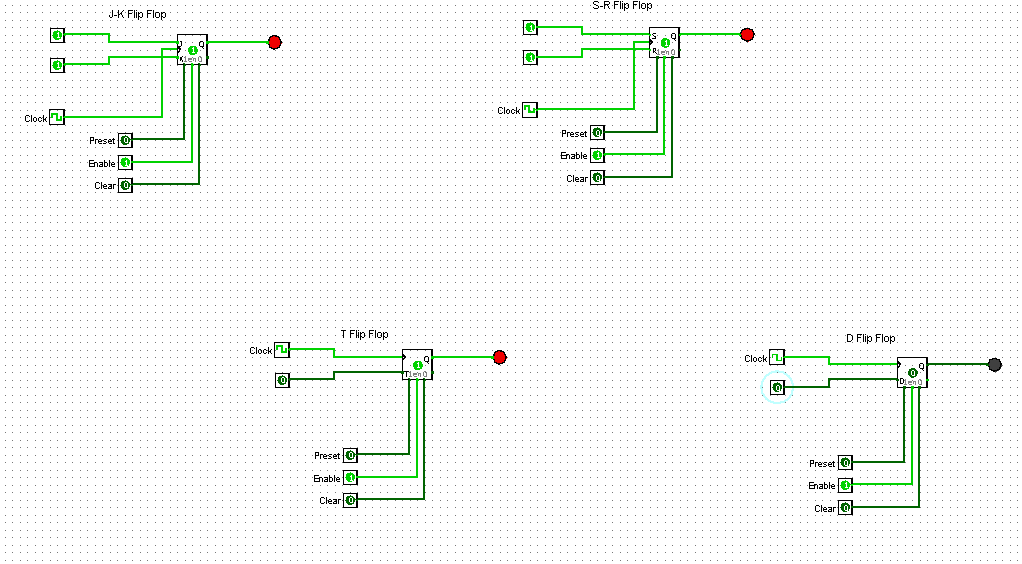
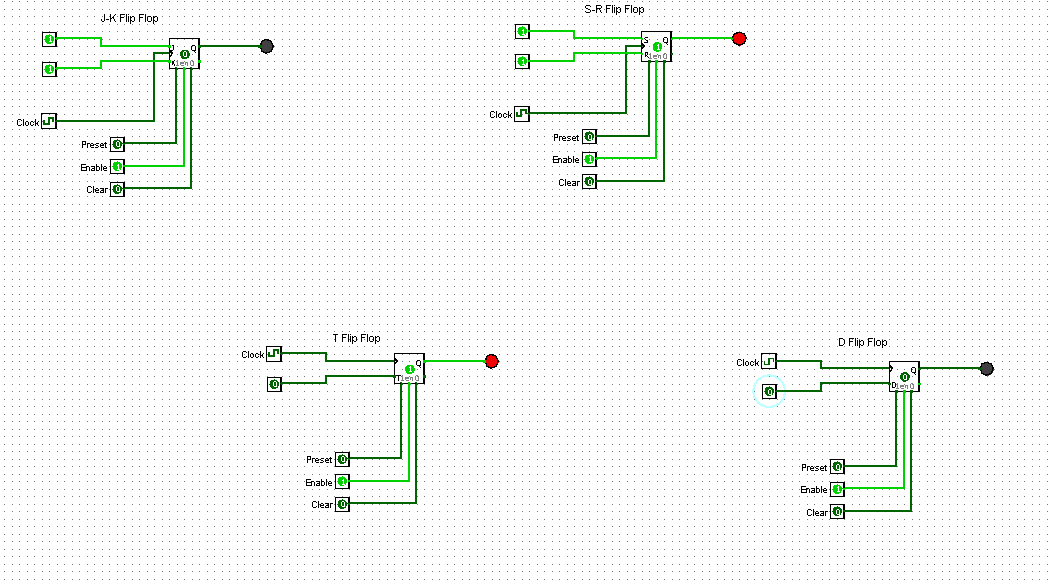
Q0) J-K Flip flop , S-R Flip Flop , D Flip Flop , T Flow

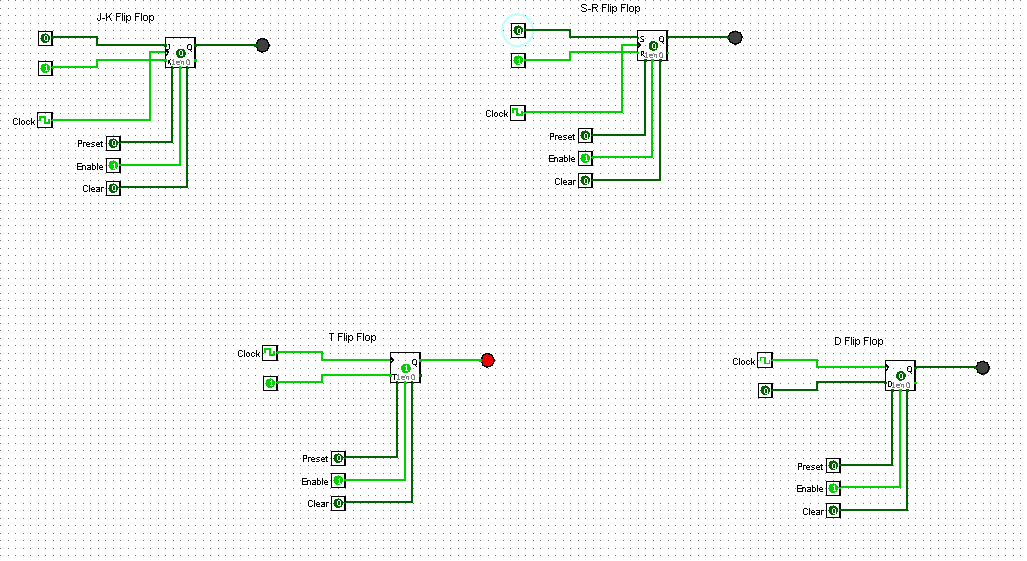




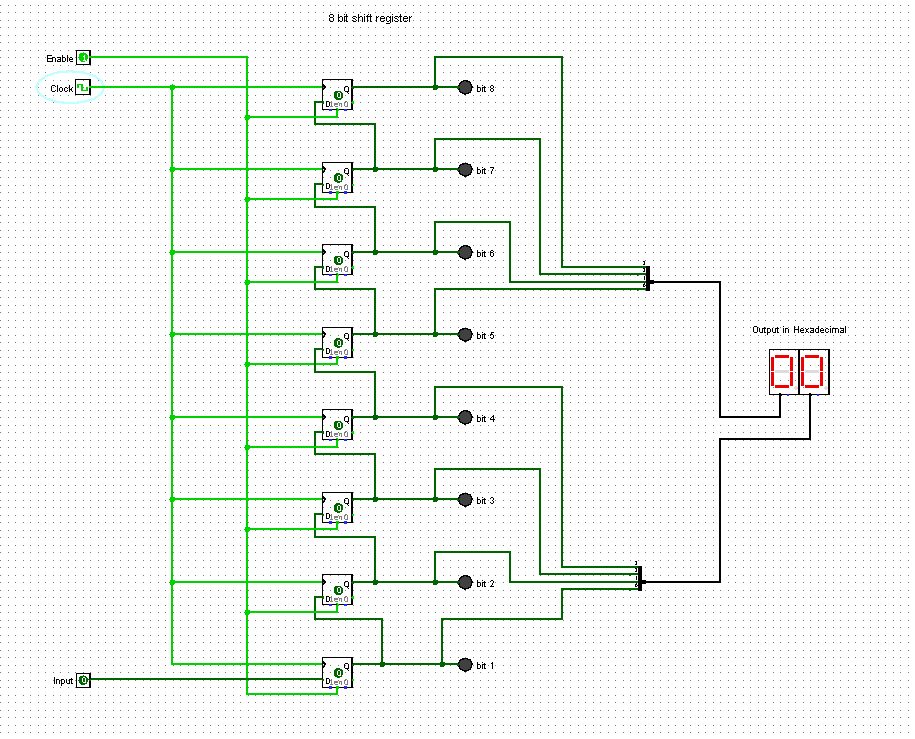


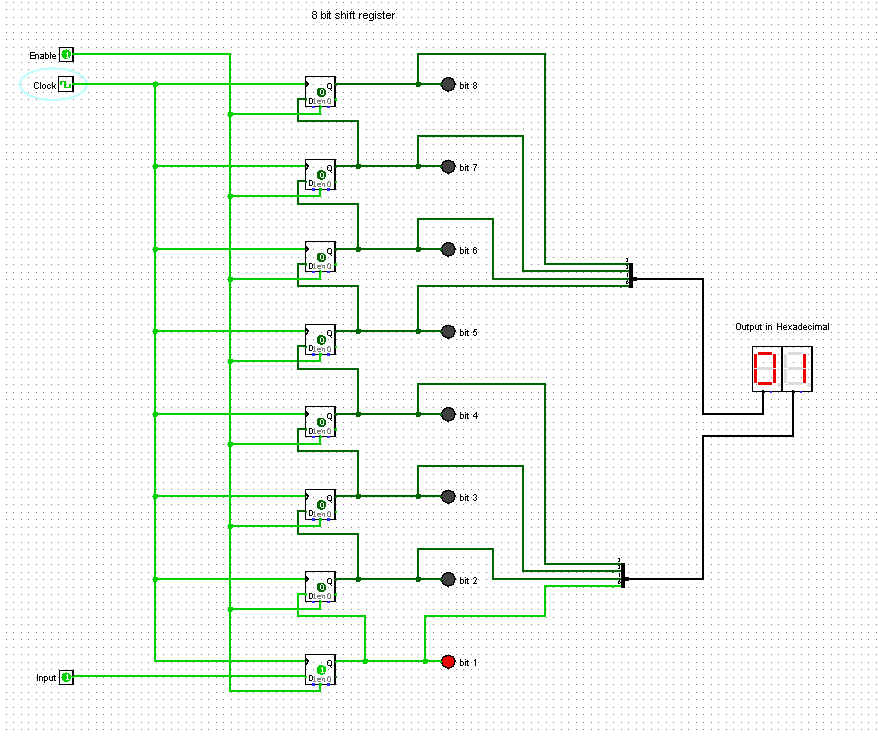


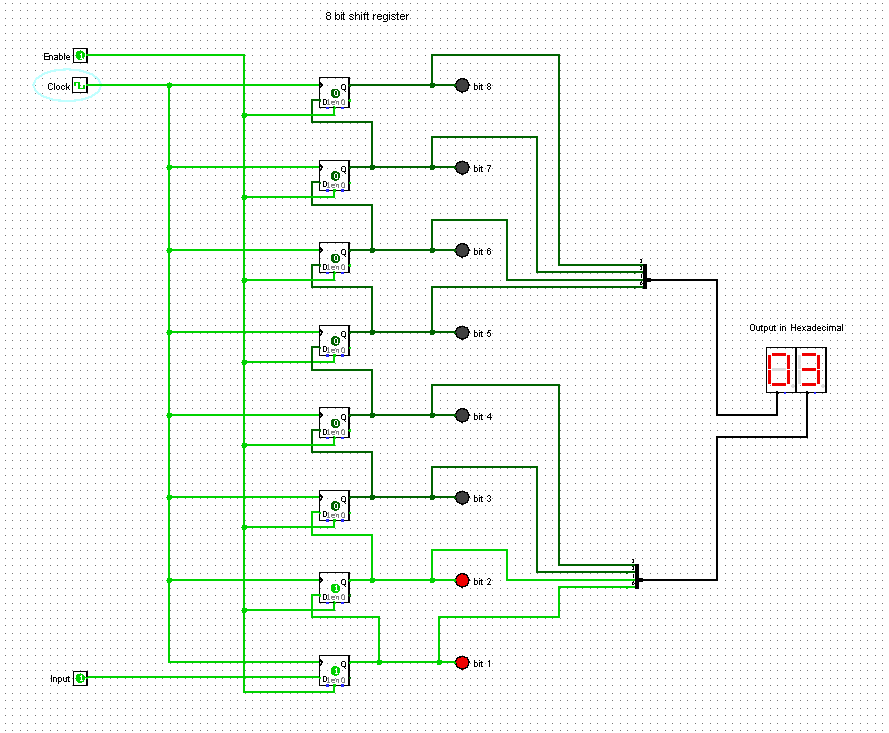


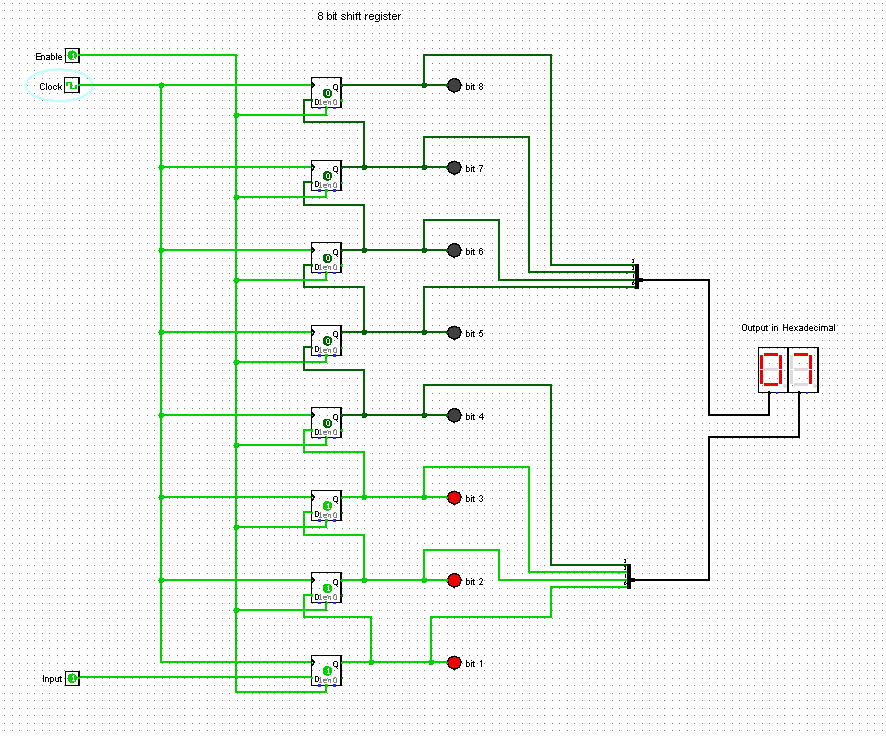


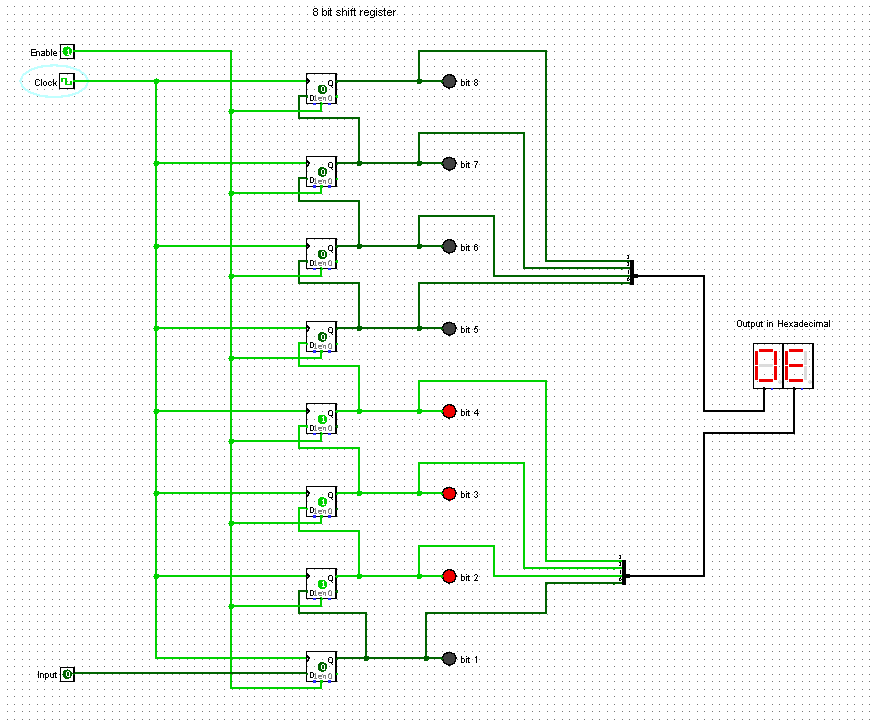
Q1)8 Bit shift register:

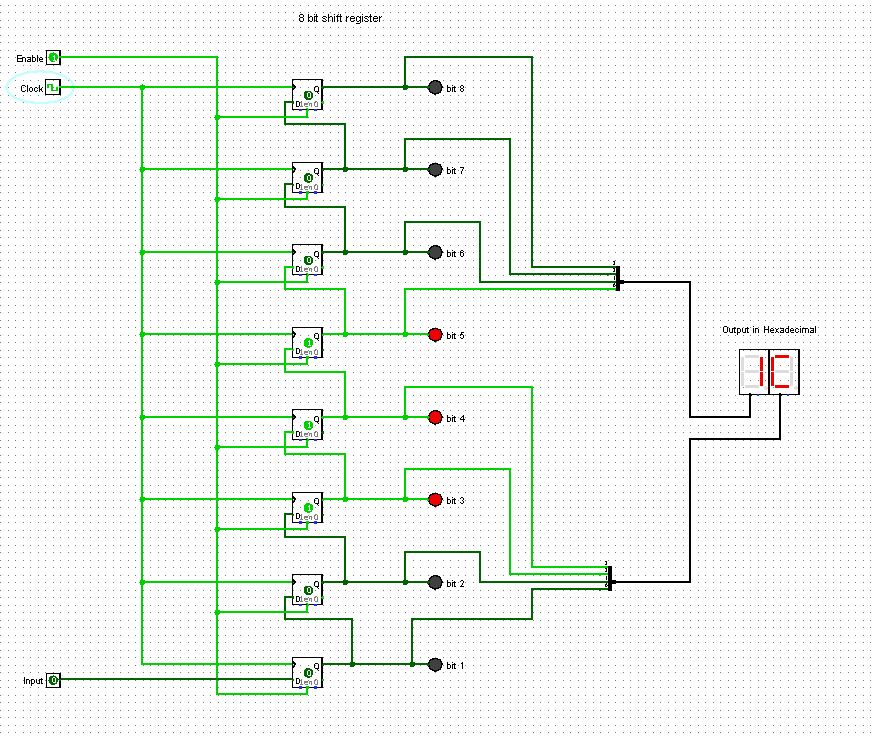


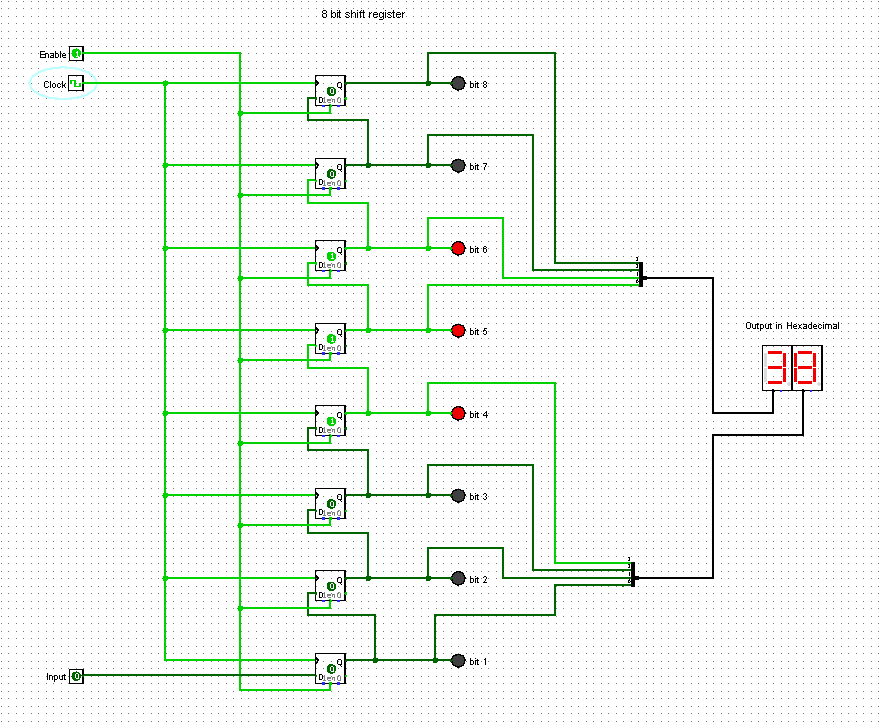




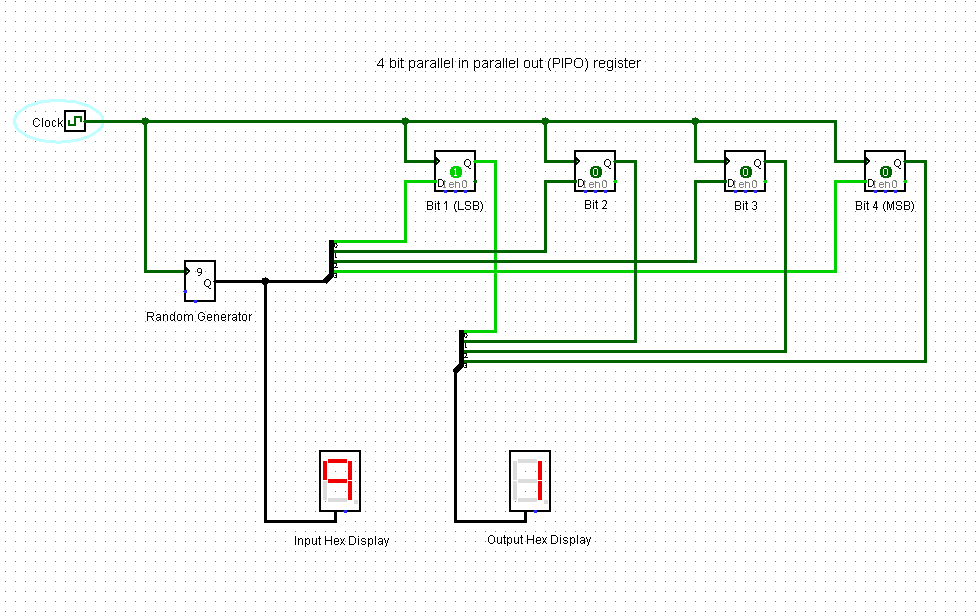
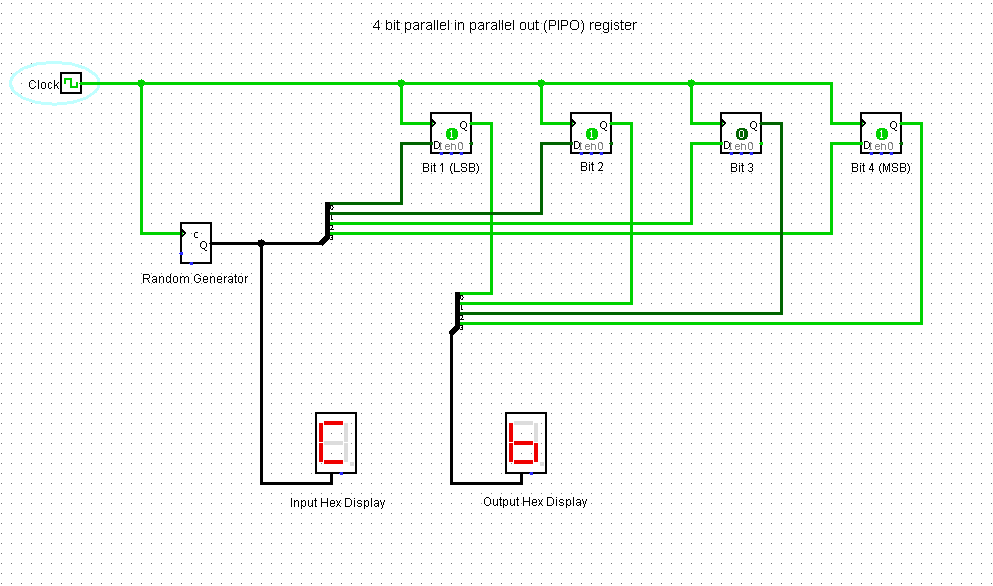


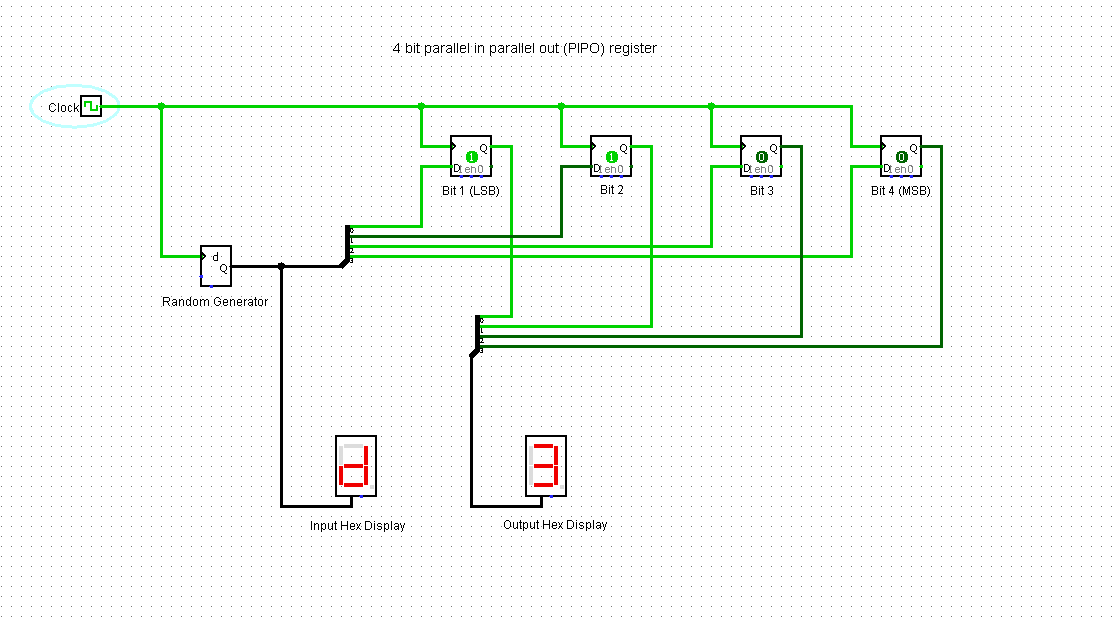




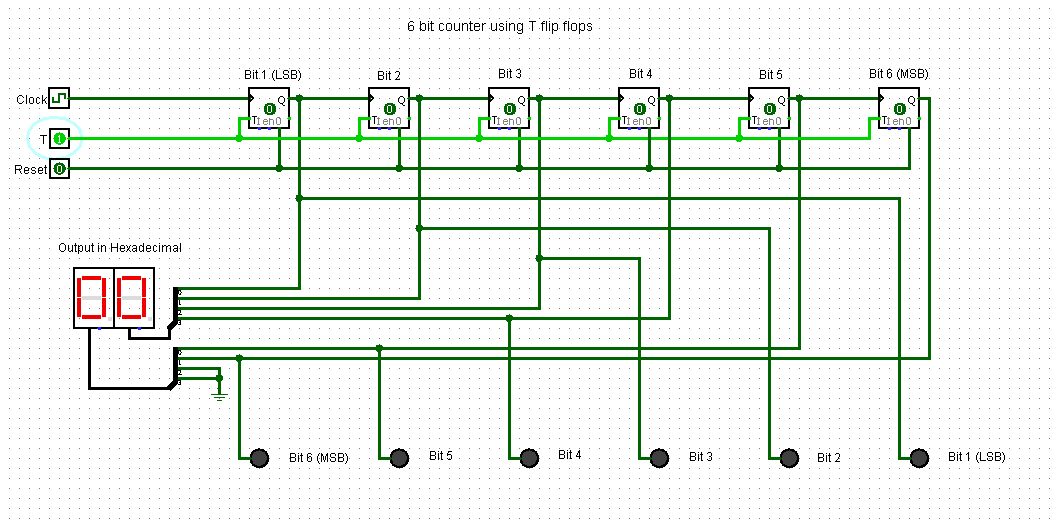


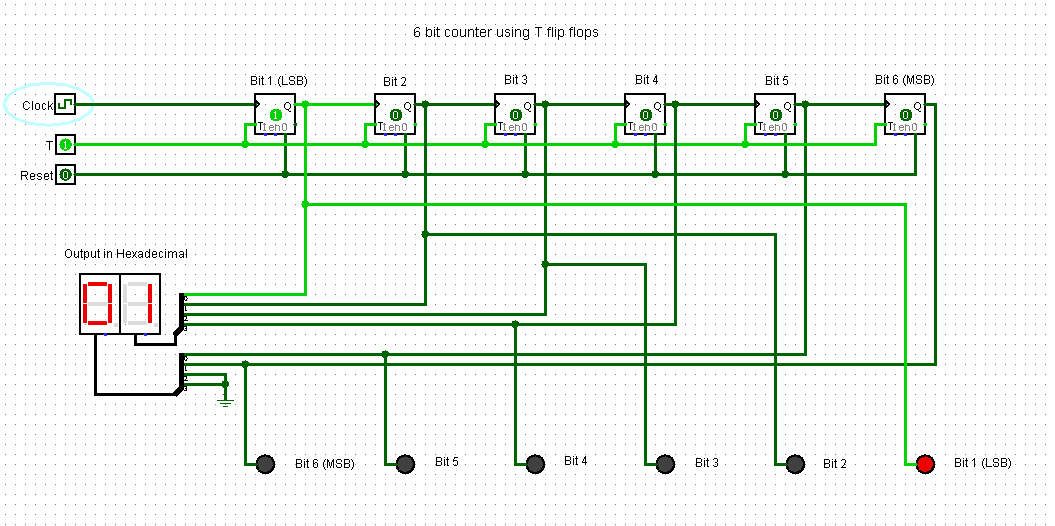
Q2)4 Bit PIPO register:

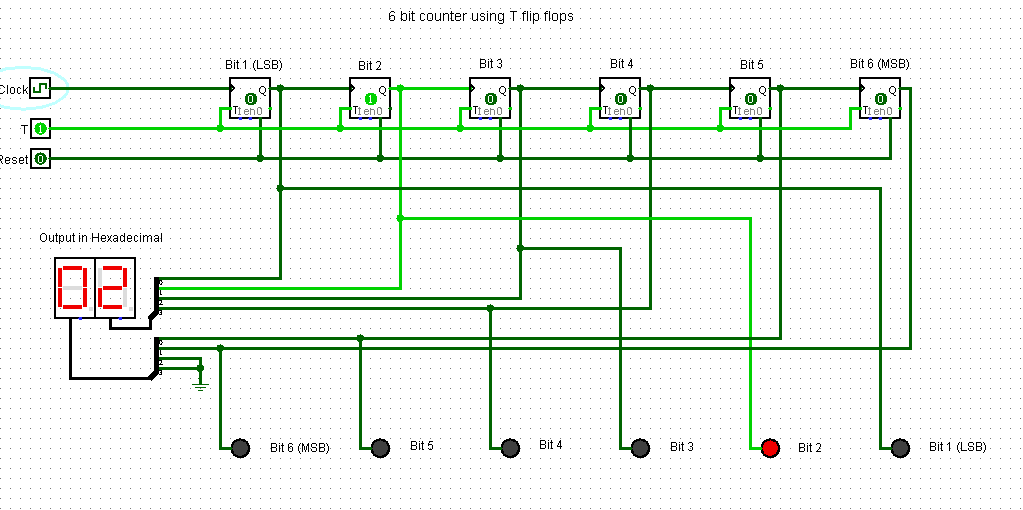


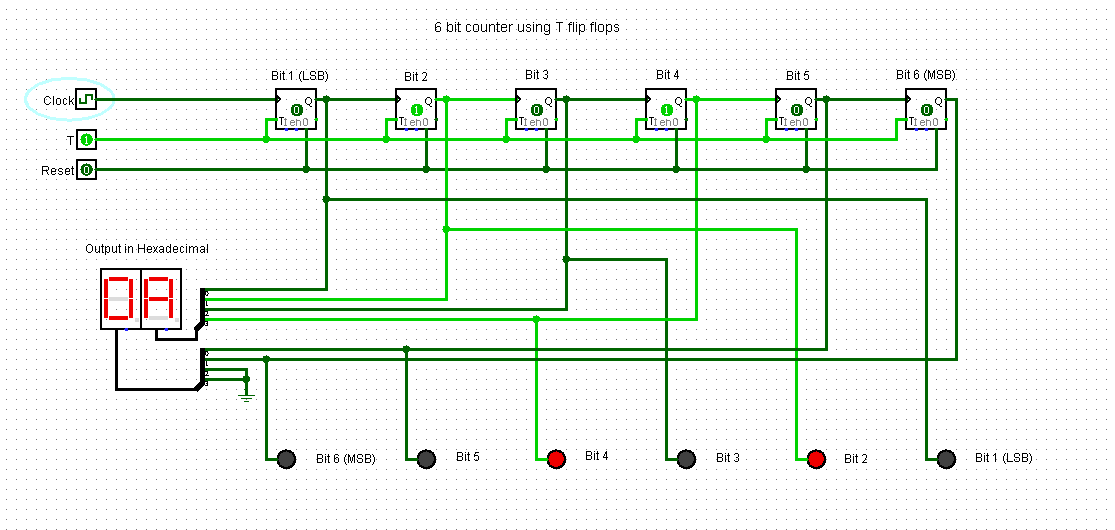


Q3)6 bit counter using T flip flops:

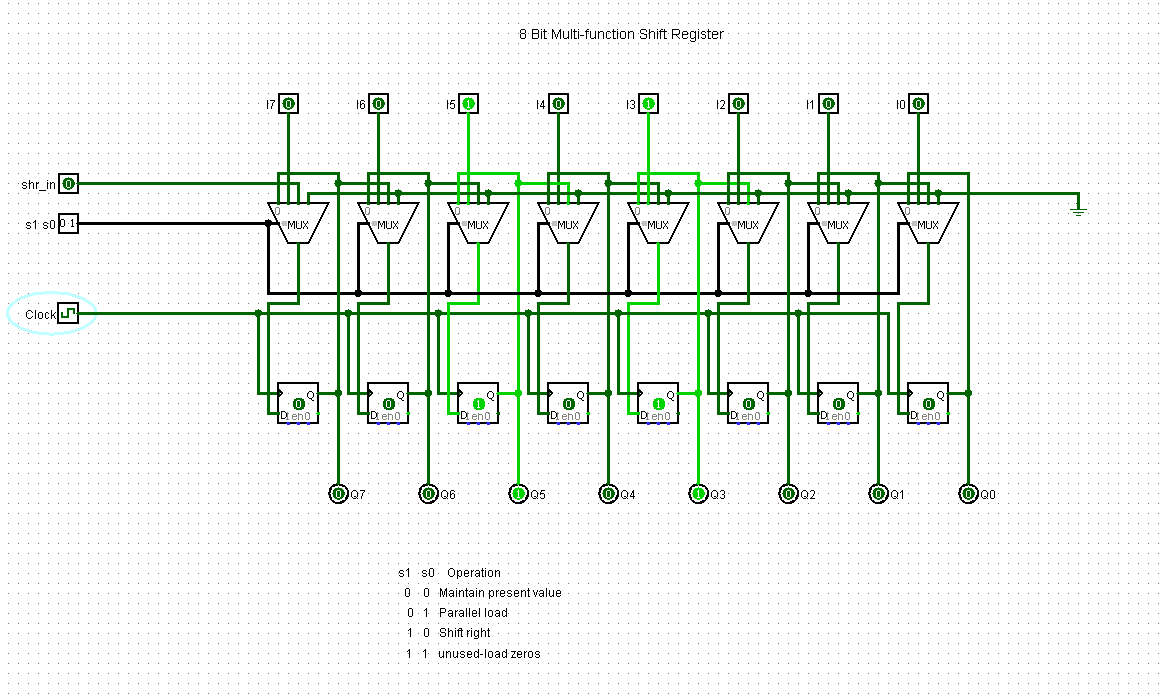


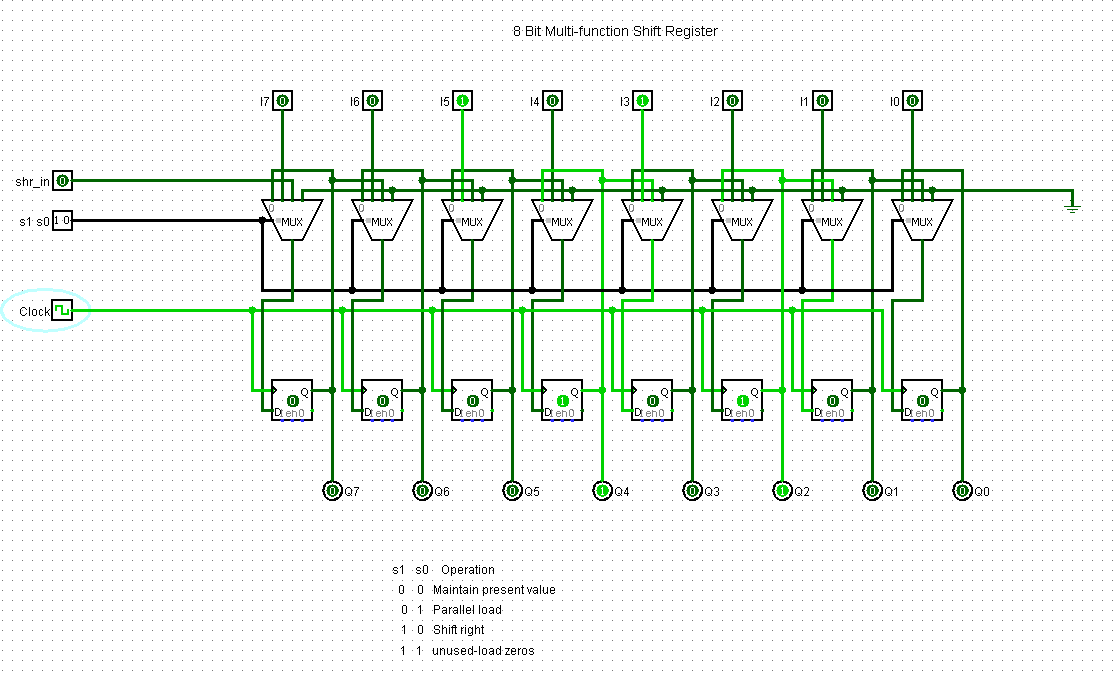


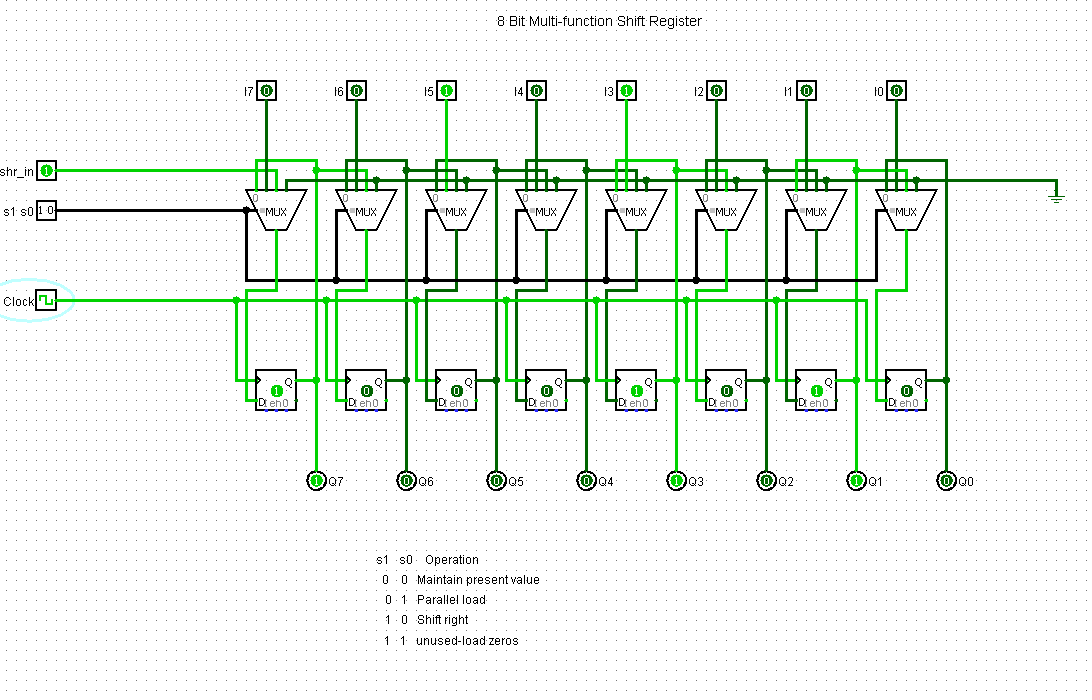


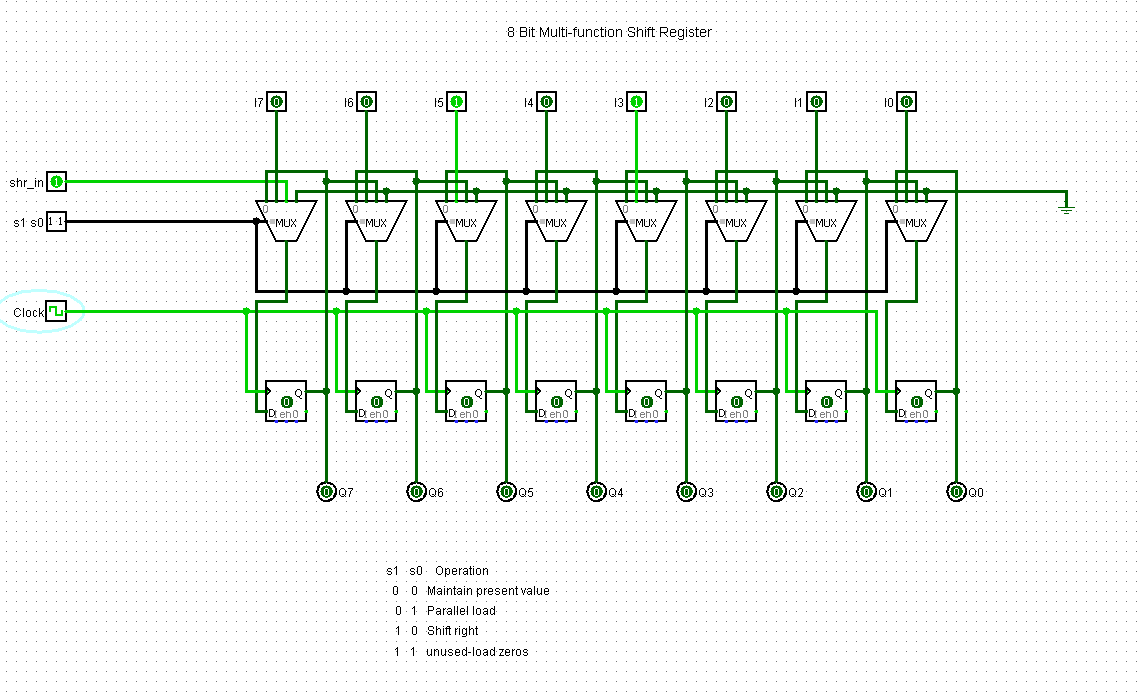


Q4)8 bit multi function register:









Q5)16x16 register file:

