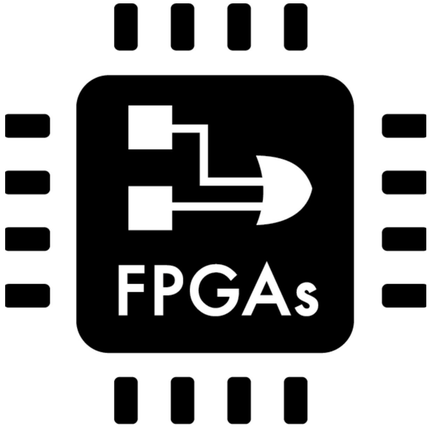

Image processing using FPGA

Contributed by: Siddharth Gupta, Prabhat Rai
Under Guidance of: Dr. GVV Sharma

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Introduction

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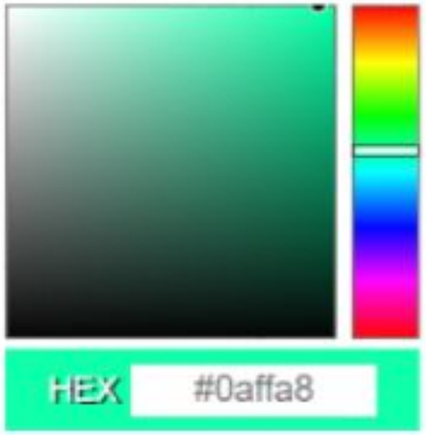
ORIGINAL COLOR

RGB
R 245 G 0 B 87

HSV
H 338.7 S 100 V 96

HSL
H 338.7 S 100 L 48

INVERTED COLOR



RGB
R 10 G 255 B 168

HSV
H 158.7 S 96 V 100

HSL
H 158.7 S 100 L 52

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Required Software and Installation

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Hardware Required

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Program Code Description

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Verilog Code Description and Pin Connection

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Project Demo

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For the processing it will take around 3-4 Minutes

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About

Important links:

GitHub Link: https://github.com/prabhatrai111/FPGA_LAB_EE5811

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3. Johnston, Christopher T., K. T. Gribbon and Donald G. Bailey. “Implementing Image Processing Algorithms on FPGAs.” (2005).
4. https://www.vision-systems.com/articles/print/volume-22/issue-8/features/cpu-or-fpga-for-image-processing-which-is-best.html

Contributors

01 **Siddharth Gupta** /gupta-siddharth/

02 **Prabhat Rai** /prabhat-kumar-rai/