







□ Current		i
R0	0x0000000C	
R1	0x00000024	
R2	0x00000019	
R3	0x00000000	
R4	0x00000000	
R5	0x00000000	
R6	0x00000000	
R7	0x00000000	
R8	0x00000000	
R9	0x00000000	
R10	0x00000000	
R11	0x00000000	
R12	0x00000000	
R13 (SP)	0x00000000	
R14 (LR)	0x00000000	
R15 (PC)	0x00000014	
⊕ CPSR	0×000000D3	
± SPSR	0×00000000	
User/System		
+ Fact Interrunt		

Register	Value
E Current	0x00000014 0x00001133 0x00001222 0x0137DEC6 0x00000000 0x00000000 0x00000000 0x000000

Register	Value
Current	
R0	0x00000014
R1	0x00001133
R2	0x00001222
R3	0x0137DEC6
R4	0x00000000
R5	0x00000000
R6	0×0000000
R7	0×0000000000
R8	0x00000000
R9	0x00000000
R10	0×00000000
R11	0x00000000
- R12	0x00000000
R13 (SP)	0x00000000
	0x00000000
R14 (LR)	0x00000010
R15 (PC)	0x000000D3
E CPSR	
⊕ SPSR	0×00000000
User/System	
Fast Interrupt	
Interrupt	
Supervisor	

R0	0x0000000C
R1	0x000000024
R2	0x00000019
R3	0x00000000
R4	0x00000000
R5	0x00000000
R6	0x00000000
R7	0x00000000
R8	0x00000000
R9	0x00000000
R10	0x00000000
R11	0x00000000
R12	0x00000000
R13 (SP)	0x00000000
R14 (LR)	0x00000000
R15 (PC)	0x00000014
E CPSR	0x000000D3
E-SPSR	0x00000000
User/System	