

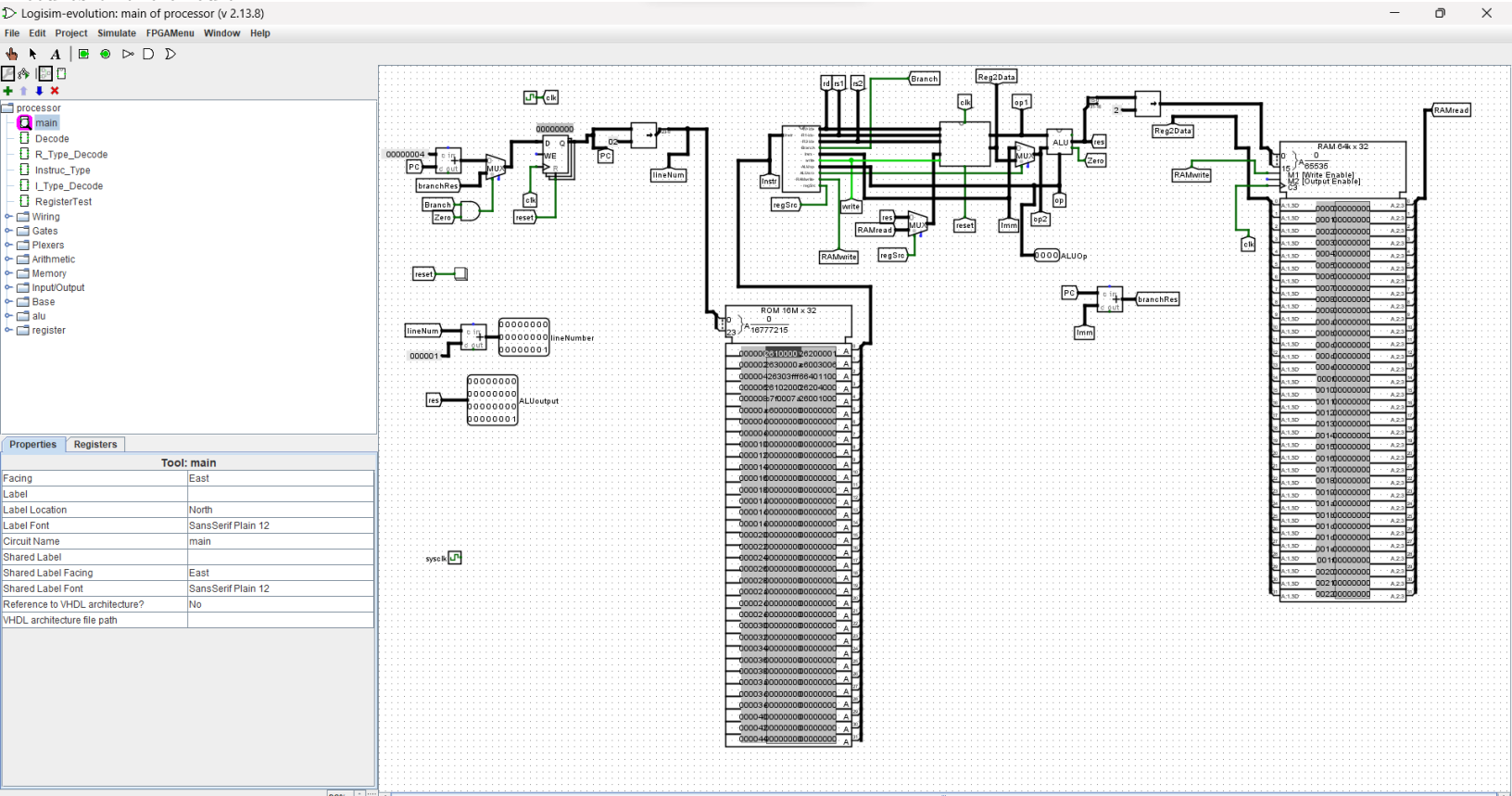
COL216 Processor Screenshot

Siddhartha Singh

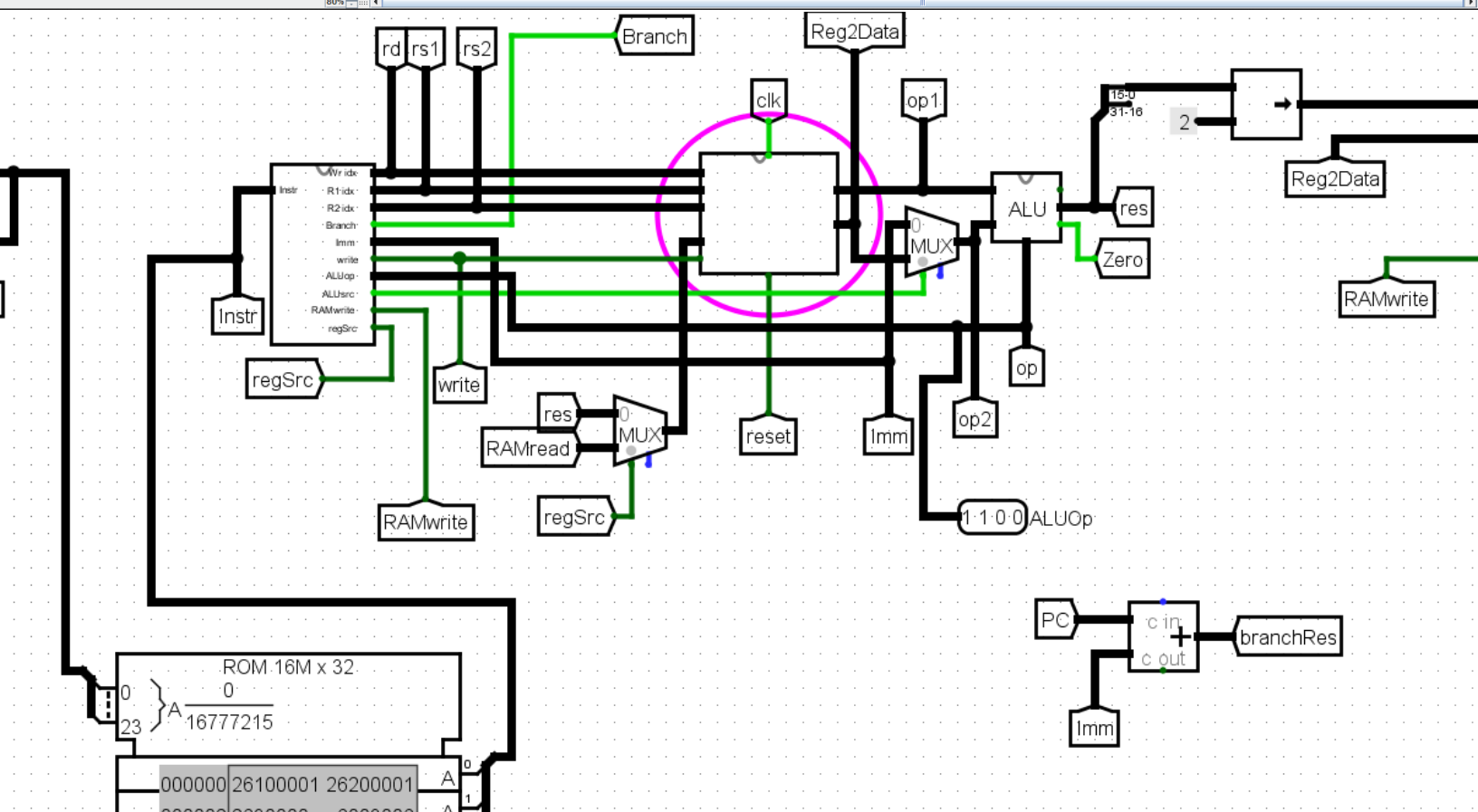
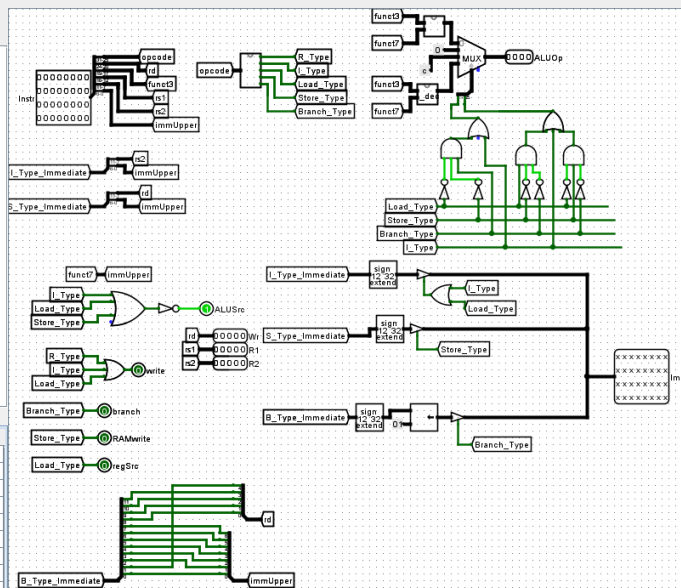
March 2024

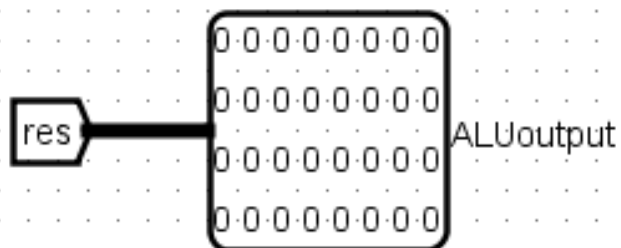
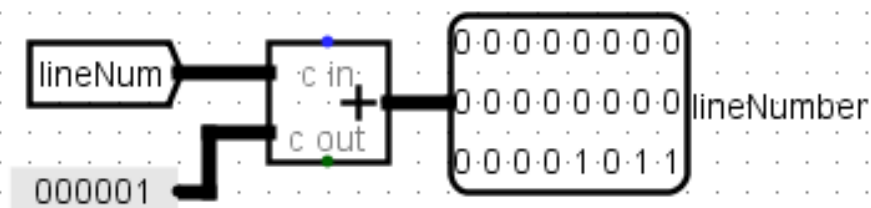
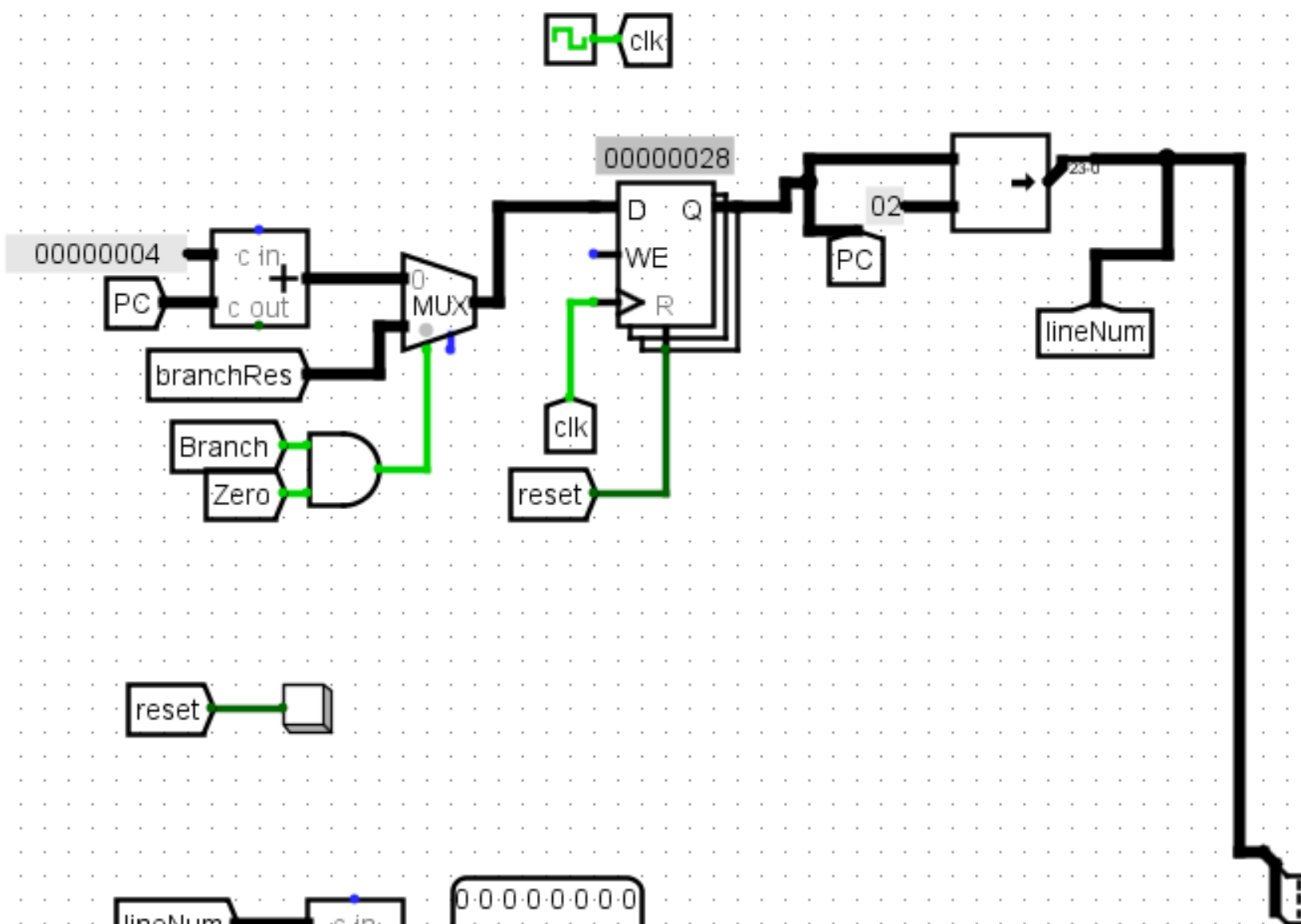
1 Introduction

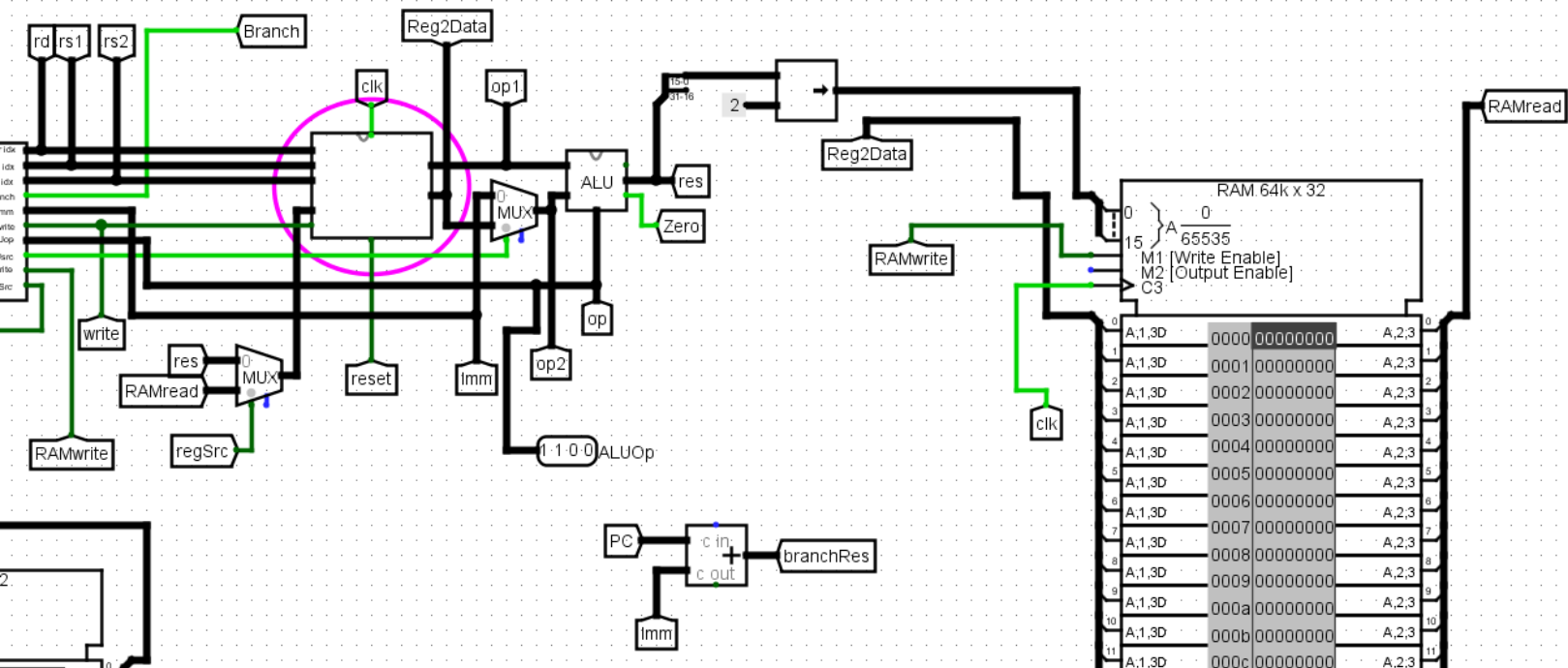
Pictures of the circuit



Facing	East
Label	
Label Location	North
Label Font	SansSerif Plain 12
Circuit Name	Decode
Shared Label	
Shared Label Facing	East
Shared Label Font	SansSerif Plain 12
Reference to VHDL architecture?	No
VHDL architecture file path	

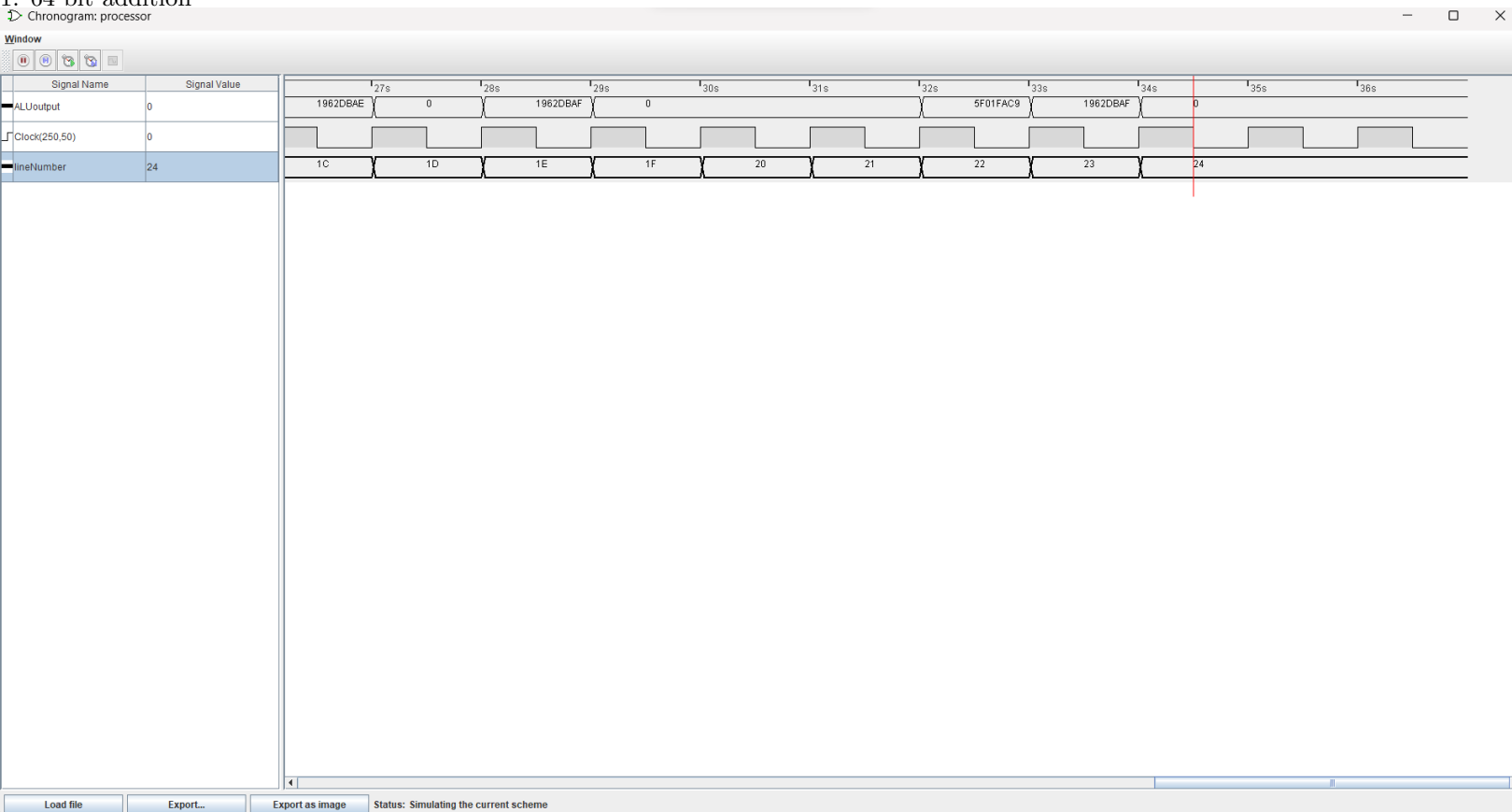






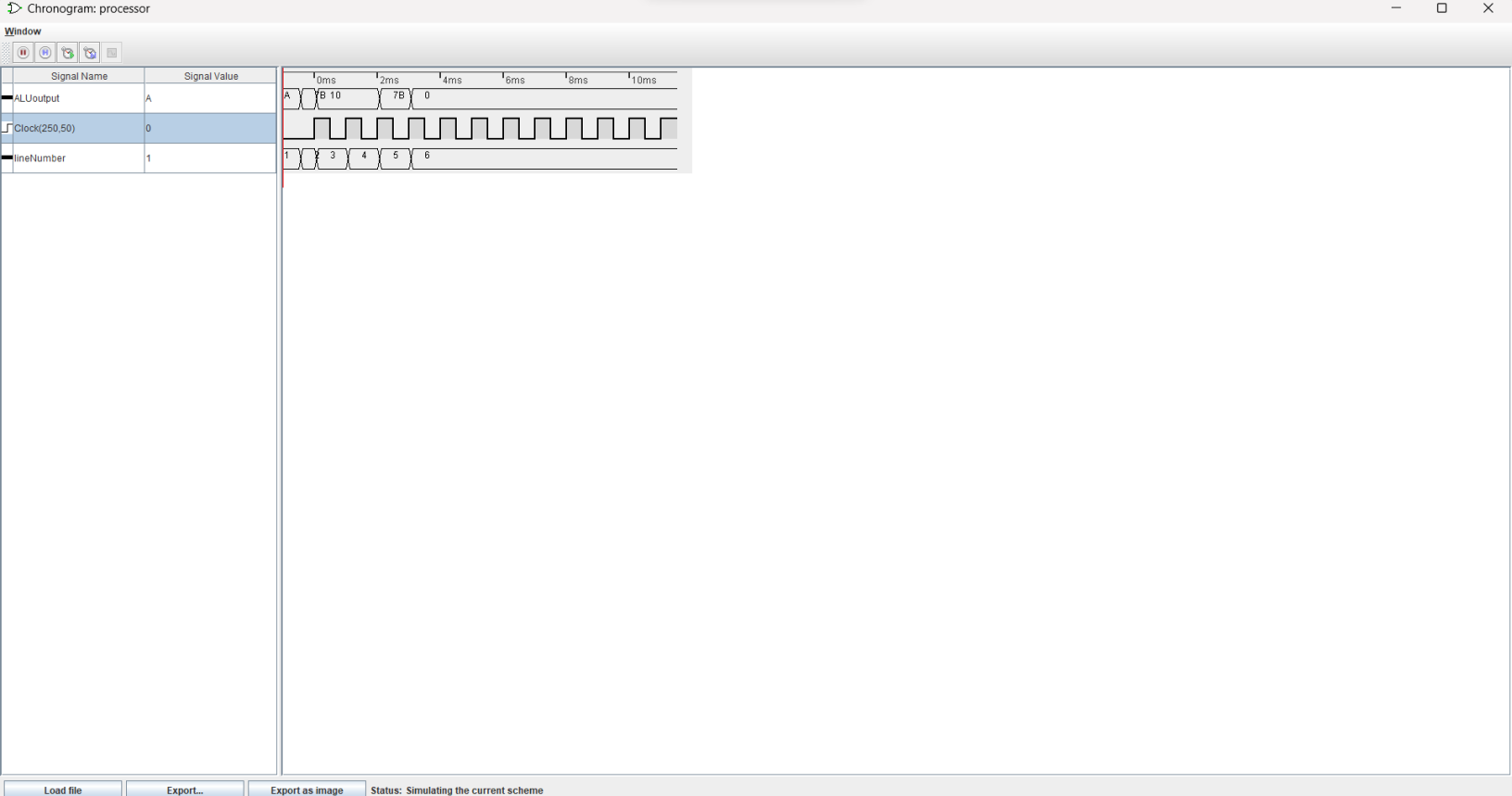
Pictures of simulation of various testcases

1. 64 bit addition

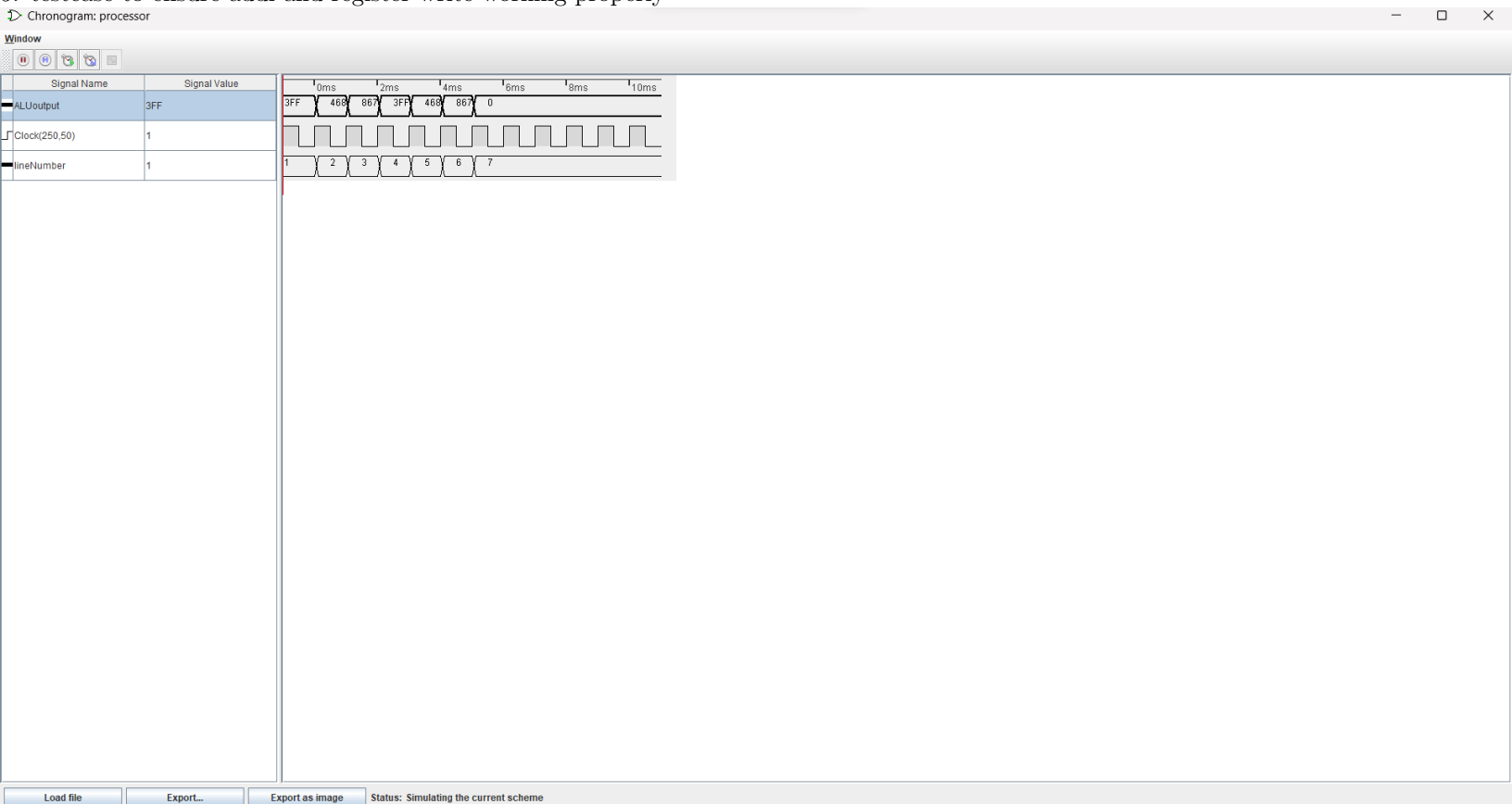


2. fibonnaci number





5. testcase to ensure addi and register write working properly



6. testcase with loops and simple addition (Adding first n numbers)

