Youchao Wang

Department of Engineering, University of Cambridge

Education

Ph.D. in Engineering

University of Cambridge, UK, 2019 – present

o Electrical Engineering Divsion, Department of Engineering.

M.Phil. in Engineering (By Research)

University of Cambridge, UK, 2018 – 2019

o Electrical Engineering Divsion, Department of Engineering.

B.Eng. Electronic Engineering (1st Class Honours)

University of Manchester, UK, 2016 – 2018

o Second year direct entry. Final grade: 87%. Third year rank: 3/250. Second year rank: 1/240.

B.Eng. Electrical Engineering and its Automation *North China Electric Power University, China,* 2014 – 2018
• First and Second year final result: 83%. First year rank: 2/119.

High School Graduate

Tsinghua High School, China, 2011 – 2014

Work Experience

Research Assistant

Department of Engineering, University of Cambridge, UK

Jan. 2019 - Mar. 2019

- o Supervisor: Dr. Phillip Stanley-Marbell
- o Embedded system circuit design and embedded system software development. Part time.

Research Assistant

Department of Engineering, University of Cambridge, UK

Aug. 2018 – Sep. 2018

- o Supervisor: Dr. Phillip Stanley-Marbell
- o Research topic: Deriving physically-inspired sensor signal invariants using a physics specification language
- o Compiler Construction, Programming language design, Signal and Noise, Dimensional analysis, Physically-inspired high-level description language (*Newton*).

Research Assistant

School of Electrical and Electronic Engineering, University of Manchester, UK

Jul. 2017 - Sep. 2017

- o Supervisors: Prof. Bruce Grieve in collaboration with Prof. Christopher Collins at University of Reading
- o Research topic: "Internet of Things" LoRaWAN Sensor System for Protecting Rivers and Watercourses
- o Internet of Things (IoT) in e-Agri, Hardware design, PCB design, Firmware design and implementation, Wireless communication.

Paid personal tutor

Hanban Foreign Learning Programme, North China Electric Power University, China

Jul. 2016 – Aug. 2016

o Paid one-on-one tutoring in the form of reading, writing and speaking (Chinese and English).

Project Portfolio

HoloDeck – Investigating Multiple Spatial Light Modulators Systems

2019 – present

- o Ph.D. Degree Research Project, Supervisor: Prof. Tim Wilkinson
- Focus: Computer generated holograms, Spatial light modulators, PCB design, Algorithm implementation, Optical Information Processing, Deep Neural Networks.

Interfacing a High Speed Ferroelectric Spatial Light Modulator

2019 – present

- o Research Side-Project, Supervisor: Prof. Tim Wilkinson
- o Focus: Spatial light modulator, Computer generated holograms, PCB hardware design, FPGA firmware implementation.

Computer Generated Holography on a Digital Signal Processor System

2019 - 2019

- o M.Phil. Degree Research Project, Supervisor: Prof. Tim Wilkinson
- o Focus: Computer generated holograms, Digital signal processing, Algorithm implementation.
- Published a first-author conference paper focusing on the implementation of a fast hologram generation algorithm on a multi-core digital signal processor.

Sensor Data Fusion using Automated Dimensional Function Synthesis

2018 - 2019

- o M.Phil. Degree Research Project, Supervisor: Dr. Phillip Stanley-Marbell
- Focus: Miniature hardware system design, Firmware implementation, Physics specification language compiler design.
- Published a first-author conference and journal double-track paper focusing on the implementation of a dimensional function synthesis compiler.

"IoT" Water Quality Monitoring System for Protecting Rivers and Watercourses

2017 - 2018

- o B.Eng. Degree Individual Project, Supervisor: Prof. Bruce Grieve
- Focus: Low-cost turbidity sensor design, Low power system design, Encoding and decoding to enhance the data transmission efficiency
- o Published a first-author journal paper focusing on the design of a low-cost turbidity sensor.

Embedded System Project Team Competition (3rd Rank Among 48 Groups)

2016 – 2017

- o Second-year Team Project: Line-following robotic buggy using light-sensitive sensor array and ultrasound sensor.
- A major contributor to hardware design and software implementation, including buggy structural design, line-detection application, motor control and programming.
- Leading role in team organisation (team of 4).
- o 90/100 personal project overall score.

Position of Responsibility

Electronic Engineering Third Year Student Representative	2017 - 2018
Electrical and Electronic Engineering Second Year Student Representative	2016 - 2017
Chairman of NCEPU International Education School Students' Union	2014 - 2015
Chairman of Tsinghua High School Students' Union	2012 - 2013
Chairman and General Secretary of Tsinghua High School Model United Nations	2012 - 2013
Selected Honours and Awards	
CSC Cambridge Scholarship (Fully-funded PhD)	Jun. 2019
CSC Masters Programme Scholarship (Partially-funded MPhil)	Jun. 2018
Third Year 3 rd Prize in School of EEE, UoM (Top 3)	Jun. 2018
2018 Beijing Outstanding Higher Education Graduate Title	Jun. 2018
Second Year 1 st Prize in School of EEE, UoM (Top 1)	Oct. 2017
Beijing Capital University & College "Pioneer Cup" Outstanding Member Title	Oct. 2016
3 rd Prize Student Scholarship at NCEPU	Sep. 2016
Entrepreneur Student Scholarship (Top 3) at NCEPU	Dec. 2015
1 st Prize (Top 2) Student Scholarship at NCEPU	Sep. 2015
Special Award (Top 1‰) in National English Competition for College Students	May. 2015
2 nd prize (Top 10) in 20 th National English Speaking Competition, Beijing region	Dec. 2014
Key Skills	

Subject Related

- o Proficient in C programming (Embedded C and compiler design). Proficient in MacOS and Linux (Ubuntu, Scientific Linux, etc.).
- Know well in Verilog and VHDL coding.
- o Know well in C++, Python, Java (Eclipse IDE and Android software development), Matlab and Simulink.
- o Proficient in MplabX IDE and Code Composer Studio. Know well in Cadence Software (VLSI), Xilinx IDE and Quartus Prime (VHDL and Verilog).
- Proficient in Altium Designer. Know well in Eagle, Designspark, NI Multisim (Circuit and PCB design). Know well in Solidworks and AutoCAD (Product design).
- $\,\circ\,$ Proficient in the use of microcontrollers (PIC18 family). Know well in Raspberry Pi.
- Know well in the Cortex M series, TI KeyStone DSPs and Lattice iCE40 FPGAs.

IT

- Proficient in the use of Adobe Family (Audition, After Effect, Premiere, Photoshop), Microsoft Office Products (Invited talk *How to make PPT better looking* at NCEPU, 2016), Corel VideoStudio, Edius, FinalCut.
- Proficient in the use of LaTeX (Invited talk How to use LaTeX at University of Cambridge, 2019)
- o Proficient in Photography, Filmmaking and Video Editing.

Driving Licence

• Full clean driving licence in China.

Language

o English (**IELTS 8.0/9.0**), Chinese (Native Speaker)

Interests

Photography, Tennis, Piano, Model United Nations.

Publication List

[1] HARDWARE IMPLEMENTATIONS ON COMPUTER GENERATED HOLOGRAPHY: A REVIEW

Youchao Wang, Daoming Dong, Peter Christopher, Andrew Kadis, Ralf Mouthaan, Fan Yang and Timothy Wilkinson. *In Submission*, 2019.

[2] Computer Hologram Generation With One-Step Phase-Retrieval Using a Digital Signal Processor Board

Youchao Wang, Daoming Dong, Peter Christopher, Andrew Kadis and Timothy Wilkinson. *Accepted*, 2019.

[3] LOOKUP TABLES FOR PHASE RANDOMISATION IN HARDWARE GENERATED HOLOGRAMS

Peter Christopher, **Youchao Wang**, Daoming Dong, Andrew Kadis, Ralf Mouthaan and Timothy Wilkinson. *In submission*, 2019.

[4] Novel Predictive Search Algorithm for Phase Holography

Peter Christopher, Youchao Wang, and Timothy Wilkinson. In Submission, 2019.

- [5] Fixed-Point Accuracy Analysis of 2D FFT for the Creation of Computer Generated Hologram Daoming Dong, **Youchao Wang**, Peter Christopher, Andrew Kadis and Timothy Wilkinson. *Accepted*, 2019.
- [6] Efficient Sensor Data Fusion Using Automated Dimensional Function Synthesis

Youchao Wang, Sam Willis, Vasileios Tsoutsouras and Phillip Stanley-Marbell. Accepted, 2019.

[7] Distributed Water Quality Monitoring System using Internet of Things Wireless Protocol – Long Range Wide Area Network

Shariar Morshed Rajib, Youchao Wang, Chris Collins and Bruce Grieve. *In Submission*, 2019.

[8] SAFEGUARDING SENSOR DEVICE DRIVERS USING PHYSICAL CONSTRAINTS

Gregory Brooks, **Youchao Wang** and Phillip Stanley-Marbell. In *Proceedings of ACM EuroSys* 2019 (poster), Dresden, 2019.

[9] LOW-COST TURBIDITY SENSOR FOR LOW-POWER WIRELESS MONITORING OF FRESH-WATER COURSES

Youchao Wang, Shariar Morshed Rajib, Chris Collins and Bruce Grieve. *IEEE Sensors Journal*, Volume: 18, Issue: 11, June 1, 2018. (Officially announced as **one of the 25 most downloaded** *Sensors Journal* papers in the months of October, November and December 2018, and **the 7th most popular document** as of January 2019)

[10] Interest Set Mechanism to Improve the Transport of Named Data Networking

Xiaoke Jiang, Jun Bi, **Youchao Wang** and You Wang. In *Proceedings of ACM SIGCOMM13* (poster, Section 12), Hongkong, 2013.

[11] Tech report. Interest Set Mechanism to Improve the Transport of Named Data Networking Xiaoke Jiang, Jun Bi, **Youchao Wang** and You Wang. *Tsinghua University*, 2013.

Referees

Available on request