

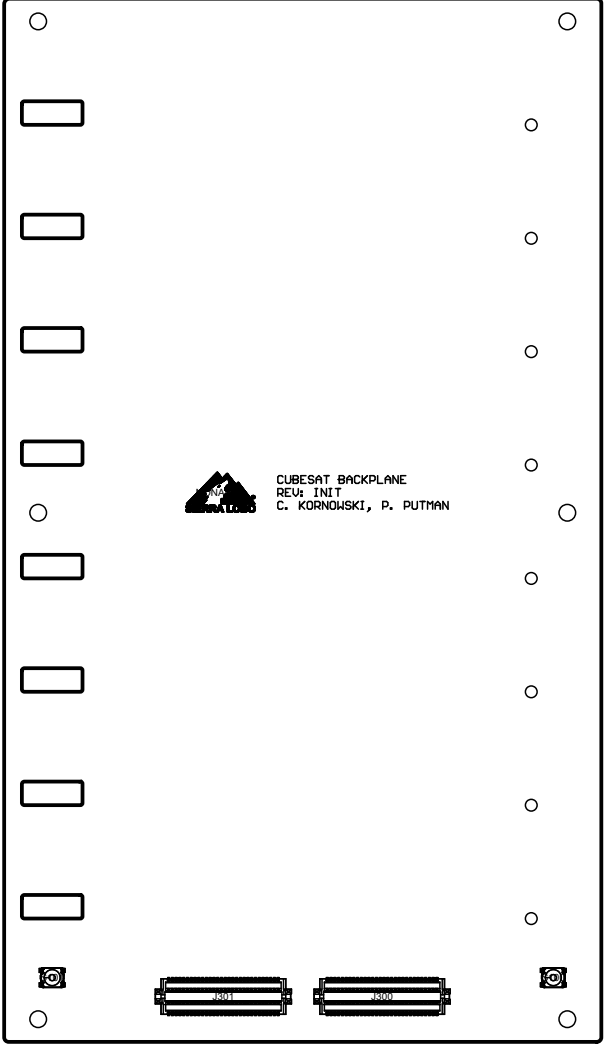
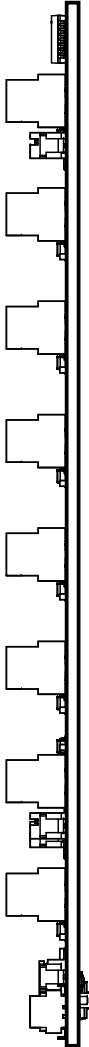
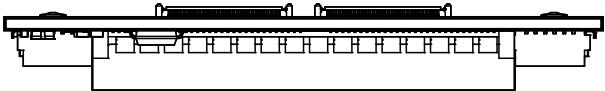
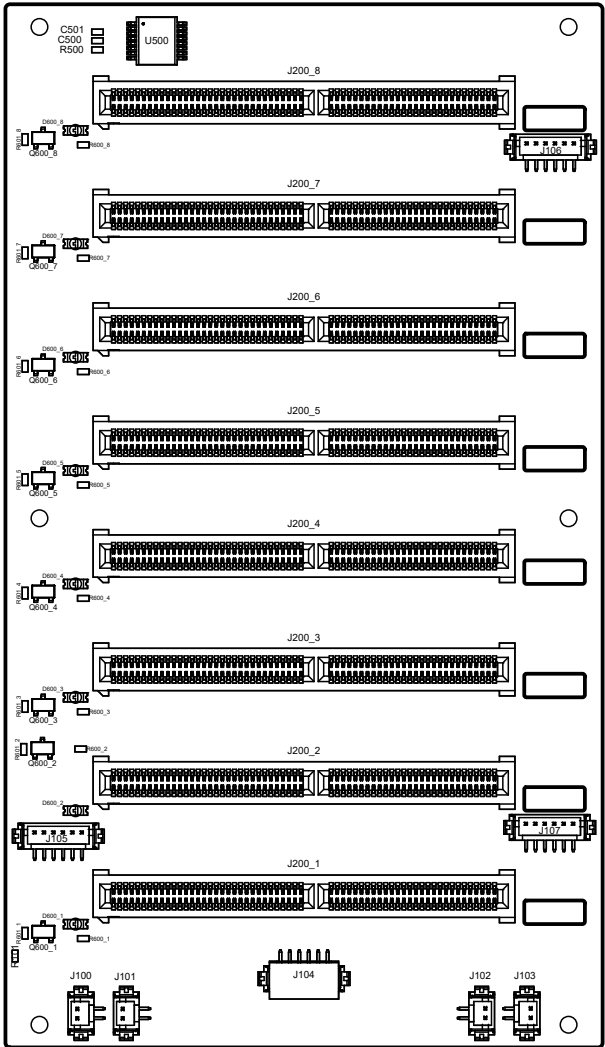
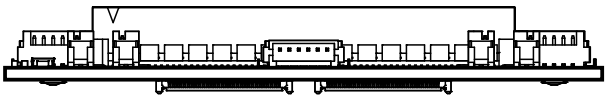
A

B

C

D

- Notes:
- 1. 63/37 LEADED SOLDER WITH WASHABLE FLUX SHALL BE USED ASSEMBLY
  - 2. SCHEMATIC DRAWING RD17-7000  
PWB FABRICATION DRAWING RD17-3000



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		UNLESS OTHERWISE SPECIFIED:		NAME	DATE	SIERRA LOBO, INC.		
		DIMENSIONS ARE IN INCHES TOLERANCES: FRACTIONAL ± ANGULAR: MACH ±    BEND ± TWO PLACE DECIMAL   ± THREE PLACE DECIMAL   ±	DRAWN	C. Kornowski	1/16/2019	TITLE  PWA FABRICATION  CUBESAT BACKPLANE		
			CHECKED					
			ENG APPR.					
			MFG APPR.					
		INTERPRET GEOMETRIC TOLERANCING PER:	Q.A.			COMMENTS:		
		MATERIAL						
		FINISH				SIZE	DWG. NO.	
NEXT ASSY	USED ON						RD17-6001	
APPLICATION		DO NOT SCALE DRAWING				SCALE: 1:1	WEIGHT:	SHEET 1 OF 2

A

B

C

D

Bill Of Materials

Line #	Designator	Manufacturer 1	Manufacturer Part Number 1	Comment	Quantity
1	C500	Samsung	CL10F104ZO8NNN C	100nF	1
2	C501	Samsung	CL10B105MO8NN WC	1uF	1
3	D600_1, D600_2, D600_3, D600_4, D600_5, D600_6, D600_7, D600_8	Würth Electronics	156125BS75000	BLUE	8
4	J100	Hirose	DF13-2P-1.25V(76)	(+X,+Y) SEP SW	1
5	J101	Hirose	DF13-2P-1.25V(76)	(+X,-Y) SEP SW	1
6	J102	Hirose	DF13-2P-1.25V(76)	(-X,+Y) SEP SW	1
7	J103	Hirose	DF13-2P-1.25V(76)	(-X,-Y) SEP SW	1
8	J104	Hirose	DF13-6P-1.25V(76)	-Z SOLAR PANEL	1
9	J105	Hirose	DF13-6P-1.25V(76)	+X SOLAR PANEL	1
10	J106	Hirose	DF13-6P-1.25V(76)	-X SOLAR PANEL	1
11	J107	Hirose	DF13-6P-1.25V(76)	+Y SOLAR PANEL	1
12	J200_1, J200_2, J200_3, J200_4, J200_5, J200_6, J200_7, J200_8	Samtec	HSEC8-160-01-S-DV-A-K-TR	HSEC8-160-01-S-DV-A-K-TR	8
13	J300, J301	Samtec	TLH-030-0.50-G-D-A	TLH-030-0.50-G-D-A	2
14	Q600_1, Q600_2, Q600_3, Q600_4, Q600_5, Q600_6, Q600_7, Q600_8	ON Semiconductor	BC847BLT3G	45V, 100mA	8
15	R101, R500	Yageo	RC0603JR-070RL	0mR	2
16	R600_1, R600_2, R600_3, R600_4, R600_5, R600_6, R600_7, R600_8	Yageo	RC0603JR-07150R L	150R	8
17	R601_1, R601_2, R601_3, R601_4, R601_5, R601_6, R601_7, R601_8	Yageo	RC0603FR-071K5L	1.5kR	8
18	S100	ITT C&K	KMT031NGJLHS	RESET	1
19	S101	ITT C&K	KMT031NGJLHS	SELF TEST	1
20	U500	NXP Semiconductors	HEF4051BTS,118	HEF4051BTS,118	1

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		DIMENSIONS ARE IN INCHES TOLERANCES: FRACTIONAL ± ANGULAR: MACH ± BEND ± TWO PLACE DECIMAL ± THREE PLACE DECIMAL ±	DRAWN	C. Kornow	11/16/2019	TITLE  PWA FABRICATION		
			CHECKED					
			ENG APPR.					
			MFG APPR.					
		INTERPRET GEOMETRIC TOLERANCING PER:	Q.A.			SIZE DWG. NO. RD17-6001		
		MATERIAL	COMMENTS:					
NEXT ASSY	USED ON	FINISH						
APPLICATION		DO NOT SCALE DRAWING						
						SCALE: 1:1	WEIGHT:	SHEET 2 OF 2