Customer Input Project Name / Rev batteryboard/B1 PCB Size 3.55 inches X 3.98 inches **Board Type** Rigid ? Material N7000-2HT Finished Thickness 0.062 inches ? Layer 2 Count STD / HDI ? STD 1 Oz ? Stackup Layer ? 2Mixed Combinatio ? Layer MM Sequence 2L_0-2-0_2S

Layer#	Material	Layer Type	Copper %		Finished Thickness (Mils)	Dielectric or Copper Base Thickness (Mils)	Copper Plating Thickness (Mils)	Dielectric Description (Mils)	Er (Dk @10 GHz)	Material Construction
	Solder Mask				0.5	0.5 mi l s		Soldermask 0.5 mils	4.2	
1	Copper	Mixed	44%		1.5	0.7 (0.50 oz)	0.8			
	Core				59	59		N7000-2HT Core 59 mils 0.50 oz / 0.50 oz	4.2	(2x1080+8x7628)-40
2	Copper	Mixed	44%		1.5	0.7 (0.50 oz)	0.8			
	Solder Mask				0.5	0.5 mils		Soldermask 0.5 mils	4.2	
Total					63					

Sr. No.	Signal Layer	Target Impedance (ohm) (tol- 10%)	Tx Line model	Ref	Ref 2	Trace Width (mils)	Trace Spacing (mils)	TPS = W + S (mils)	Coplanar Spacing (mils)	Calculated Impedance (Ω)	Calculated Impedance before Mask (Ω)	Propagatio Delay (ps/inch)

Technology Parameters and Cost Index ?



PCB TECHNOLOGY LEVELS	Level 1	Level 2	Level 3
Mechanical Via Diameter (in mils)	8	7	6
Mechanical Via Pad Diameter (in mils)	14	13	12
Trace Width Top Layer (in mils)	5	4	4
Trace Width Inner Layers (in mils)	4	3.5	3
Trace Width Bottom Layer (in mils)	5	4	4
Cost Index	0.8	0.94	1.15

Via Set Information

This Stackup supports the following Via Set

L1-L2

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