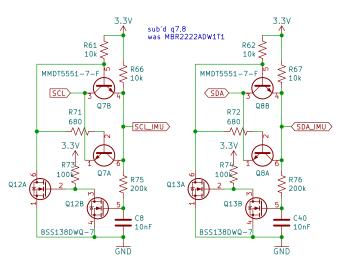
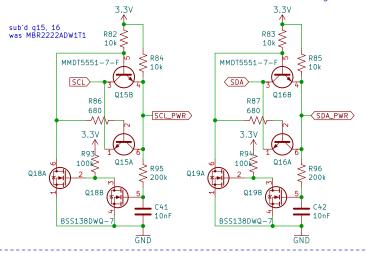


#### 12C Bus Protection - IMU



### I2C Bus Protection — Power Monitor & USB Charger



## NOTE

These novel bus protection circuits prevent traditional I2C/SPI failure modes where a single slave failure can disable the entire bus.

Learn more:

https://doi.org/10.36227/techrxiv.15166620

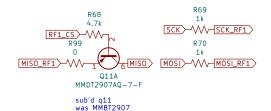
By default, slave clock and/or data lines can be held low and the Master (SAMD51) will still be able to communicate with the remainder of the bus.

They can individually be bypassed by removing the transistor(s) and soldering the Oohm the jumpers below.

#### SPI Bus Protection — SD Card and Payloads



#### SPI Bus Protection - Radio 1

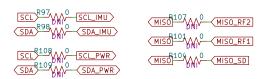


#### SPI Bus Protection - Radio 2



NOTE: Components labeled "do not install" (DNI) are not populated by default

### Bus Protection - Bypass Jumpers



## **Bus Protection**

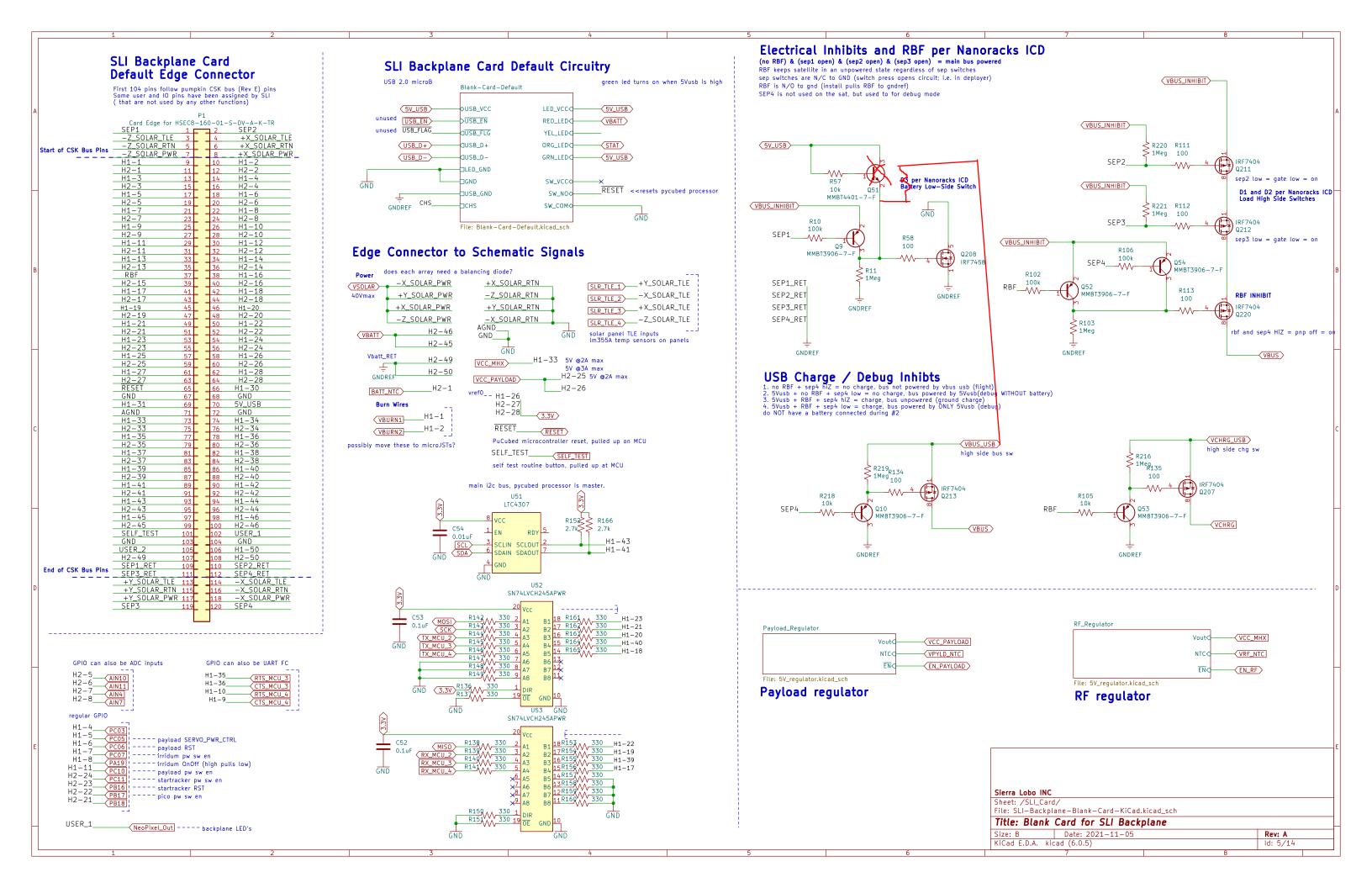
Max Holliday

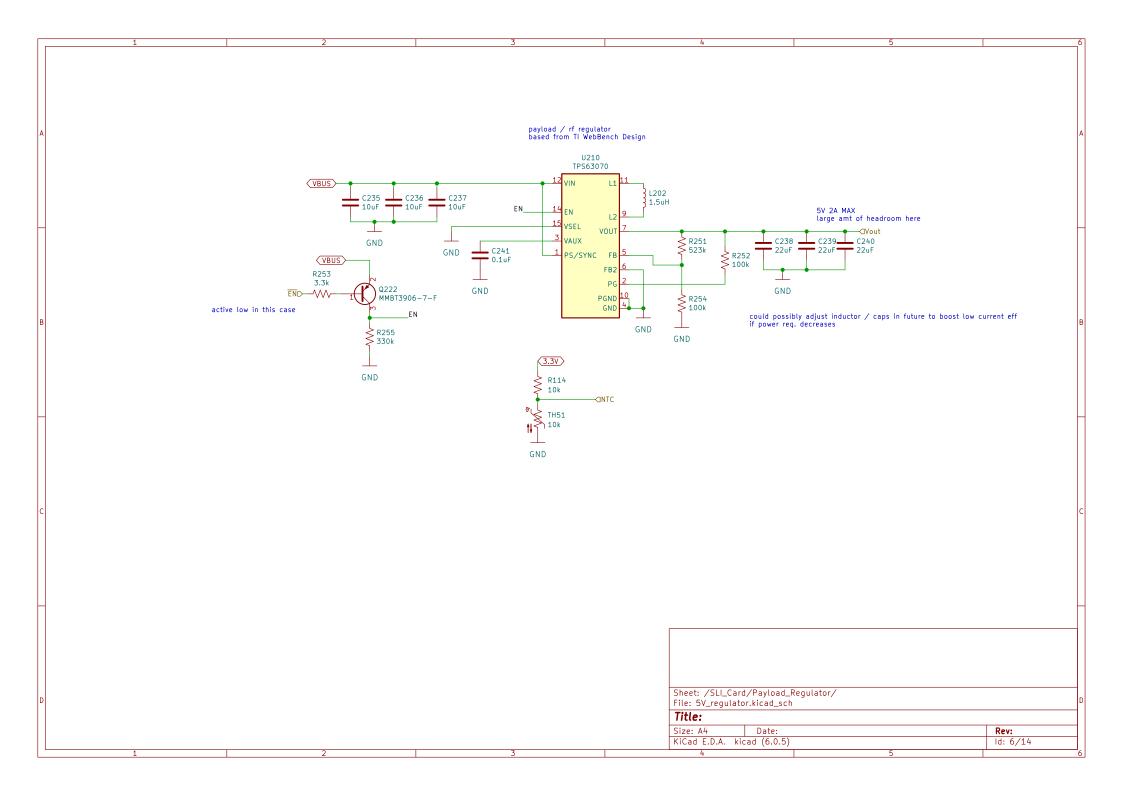
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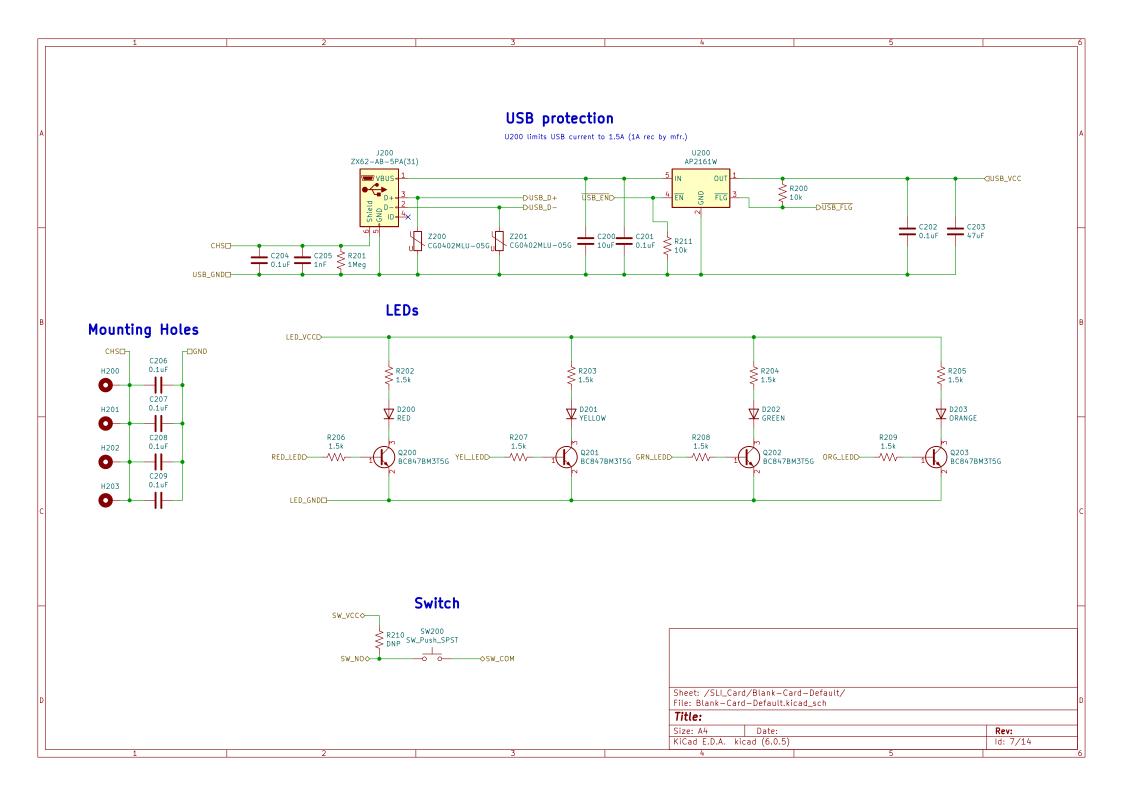
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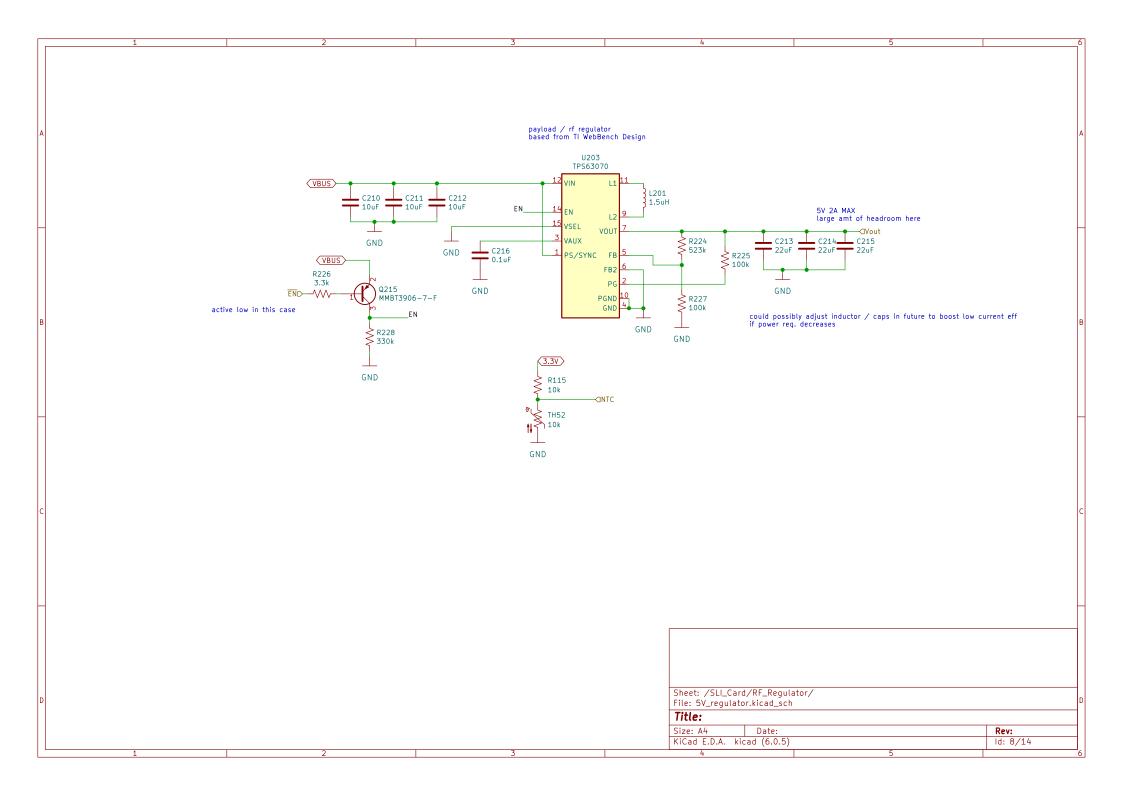
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 Rev: v05c

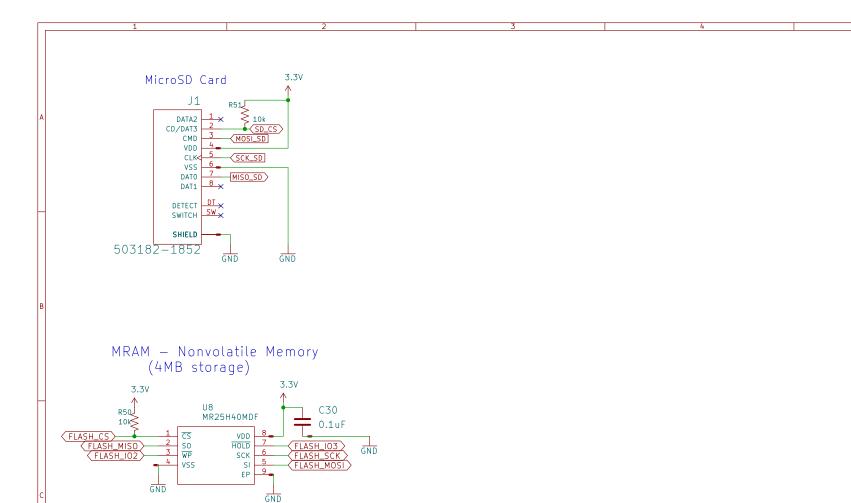
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 Id: 4/14











JTAG 3.3V

GND

NOTE: Components labeled "do not install" (DNI) are not populated by default

# **Connectors**

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Title: PyCubed Mainboard

 Size: A4
 Date: 2021-06-09
 Rev: v05c

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 Id: 9/14

