United International University

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Trimester: Spπing Date: 9/5/2022

Experiment No. 08

8. a: Name of the Experiment: To investigate the characteristics of a series DC circuit and to verify Kirchoff's Voltage Law (KVL).

OBJECTIVE:

The objective of this experiment is to investigate the characteristics of a series DC circuit and to verify Kirchoff's Voltage Law (KVL).

THEORY:

In a series circuit (Figure 3.1) the current is same through all of the circuit elements.

The equivalent Resistance,

$$R_T = R_1 + R_2 + R_3$$
.

By Ohm's Law, the Current is

$$I = \frac{V_{supply}}{R_T}$$

KVL states that the voltage rises must be equal to the voltage drops around a close circuit. Applying Kirchoff's Voltage Law around closed loop of Figure 3.1, we find,

$$V_{\text{Supply}} = V_1 + V_2 + V_3$$

Where,

$$V_1 = IR_1$$

$$V_2=IR_2$$

$$V_3=IR_3$$

Current I is same throughout the circuit for figure 3.1..

The voltage divider rule states that the voltage across an element or across a series combination of elements in a series circuit is equal to the resistance of the element divided by total resistance of the series circuit and multiplied by the total impressed voltage. For the elements of Figure 3.1

$$V_3 = \frac{R_3 E}{R_T}$$

$$V_1=\frac{R_1E}{R_T},$$

$$V_2 = \frac{R_2 E}{R_T},$$

EQUIPMENTS:

- Variable DC power supply -1piece
- Digital Multimeter (DMM)/ Analog multimeter-lpiece.
- Resistances: 100Ω , 220Ω , 470Ω -1 piece each.
- Trainer Board-1piece
- · Connecting Wires.

CIRCUIT DIAGRAM:

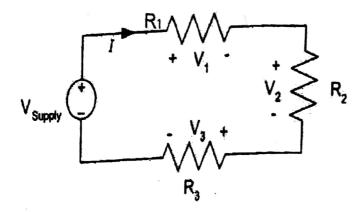


Figure 3.1

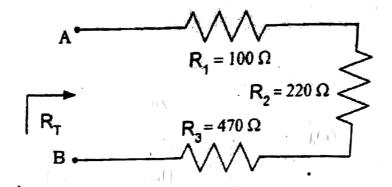


Figure 3.2

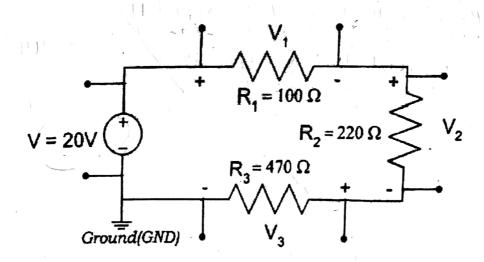


Figure 3.3

PROCEDURE:

- 1. Measure the resistances having values 100Ω , 220Ω & 470Ω by using Ohmmeter and record the values in Table 3.1.
- 2. Construct the circuit as shown in Fig 3.2.
- 3. Then measure input resistance R_T across points A-B using Ohmmeter and record that value in Table 3.1.
- 4. Now construct the circuit as shown in Fig 3.3. Turn on the DC power supply and set the DC supply to 20V by using Voltmeter.
- 5. Measure voltage across each resistor with Voltmeter and record in the Table 3.1
- 6. Calculate V₁, V₂ and V₃ using Voltage Divider Rule (VDR). [Use measured values of resistances for all calculations.]

Experimental Data:

(A) The supply voltage, $V_{\text{supply}}=V_{\text{s}}=V_{\text{in}}=V_{\text{The}}=V_{\text{The}}=V_{\text{the}}$

(B) Tolerance Color = (10)

and Tolerance value as percentage = ± 5.....%

(C) Tolerance value = Maximum or Minimum = Minimum (- ve)

R, = R, nominal + 87. R, = 100+5 = 85/2, 105/2

 $R_2 = R_2$ nominal ± 57 , $R_2 = 220 \pm 11 = 209 \Omega$, 231Ω $R_3 = R_3$ nominal ± 57 , $R_2 = 4794$ $23.5 = 446.5\Omega$, 493.5Ω

DATA SHEET:

Table 3.1

$2.531 = \frac{100}{290} \times 20 = \sqrt{15} \frac{15}{15} \times \sqrt{15} \frac{100}{280} \times 100 = 2.365$	
5.519 = $\frac{220}{790} \times 20 = \sqrt{2} = \frac{R2}{R1} \times \sqrt{\frac{210}{780}} \times 19.98 = 5.379$	
11.898 = 430 ×20 = V3 = R3 N- 460 ×19.98=11.783	

Al-maimal realizad	Measured values of Resistance by Ohmmeter (Ω)		Equivalent Measured R _T by using Ohmmeter (0)	t Resistance, R_1 Calculated $R_1 = R_1 + R_2 + R_3$ (C)	across each	Calculated Voltage using VDR (V)
R ₁ =100	90 =	95	,		V ₁ = 2,56	2.305
R ₂ =220	210 -	209	780	230	V ₂ = 5.53	5.379
R ₃ =470	460	446.5	3		V3= // 88	11.783
RT = 790	Rm= 760	Ryo)= 750.	5 🛫 😕	The Marie	V=19,98	V=VVDR= 12.467

Calculation:

(A) KVL Veriliacation:

According to KVL, Vin = Vout

. '. Vgain = Vzost, Here, Vgain = Vin = Vs = Vsupply = 20V

... VLost = Voct = Vm = Vmes = Ve = VVDR = 19.467 1. VI-st = Vout = Vm = Vmes = Vc = VVDR = 10.417

1. Vs= Vm= Vc, i.e 1. 20=19.98=19.467

.: KVL is veribled.

<u> </u>	
E 14 1	12 / Emplowerted value)
of the Theores	ical value) ~ (Experimental value)
CHARLES	Theorital value.

= 97.433 %

Signature of the Teacher

Discussions:

Q: What can you deduce about the characteristics of a series circuit from observation Table 3.1? From observation table 3.1. We know that all nesistance are in series connection. And we also find the Voltage are different in dibbenent resistance and current are same in every nesistance.

Q: From the data found in Table 3.1, mathematically prove that the current in the series network of figure 3.3 is equal for each resistance.

From fable 3.1 We know, $R_1 = 100$, $V_1 = 2.56$ So, $11 = V_1/R_1 = 2.56/100 = 0.0256$ $R_2 = 220$, $V_2 = 5.57$ So, $12 = V_2/R_2 = 5.53/220 = 0.0259$ $R_3 = 470$, $V_3 = 11.88$ $R_3 = 470$, $V_3 = 11.88$ So, $13 = V_3/R_3 = 11.88/470 = 0.0253$ So, $13 = V_3/R_3 = 11.88/470 = 0.0253$

Q: Verify KVL from the data obtained in Table 3.1.

According to the kVL, Vin = Vout

Here, Vin = 20V

Vout = (2.55+5.57+11.88) V

= 20.00

= 20.00

= 20 volt

So it maintains KVL,

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Name: ID: Section: Group: Trimester: Date:

Experiment No. 08

8. b: Name of the Experiment: To investigate the characteristics of a Parallel DC circuit and to verify Kirchoff's Current Law (KCL).

OBJECTIVE:

The objective of this experiment is to investigate the characteristics of a parallel DC circuit and to verify Kirchoff's Current Law (KCL).

THEORY:

In a parallel circuit (Figure 4.1) the voltage across parallel elements is the same.

The total or equivalent resistance (R_T) is given by,

$$\frac{1}{R_T} = \frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{R_3} + - - - - - - - + \frac{1}{R_N}$$
If there are only two resistors in parallel, it is more convenient to use,

$$R_T = (R_1 R_2)/R_1 + R_2$$

In any case, the total resistance will always be less than the resistance of the smallest resistor of the parallel network.

KCL states that the currents entering a node must be equal to the currents leaving that node. For the network of Figure 4.1, the currents are related by the following expression:

$$I_T = I_1 + I_2 + I_3 + - - - - + I_N$$

Applying current divider rule (CDR) for a circuit of only two resistors in parallel as shown in figure 4.2,

$$I_1 = \frac{R_2 I_T}{R_1 + R_2}$$
 and $I_2 = \frac{R_1 I_T}{R_1 + R_2}$

For equal parallel resistors, the current divides equally and the total resistance is the value of one divided by the 'N number of equal parallel resistors, i.e.:

$$R_T = \frac{R}{N}$$

For a parallel combination of N resistors, the current l_1 through R_1 is:

$$I_1 = I_T \times \frac{\frac{1}{R_1}}{\frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{R_3} + ---- + \frac{1}{R_N}}$$

EQUIPMENTS:

- Variable DC power supply -1 piece
- Digital Multimeter (DMM)/ Analog multimeter-1piece.
- Resistances: $1 K\Omega, 2.2 K\Omega, 4.7 K\Omega$ -1 piece each.
- Trainer Board-1piece
- Connecting Wires.

CIRCUIT DIAGRAM:

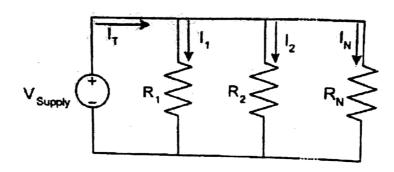
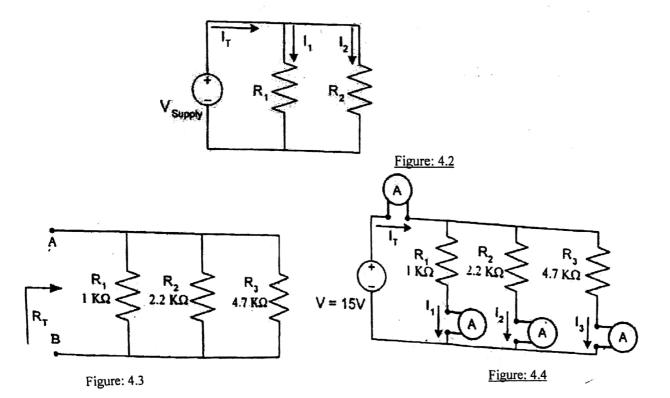


Figure: 4.1



PROCEDURE:

- 1. Measure the resistances having values 1 K Ω , 2.2 K Ω & 4.7 K Ω by using Ohmmeter and record the values in Table 4.1.
- 2. Construct the circuit as shown in Fig 4.3.
- 3. Then measure input resistance R_T across points A-B using Ohmmeter and record that value in Table 4.1.
- 4. Now construct the circuit as shown in Fig 44. Tum on the DC power supply and set the DC supply to 15V by using Voltmeter.
- 5. Measure the currents I_T , I_1 , I_2 and I_3 by using Ammeter and record in the Table 4.1.
- 6. Calculate I₁, I₂ and I₃ using Current Divider Rule (CDR). [<u>Use measured values of resistances for all calculations.</u>]

Experimental Data:

(A) The supply voltage, $V_{supply} = V_s = V_{in} = V_{T(S)} = V_T = V_{The} = V = ...$ Volt

(B) Tolerance Color = 60 and Tolerance value as percentage = $\pm .5...$ %

(C) Tolerance value = Maximum or Minimum ()

R1= nominal of R1 ± 51 \$ R1= 1000 ± 50 -2 = 950-2,1050-2

R2 = nominal of R2 ± 5:1. of R2 = 2200 ± 110-2 = 2090-2, 2310-2

R3: nominal of R3 ± 51/d R3=4200 ± 235-2 -4465-2,4935-2

In- 26mA

 $15.594 = \frac{0.5998}{1} \times 26 = \overline{I}_{1} = \frac{R_{1}}{R_{1}} \times \overline{I}_{1} = \frac{0.59}{0.99} \times 26 = 15.494$ 3 7 16 7.088 - 0.598 ×26 = IZ = RT × IT = 0.59 × 26 = 7.118

DATA SHEET:

Table 4.1

3.3/8 = $\frac{6.598}{49} \times 26 = 1_3 = \frac{R_T}{4.64} \times 1_T = \frac{6.59}{4.64} \times 26 = 3.306$

No include	Measured values	Measured	Equivaler	Equivalent Resistance, R _T		
Nominal values of Resistance (kΩ)	Ohmmeter (kΩ)	values of Resistance by Tolerance (kΩ)	esistance by using $1/R_T = 1/R_1 + 1/R_2 + 1/R_3$ resistor (A)		through each	Calculated Current using CDR (A)
R ₁ =I	0.99	0.950		0.5000	I _I = 3	15.494
R ₂ =2.2	2.14	2.490	0.59	0.59%	I ₂ = 7	7.168
R ₃ =4.7	4.64	4.465			I3= 1.6	3.366
RT = 0.5998	$R_{\rm m} = 0.59$	RTd = 0.5698		Im: I	= 26 mA	I=Icol 25.968

(A) KCL verification: Tout =) IT = In+I2+I3=I => Im=IT

According to KCL, I'm = Iout => IT=In+I2+I3=I ... In = Tout Here, In = IT = I supply = Is = I = 26 mA

: I Tout = II + Iz+ 13 = Im = Imes = IC = Icap = 25.968 and IT = Is = Iin = 26

 $I = I_1 + I_2 + I_3 = Iout = 26$

1. I'm & Jout , ne , 1. 26 = 26

... In 5 Tout = Ic = 1, e: 26 = 26 = 25.918

: KCL is renibud

Of FROMIC > (Theorie fical value) ~ (Experie val)

- 0.123 n

: Accuracy (9.) = 100 % -0.123%

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Discussions:

Q: What can you deduce about the characteristics of a parallel circuit from observation Table 4.1?

From observation table 4.1 we know that all nesistance are in servies connection. And we also bind the Voltage and different in dibbenent nesistance and current one same in every nesistance.

Q: From the data found in Table 4.1, Calculate I1, I2, and I3 using Ohm's Law.

$$4.1 \Rightarrow Iin = 26 \text{ mA}$$
 $I_1 = \frac{RT}{R_1} \times I_T = \frac{0.6}{1} \times 26 = 15.49 \text{ mA}$
 $I_2 = \frac{RT}{R_2} \times I_T = \frac{0.6}{2.2} \times 26 = 7.16 \text{ mA}$
 $I_3 = \frac{RT}{R_3} \times I_T = \frac{0.6}{4.7} \times 26 = 3.306 \text{ mA}$

ohms law = V= IR

Q: Verify KCL from the data obtained in Table 4.1.

kcl,
$$i_{T} = i_{1} + i_{2} + i_{3}$$

= $26 = (15.45 + 4.16 + 3.306)$
= $26 mA \approx 25.968 mA$
 $1.26 = 26$
 $1.T_{T} = 1.1 + I_{2} + I_{3}$
(verified)