

S32K344 Whiteboard SW(LLD)

Quick Start Guide

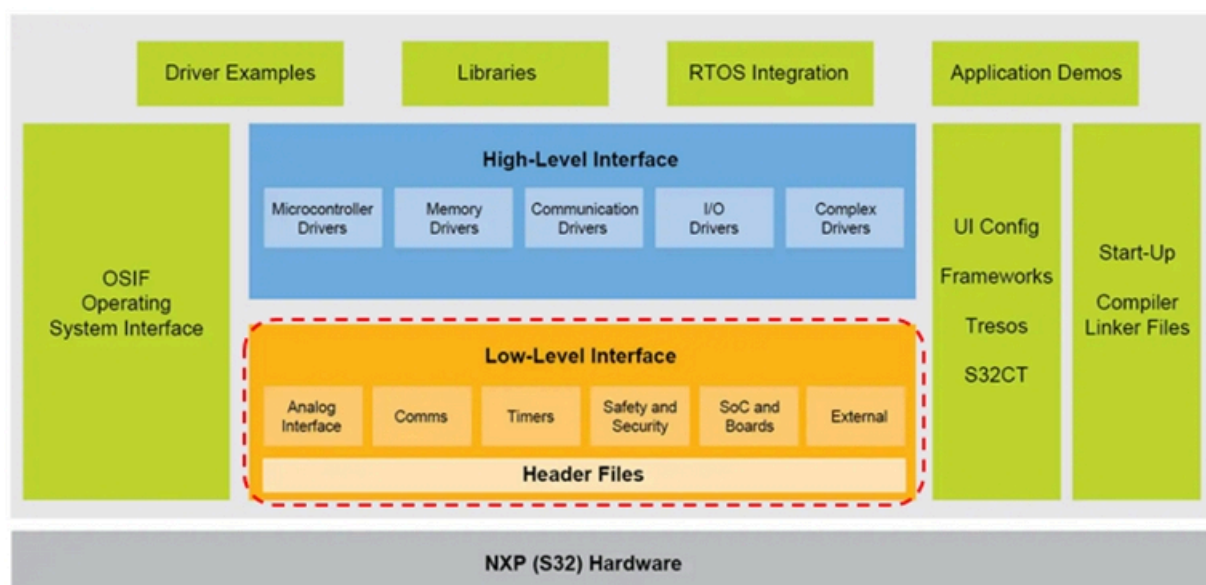
Rev. 1.0 — 23 April 2023

User guide

1 Introduction

This document is a quick start guide of this software example package for the [S32K344 Whiteboard \(S32K344-WB\)](#). This software package is based on the [Real-Time Drivers \(RTD\)](#) /Low-Level Driver (LLD) (Shows in the below picture: Low-Level Interface). All projects in this package are created with [S32 Design Studio IDE \(S32DS\)](#). And based on RTD version RTM 2.0.0. There is another package (S32K344_Whiteboard_Example(HLD)_RTM_1.0.0) also for the same hardware, but that package is focused on High-Level Driver (HLD). For the RTD perspective of view, it is full covered by these two packages. On the hardware side, this software package focused on the external device, and another HLD package focused on the on-chip peripherals. Thus, provides customers with more choices from different perspectives to meet the different needs.

This document will only guide you how to use these example projects quickly. If you want to know how to setup the development environment or how to create your own project in S32DS, please refer to the other materials (For example: UG10047). The below picture is the architecture of RTD. More detail information of RTD, please refer to the webpage. More detail information of the hardware, you can refer to the hardware user manual.

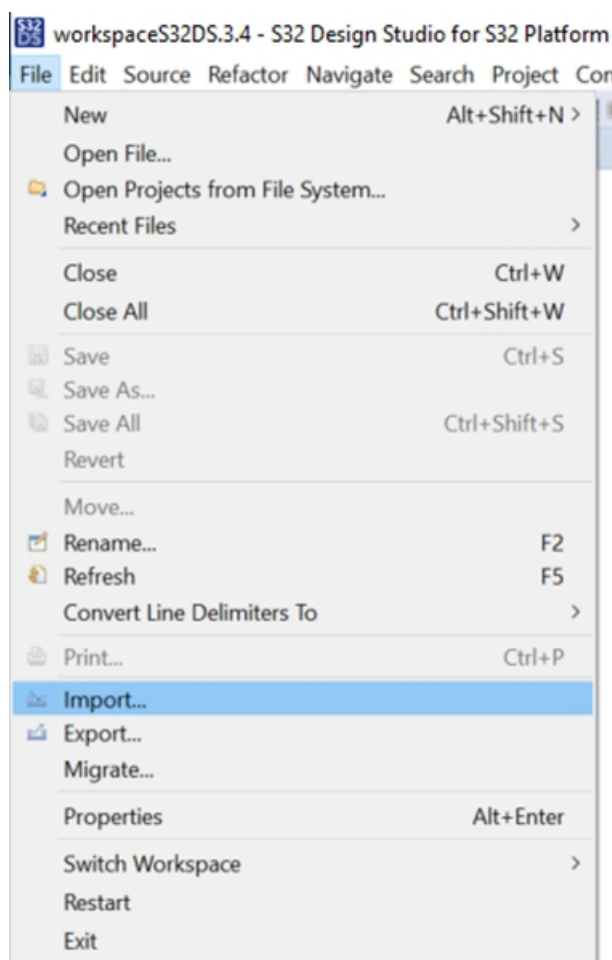


2 Working with the example projects

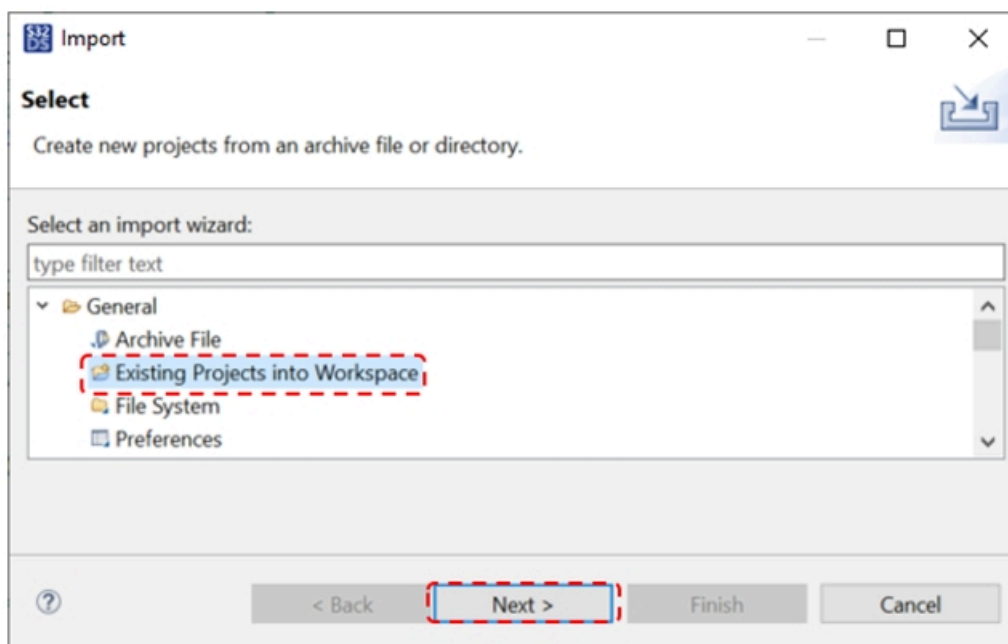
Before you start working with this package, the development environment should be installed. This is not included in this document, please refer to other materials (For example: UG10047).

2.1 Import Project

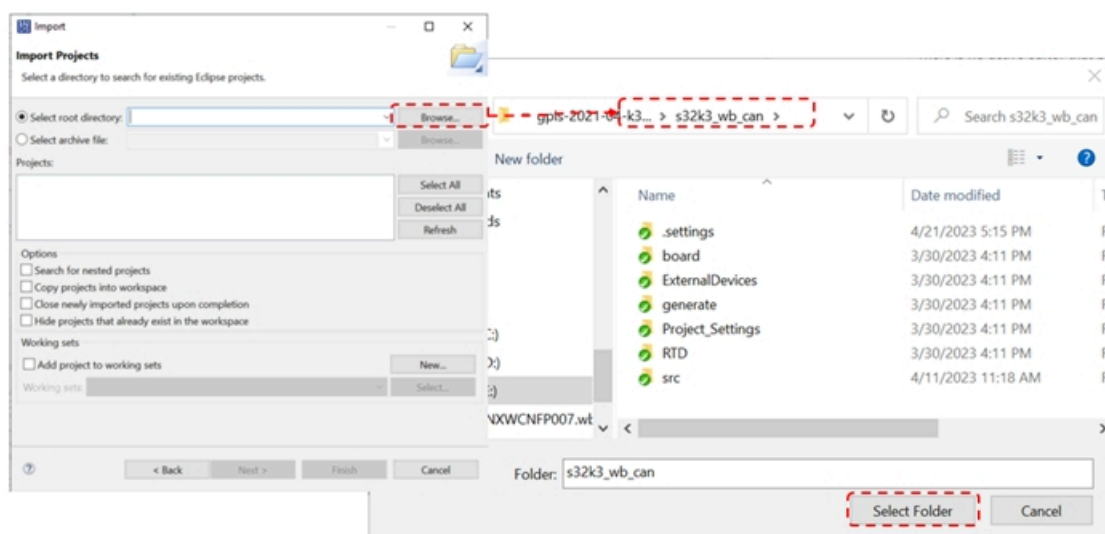
- Open S32 Design Studio: File -> "Import..."



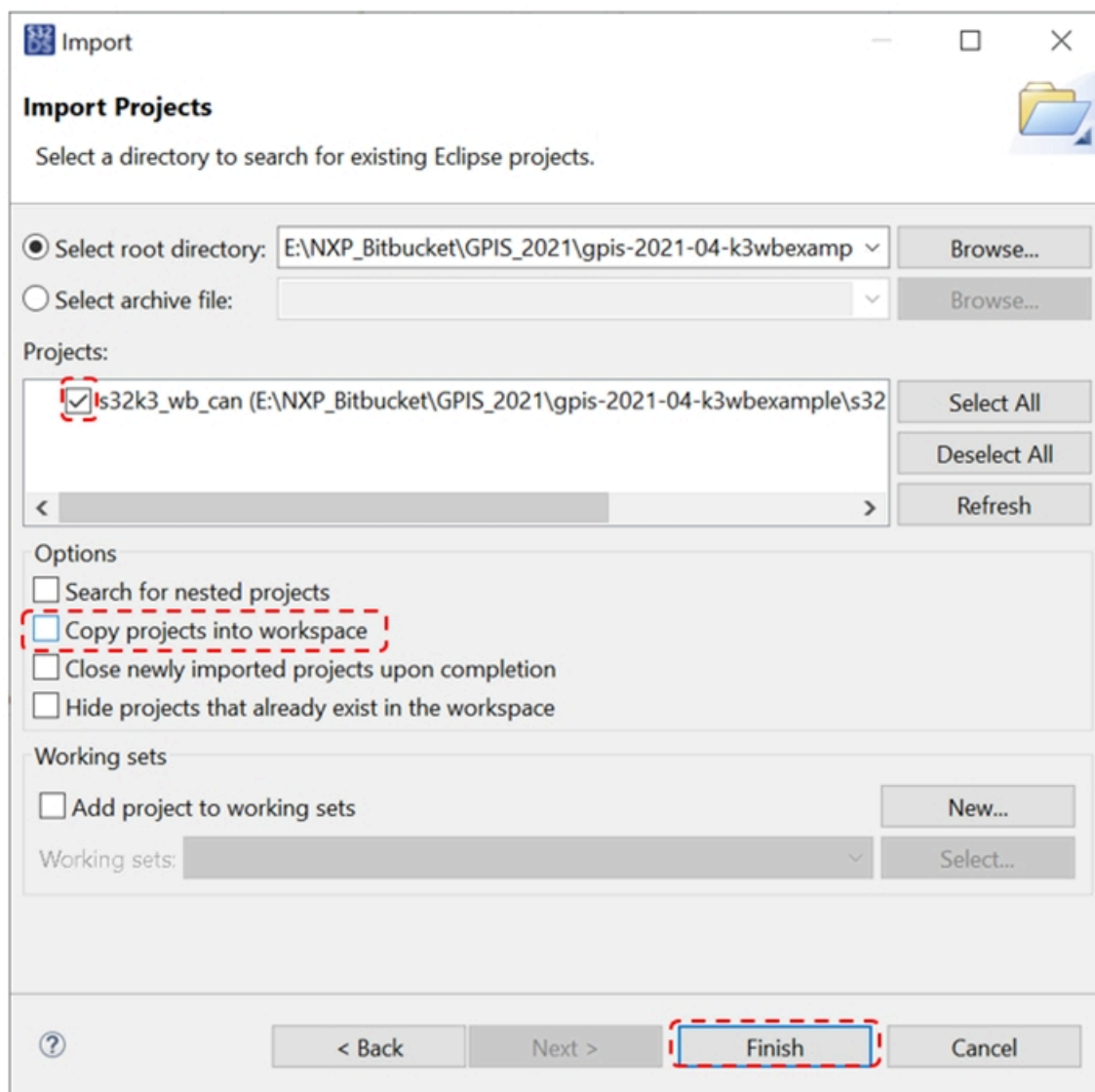
- General -> "Existing Projects into Workspace".



- Browse the project.



- Setting options, then click on Finish to complete.

**Note:**

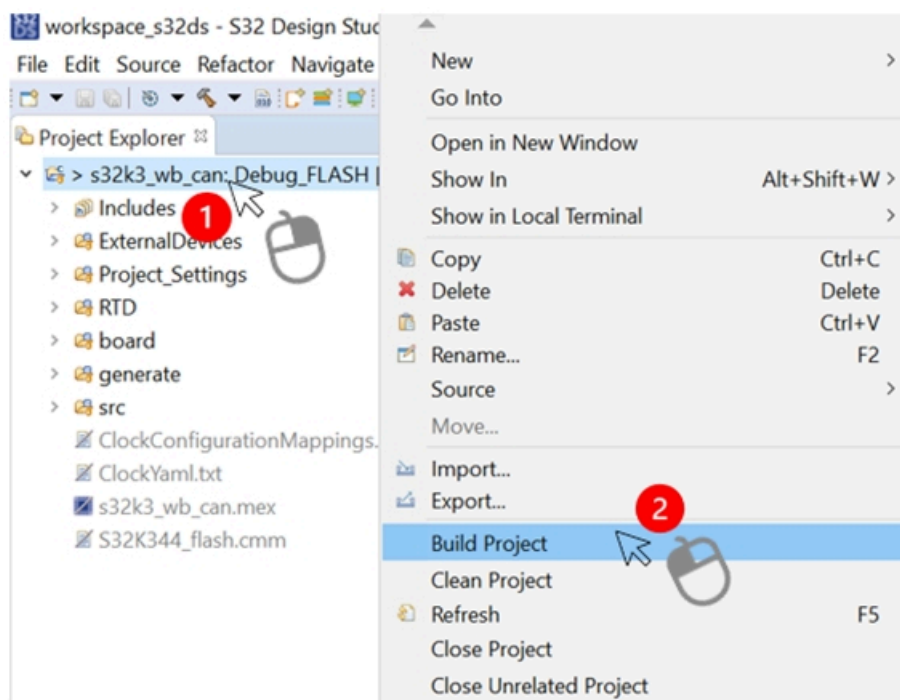
In the above figure, if you plan to do some changes based on the example project, suggest you check the option: ☐ *Copy projects into workspace*. Otherwise, please keep it be unchecked. After check the item, all project files will be copied to your workspace by the importer.

2.2 Generate Code (Cautious)

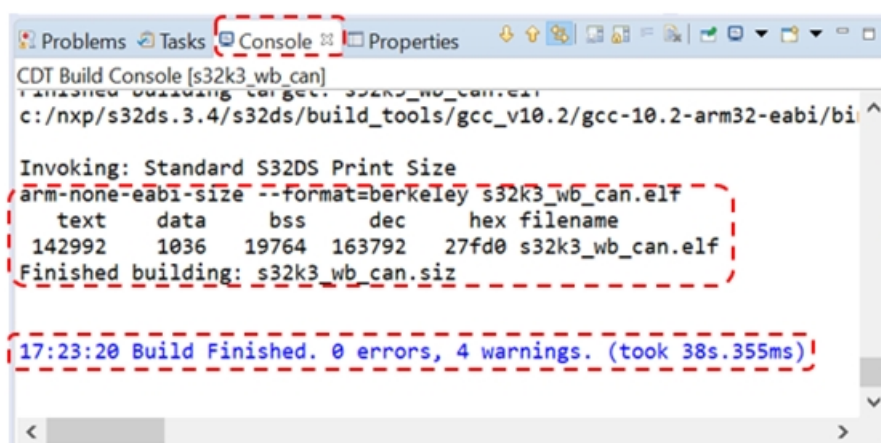
In general, the configuration files must be generated prior compiling the S32DS project. But in this example package, it has been provided in default, and we fixed some issues temporarily in some of the files (will be officially fixed in the new version), so please **DON'T** generate code (If you generate code, the files will be replaced), compile the project directly. Although each project provides a *.mex file, you can refer to the configuration of each module by opening this file.

2.3 Build Project

- Right-click on the project name, click “*Build Project*” to start the build.



- After it complete, you will see the compile result in the “*Console*” window.



- Now you can download and debug with the hardware, please refer to other materials (For example: UG10047).

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