S32K344 Secure Boot Security Features Guide

Document Version: 1.0 Date: 2025-05-07 Author: SignalSlinger

Table of Contents

- 1. Introduction
- 2. <u>Security Features Overview</u>
- 3. <u>Threat Model</u>
- 4. <u>Security Mitigations</u>
- 5. <u>Key Management Security</u>
- 6. Runtime Security
- 7. <u>Security Lifecycle</u>
- 8. <u>Security Recommendations</u>
- 9. <u>Security Evaluation</u>

Introduction

This document details the security features implemented in the S32K344 Secure Boot solution, the threats they address, and recommended practices for maintaining security. It serves as both a reference for security features and guidance on security best practices.

Security Features Overview

The S32K344 Secure Boot implementation includes the following key security features:

Feature	Description	Security Benefit
ECC P-256 Signature Verification	Cryptographic verification of firmware	Prevents unauthorized firmware execution
Hardware Security Engine (HSE)	Dedicated security hardware	Hardware-based security with key protection
Memory Protection	Configuration of protected memory regions	Prevents runtime tampering with secure assets
Boot Status Indicators	Visual feedback on boot status	Enables detection of boot failures
Secure Key Management	Separation of signing and verification keys	Protects signing capability

Threat Model

The secure boot implementation addresses the following threat categories:

Firmware Tampering

Threat: Attackers may attempt to modify firmware to introduce malicious code.

Impact: Unauthorized access, data theft, system dysfunction, or safety hazards.

Unauthorized Firmware

Threat: Attackers may attempt to load unauthorized firmware onto the device.

Impact: IP theft, feature unlocking, device repurposing, or security bypass.

Key Extraction

Threat: Attackers may attempt to extract cryptographic keys.

Impact: Ability to sign malicious firmware that would be accepted by devices.

Debug Interface Attacks

Threat: Unauthorized debug access may be used to bypass security.

Impact: Memory inspection, code extraction, or security bypass.

Security Mitigations

Firmware Authentication

Implementation:

- ECC P-256 digital signatures verify firmware authenticity
- Signatures created with private key (offline)
- Verification performed with public key (on-device)

Mitigation Strength: Strong protection against firmware tampering and unauthorized firmware.

Potential Weaknesses:

- Relies on secrecy of private key
- Verification occurs only at boot time

Memory Protection

${\bf Implementation:}$

- Configuration of protected memory regions
- Execution prevention from data regions
- Access control based on security state

Mitigation Strength: Good protection against runtime attacks and code injection.

Potential Weaknesses:

- Configuration errors may leave gaps
- Some sophisticated attacks might bypass protection

Boot Failure Handling

Implementation:

- Visual indicators for boot status
- Containment of failed boot attempts

Mitigation Strength: Good detection of boot failures, prevents execution of invalid code.

Potential Weaknesses:

- Limited recovery options
- Dependent on proper LED functionality

Key Management Security

Key Generation

Keys must be generated in a secure environment using proper randomness sources. The implementation uses OpenSSL with appropriate parameters:

openssl ecparam -name prime256v1 -genkey -noout -out private_key.pem

Private Key Protection

The private key must never be stored on the device and should be protected using:

- Hardware Security Modules (HSMs) in production environments
- · Access controls and encryption for key storage
- Proper key backup and recovery procedures

Public Key Storage

The public key is stored on the device and used for signature verification. Security considerations:

- Public key cannot be modified after programming
- Multiple public keys can be supported for key rotation
- Verification of the public key itself is recommended

Runtime Security

Beyond secure boot, the implementation includes:

Memory Access Protection

- ullet Configuration of execute-never (XN) regions
- Separation of code and data memory
- Protection of sensitive configuration data

Runtime Integrity

- Basic integrity checks during operation
- Status monitoring via LED indicators
- Error detection and handling

Security Lifecycle

The secure boot implementation supports the following security lifecycle stages:

- 1. Development: Full debug access, test keys
- 2. Production Provisioning: Loading of production keys, configuration
- 3. Deployed: Locked down, limited debug, full security
- 4. End-of-Life: Optional secure decommissioning

Security Recommendations

Development Practices

1. Separate Development and Production Keys

- Never use development keys in production
- Implement proper key rotation procedures

2. Secure Build Environment

- Validate build tools and dependencies
- Use reproducible build processes
- Implement build signing and verification

3. Code Reviews and Testing

- Perform security-focused code reviews
- Implement security testing for each release
- Consider formal verification for critical components

Deployment Practices

1. Secure Manufacturing

- Implement secure provisioning procedures
- Protect manufacturing environment
- Audit provisioning results

2. Key Ceremony

- Establish formal key generation ceremonies
- Document key handling procedures
- Implement multi-person controls for key operations

3. Secure Updates

- Implement secure update procedures
- Verify firmware authenticity before updates
- Consider version rollback protection

Security Evaluation

Security Testing Methods

1. Penetration Testing

- Boot bypass attempts
- Signature forgery attempts
- Memory protection testing
- Debug interface testing

2. Side-Channel Analysis

- Power analysis during cryptographic operations
- \bullet Timing analysis during verification
- \bullet Electromagnetic emissions analysis $\,$

3. Fault Injection

- Clock glitching
- Voltage glitching
- Temperature manipulation

Security Certifications

For critical applications, consider pursuing:

- Common Criteria certification
- FIPS 140-2/3 validation
- Industry-specific security certifications

Appendix: Threat Countermeasure Matrix

Threat	Countermeasure	Implementation
Firmware Tampering	Digital Signature Verification	ECC P-256 signatures verified by HSE
Unauthorized Firmware	Public Key Verification	Embedded verified public key used for authentication
Key Extraction	Hardware Key Protection	Keys managed by HSE, private key never on device
Debug Interface Attacks	Debug Access Control	Configuration of debug authentication and permissions
Runtime Tampering	Memory Protection	XN regions and access control configuration