

# Presentation

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here is my presentation starts:

## Question statement:

A 4:1 multiplexer is to be used for generating the output carry of a full adder. A and B are the bits to be added while  $C_{in}$  is the input carry and  $C_{out}$  is the output carry. A and B are to be used as select bits with A being more significant select bit.

Which one of the following statement correctly describes the choice of signals to be connected to the inputs  $I_0, I_1, I_2$  and  $I_3$  so that the output is  $C_{out}$  ?

# options

- ①  $l_0 = 0, l_1 = C_{in}, l_2 = C_{in}, l_3 = 1$
- ②  $l_0 = 1, l_1 = C_{in}, l_2 = C_{in}, l_3 = 1$
- ③  $l_0 = C_{in}, l_1 = 0, l_2 = 1, l_3 = C_{in}$
- ④  $l_0 = 0, l_1 = C_{in}, l_2 = 1, l_3 = C_{in}$

ANSWER:-

$$l_0 = 0, l_1 = C_{in}, l_2 = C_{in}, l_3 = 1$$

# MUX Diagram

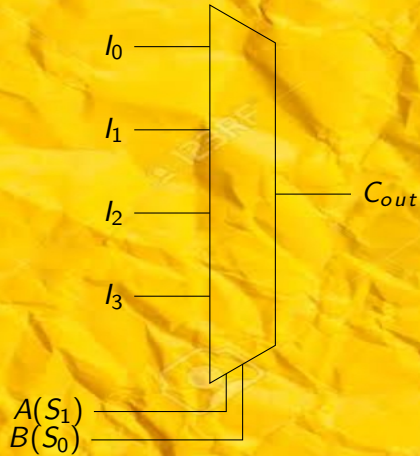


Figure: question MUX diagram

# Solution

## TRUTH TABLE

A	B	$C_{in}$	Sum	$C_{out}$
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

Table: TRUTH TABLE



## Explanation:

		$BC_{in}$			
		00	01	11	10
A	0	0	1	0	1
	1	1	0	1	0

Figure: k-map for Sum

Boolean expression for Sum -

$$\text{Sum} = A\overline{B}\overline{C_{in}} + \overline{A}\overline{B}C_{in} + \overline{A}B\overline{C_{in}} + ABC_{in}$$

# Explanation

$BC_{in}$		00	01	11	10
A	0	0	0	1	0
	1	0	1	1	1

Figure: k-map for  $C_{out}$

Boolean expression for  $C_{out}$  -

$$C_{out} = BC_{in} + AB + AC_{in}$$



## Explanation:

A	B	$C_{out}$
0	0	0
0	1	$C_{in}$
1	0	$C_{in}$
1	1	1

Table: TRUTH TABLE for  $C_{out}$  to be output

So, our final answer is=

$$l_0 = 0, l_1 = C_{in}, l_2 = C_{in}, l_3 = 1$$

COMPLETE

THANK YOU