Project Technical Data

DOS Mario

Created by Wojciech Grędel and Hubert Górowski
Written in Verilog 2001 – Syntax using Vivado 2016.1
Written for Basys 3 FPGA Board with XC7A35-1CPG236C from Xilinx
Version 1.0 - Issued June 3, 2016

This document is used to give information about algorithms used in the game and to describe game proper behavior.

Project: DOS Mario - milestones	Planned Issue Date:	Date Issued:
Project consult	March 1	March 1
Project plan	March 8	March 7
Create project base	March 15	March 7
Adding the monsters	March 22	Not finished
Programming the game physics	March 29	April 20
Step into keyboard steering	April 5	March 10
Add special blocks and bonuses	April 12	May 25
Add coins and lives indicators	April 19	June 2
Improving game graphics	April 26	May 10
Add music	May 5	April 25
Add additional stages	May 17	Not finished
Testing the project	May 20	June 1
Documentation	May 25	June 2
Releasing the project	May 27	June 3

Project Overview:

The goal of the project was to create Mario game similar to the DOS Mario version published in 1995. The project uses Basys 3 board from Digilent as well as external module for creating VGA signal and module for music amplification. The game requires an external keyboard to be connected to the Basys

3 board. The game is displayed on VGA compatible display, with resolution 640x480 and 24bit color depth. The game is written in Verilog, few Matlab scripts are also used.

External modules:

VGA extension board – uses four PMOD connectors on the Basys 3 board, contains Analog Devices ADV7125KSTZ50 chip. It extends bits per color to 8, so the whole color depth is 24 bit. This module was made by us. The PMOD's connectors are not matched pairs but the VGA clock frequency 25MHz appears to be low enough to not cause any problems.

Music amplifier – uses single LM358 operational amplifier to amplify the signal from Basys 3 and put it on the speaker.

Game rules and description:

The rules are quite simple for someone who played Mario at least ones. At the beginning your player - Mario has three lives. The goal is to go through the whole stage and collect as many coins as possible. You collect coins by collision with them. The board is build from different blocks. Some of them are solid, which means that you can't destroy them, some of them are fragile which means that after collision they disappear. To destroy the block you must hit it with at least half of player body. When you fall into a water you will lose one life, after losing all three lives the game will over. The player has two speeds, greater speed does not cause player to jump higher.

Steering:

CTRL - speed up

ALT - jump

RIGHT and LEFT ARROWS - moving

The BTNC on Basys 3 restarts the Game.

Clocking sources:

The game uses sets of generated clocks for operation:

25MHz for VGA display

50MHz for complex math formulas

100MHz for high speed algorithms

600Hz for very slow tasks

200kHz for keyboard

We used clocking Wizard (5.3) for generation of 25MHz for buffering main 100MHz clock, and custom module for other clocks.

Output Ports:

Our game uses all four external PMOD connectors, below is pinout we use:

JA1 - VGAext Clk

JA4 - Music output

JA7-9 – VGAext synchronizations signals

JB, JC, JXADC – VGAext color bits

For more detailed description please see the .xdc constraint file.

Program structure and used algorithms:

In the program displaying part is written in separation to the game algorithms part. The most important module in the game is GameEngine. This module calculates actual Mario position, communicates with stage decoder, read data from keyboard, count points and Mario lives, gives instruction to stage decoder to delete block in RAM.

Actual board is saved in the RAM, there is a copy in the ROM which is used when restarting the stage or when reseting.

GAME ENGINE:

Mario movement:

Mario movement is divided into vertical and horizontal movement, which are independent form each other. Those are simple state machines that checks if an arrow on keyboard is pressed. Those state machines checks also blocking register which contains data about blocking blocks around Mario.

Background movement:

If player reaches the most right or left allowed position the background starts to move, it moves faster than player, as it was in the DOS game.

Board movement:

It is achieved similarly to the background movement, the main difference is that it moves with the same speed as Mario.

Checking blocks around:

It is written using state machine Every time Mario changes position it checks what kind of blocks are around him. State machine contains of three main states:

- Checking if the block is blocking or not. It update blocking register which is used by Mario movement state machines.
- Saving what kind of blocks are around Mario.
- Checking if the block is a special one. If that certain kind of operation is performed and new block is saved into RAM

Mario lives:

If Mario falls to the water, it finally reaches the lower position possible, a signal is then send to the combinational logic which lower lives by one, a restart_game signal is also send to different state machines and modules. Every module resets parameters to default values, original stage is also loaded into RAM. When every module using restart_game finishes its operation a new game is started.

Mario coins:

When coin_containing_block or regular coin is scored signal is send from state machine. Every time the signal is send number of coins is incremented by one.

KEYBOARD:

We are using keyboard module found in the Internet. We modified it for our purpose. It simply decodes the PS/2 protocol and sends forward information about pressed keys.

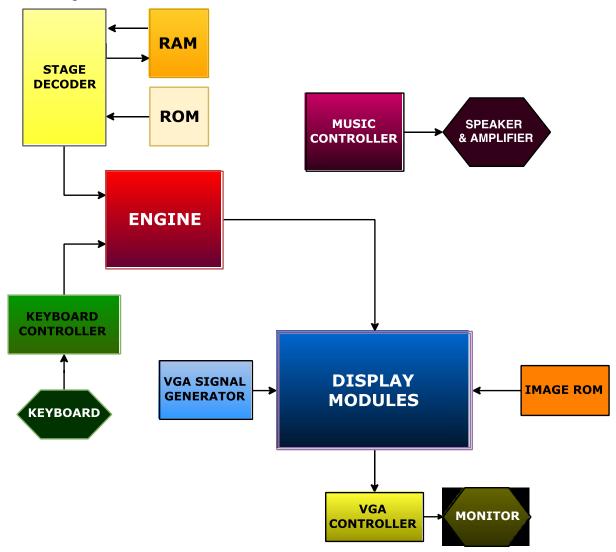
BOARD DISPLAY:

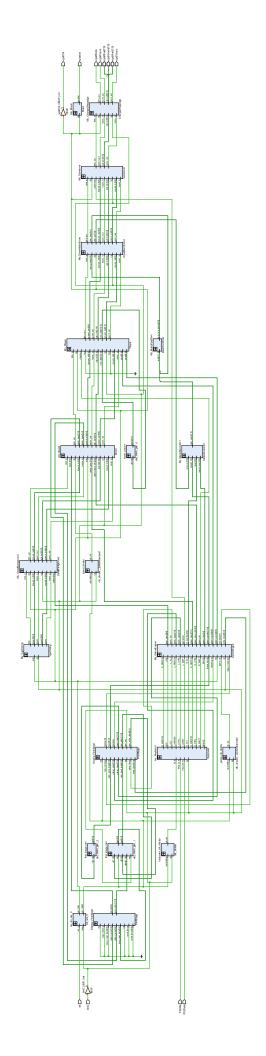
This module gets data from GameEngine about actual board position. Is reads data from ROM and put different colors on the rgb bus depending on what kind of block actual pixel is going through.

The operation of other modules is quite straightforward, comments are also added if needed.

Below we include block diagram, Vivado RTL schematic and code of not-ROM modules.

Block diagram:





```
`timescale 1 ns / 1 ps
///////
// Company:
                AGH UST
// Engineer: Wojciech Gredel, Hubert Górowski
//
// Create Date:
// Design Name:
// Module Name:
                 Board
// Project Name:
                DOS Mario
// Target Devices: Basys3
// Tool versions: Vivado 2016.1
// Description:
// This module displays board: blocks, special blocks, coins and
everything saved in stage RAM.
// Dependencies:
//
// Revision:
// Revision 0.01 - Module created
// Additional Comments:
///////
module Board (
   input wire [9:0] hcount_in,
   input wire hsync_in,
   input wire [9:0] vcount_in,
   input wire vsync in,
   input wire pclk,
   input wire [23:0] rgb in,
   input wire blnk in,
   input wire clk,
   input wire rst,
   input wire [7:0] plane_xpos,
   input wire [5:0] plane_xpos_ofs,
   input wire [5:0] block,
   output reg [7:0] block xpos,
   output reg [3:0] block ypos,
   output reg [9:0] hcount out,
   output reg hsync out,
   output reg [9:0] vcount out,
   output reg vsync out,
   output reg [23:0] rgb out,
   output reg blnk out
   );
   //Definition of blocks, makes it easier to use
   localparam A = 1 ;
   localparam B = 0 ;
   localparam C = 2 ;
   localparam D = 3 ;
   localparam E = 4 ;
   localparam F = 5 ;
   localparam G = 6 ;
   localparam H = 7;
   localparam I
                 = 8;
   localparam J = 9;
   localparam K = 10;
   localparam L = 11;
```

```
localparam M = 12;
    localparam N = 13;
    localparam 0 = 14;
    localparam P = 15;
    localparam Q = 16;
    localparam R = 17;
    localparam S = 18;
    localparam T = 19;
    localparam U = 20;
    localparam V = 21;
    localparam W = 22;
    localparam X = 23;
    localparam Y = 24;
    localparam Z =
                      25;
    localparam AY =
                      26;
    localparam IY = 27;
   localparam IY =
localparam GY =
localparam KY =
localparam PY =
localparam UY =
localparam WY =
localparam DY =
                      28;
                      29;
                       30;
                       31;
                       32;
                       33;
                      34;
    reg [23:0] rgb nxt;
    reg [7:0] block_xpos_nxt;
    reg [3:0] block_ypos_nxt;
    reg [11:0] ram_addr_nxt;
    always @* begin
        block ypos nxt = ((479 - vcount in) >> 2)/10;
        block xpos nxt = plane xpos + ((hcount in +
plane xpos ofs)>>2)/10;
    end
    always @* begin
        case(block) //block colors
           A : rgb_nxt = 24'h0_20_20;
           B : rgb nxt = rgb in;
            C: rgb nxt = 24'h6A 3D 1E;
            D : rgb nxt = 24'hBB 77 00 ;
           E : rgb_nxt = 24'h00_00_00;
            F : rgb nxt = 24'h00 00 00 ;
            G : rgb nxt = 24'h10 50 10 ;
           H : rgb nxt = 24'h00 00 00 ;
            I : rgb nxt = 24'h00'00'00';
            J : rgb nxt = 24'h70 10 10 ;
           K : rgb nxt = 24'h00 00 00 ;
           L: rgb nxt = 24'h6A 3D 1E;
           M : rgb_nxt = 24'h6A_3D_1E;
           N : rgb nxt = 24'h6A 3D 1E ;
           0 : rgb nxt = 24'h50 30 20 ;
            P: rgb nxt = 24'h6A 3D 1E;
           Q : rgb nxt = 24'h10 50 10 ;
           R : rgb nxt = rgb in ;
           S: rgb nxt = 24'h6A 3D 1E;
           T : rgb nxt = rgb in ;
           U : rgb nxt = 24 h10 50 10 ;
           V : rgb nxt = 24'h10 50 10 ;
           W : rgb nxt = 24'h00'00'f0';
           X : rgb nxt = 24'h27 75 02;
```

```
Y : rgb nxt = rgb in ;
           Z : rgb = 24 h30 30 f0 ;
           AY: rgb nxt = 24'h00'00'00';
           GY: rgb nxt = 24'hff ff 00;
           KY: rgb nxt = 24'h00'00'00';
           PY: rgb nxt = 24'h00'00'00';
           TY: rgb nxt = 24'h00'00'00';
           WY: rgb nxt = 24'hb0 70 20;
           DY: rgb_nxt = 24'h7A_4D_2E;
           default: rgb_nxt = rgb_in;
        endcase
    end
    always @(posedge clk or posedge rst) begin
        if(rst) begin
           block xpos <= #1 0;
           block ypos <= #1 0;
        end
       else begin
           block_xpos <= #1 block_xpos_nxt;
block_ypos <= #1 block_ypos_nxt;</pre>
       end
    end
    always @(posedge pclk or posedge rst) begin
        if(rst) begin
                       <= #1 0;
           rgb out
           hcount_out <= #1 0;
           hsync_out <= #1 0;
           vcount_out <= #1 0;</pre>
           vsync_out <= #1 0;</pre>
           blnk out <= #1 0;
       end
        else begin
                       <= #1 rgb_nxt;
           rgb out
           hcount_out <= #1 hcount_in;</pre>
           hsync_out <= #1 hsync_in;
           vcount_out <= #1 vcount_in;</pre>
           vsync out <= #1 vsync in;</pre>
           blnk out \leftarrow #1 blnk in;
        end
    end
endmodule
`timescale 1 ns / 1 ps
///////
// Company:
                   AGH UST
// Engineer: Wojciech Gredel, Hubert Górowski
//
// Create Date:
// Design Name:
// Module Name:
                   Change2Negedge
// Project Name: DOS Mario
// Target Devices: Basys3
// Tool versions:
                   Vivado 2016.1
// Description:
// A module which is used to put input data out on negedge of clock
//
// Dependencies:
```

//

```
// Revision:
// Revision 0.01 - Module created
// Additional Comments:
///////
module Change2Negedge
   input wire hsync in,
   input wire vsync in,
   input wire blnk in,
   input wire [23:0] rgb in,
   input wire clk,
   input wire rst,
   output reg hsync out,
   output reg vsync out,
   output reg blnk out,
   output reg [23:0] rgb out
   always @(negedge clk or posedge rst) begin
      if(rst) begin
         hsync out
                   <= #1 0;
                  <= #1 0;
         vsync_out
         blnk out
                   <= #1 0;
         rgb out
                   <= #1 0;
      end
      else begin
         hsync out
                  <= #1 hsync in;
         vsync_out <= #1 vsync_in;</pre>
         blnk_out <= #1 blnk_in;
                  <= #1 rgb in;
         rgb_out
      end
   end
endmodule
`timescale 1 ns / 1 ps
///////
// Company:
               AGH UST
// Engineer: Wojciech Gredel, Hubert Górowski
//
// Create Date:
// Design Name:
// Module Name:
               Clouds
// Project Name: DOS Mario
// Target Devices: Basys3
// Tool versions: Vivado 2016.1
// Description:
//
    This module displays clouds
//
// Dependencies:
//
// Revision:
// Revision 0.01 - Module created
// Additional Comments:
///////
```

```
module Clouds (
      input wire clk,
      input wire rst,
      input wire [9:0] xoffset,
      input wire [9:0] hcount in,
      input wire hsync in,
      input wire [9:0] vcount in,
      input wire vsync_in,
      input wire [23:0] rgb in,
      input wire blnk_in,
      output reg [9:0] hcount out,
      output reg hsync out,
      output reg [9:0] vcount out,
      output reg vsync out,
      output reg [23:0] rgb out,
      output reg blnk out
      localparam MAX CLOUDS = 11;
      localparam YOFFSET = 100;
      localparam XRES = 640;
      localparam YRES = 480;
      localparam CLOUD COLOR = 24'h88 99 cc;
      localparam reg [10:0] CLOUD MAP X [0:10] = { 30, 90, 180, 220, 320,
450, 530, 590, 615, 0, 0};
      localparam reg [10:0] CLOUD MAP Y [0:10] = {100,100, 100, 100,
70, 100, 100, 80, 0, 0};
      localparam reg [10:0] CLOUD MAP S [0:10] = { 30, 70, 25, 50, 100,
70,
    25, 50, 40, 0, 0};
      reg [23:0] rgb nxt;
      reg [3:0] i;
      reg [10:0] xpos;
      always @(posedge clk or posedge rst) begin
        if(rst) begin
            hcount out \leftarrow #1 0;
            hsync out <= #1 0;
            vcount out <= #1 0;
            vsync out <= #1 0;
            rgb out <= #1 0;
            blnk out <= #1 0;
        end
        else begin
            hcount out <= #1 hcount in;
            hsync out <= #1 hsync in;
            vcount out <= #1 vcount in;</pre>
            vsync out <= #1 vsync in;</pre>
            rgb_out <= #1 rgb_nxt;
blnk_out <= #1 blnk_in;
        end
      end
    always @* begin
        xpos = (hcount in + xoffset) % (XRES - 1);
      end
      always @* begin
            if((YRES -1 - vcount in) < YOFFSET ) rgb nxt = CLOUD COLOR;</pre>
```

```
else begin
           //REQUIRES CHANGE BECAUSE OF TAKING TOO MUCH LUTS AND DSP, IT
IS JUST CIRCLE EQUATION
                if(((xpos - CLOUD MAP X[0])*(xpos - CLOUD MAP X[0]) +
(YRES -1 - vcount in - CLOUD MAP Y[0])*(YRES -1 - vcount in -
CLOUD MAP Y[0]) < CLOUD MAP S[0] * CLOUD MAP S[0]) rgb nxt = CLOUD COLOR;
           else if(((xpos - CLOUD MAP X[1])*(xpos - CLOUD MAP X[1]) +
(YRES -1 - vcount in - CLOUD MAP Y[1])*(YRES -1 - vcount in -
CLOUD MAP Y[1]) < CLOUD MAP S[1] *CLOUD MAP S[1]) rgb nxt = CLOUD COLOR;
           else if(((xpos - CLOUD MAP X[2])*(xpos - CLOUD MAP X[2]) +
(YRES -1 - vcount in - CLOUD MAP Y[2])*(YRES -1 - vcount in -
CLOUD MAP Y[2])) < CLOUD MAP S[2] *CLOUD MAP S[2]) rgb nxt = CLOUD COLOR;
           else if(((xpos - CLOUD MAP X[3])*(xpos - CLOUD MAP X[3]) +
(YRES -1 - vcount in - CLOUD MAP Y[3])*(YRES -1 - vcount in -
CLOUD MAP Y[3]) < CLOUD MAP S[3] *CLOUD MAP S[3]) rgb nxt = CLOUD COLOR;
           else if(((xpos - CLOUD MAP X[4])*(xpos - CLOUD MAP X[4]) +
(YRES -1 - vcount in - CLOUD MAP Y[4])*(YRES -1 - vcount in -
CLOUD MAP Y[4])) < CLOUD_MAP_S[4] *CLOUD_MAP_S[4]) rgb_nxt = CLOUD_COLOR;
           else if(((xpos - CLOUD MAP X[5])*(xpos - CLOUD MAP X[5]) +
(YRES -1 - vcount_in - CLOUD MAP Y[5])*(YRES -1 - vcount in -
CLOUD MAP Y[5]) < CLOUD MAP S[5]*CLOUD MAP S[5]) rgb nxt = CLOUD COLOR;
           else if(((xpos - CLOUD MAP X[6])*(xpos - CLOUD MAP X[6]) +
(YRES -1 - vcount in - CLOUD MAP Y[6])*(YRES -1 - vcount in -
CLOUD MAP Y[6]) < CLOUD MAP S[6] *CLOUD MAP S[6]) rgb nxt = CLOUD COLOR;
           else if(((xpos - CLOUD_MAP_X[7])*(xpos - CLOUD_MAP_X[7]) +
(YRES -1 - vcount_in - CLOUD_MAP_Y[7])*(YRES -1 - vcount in -
CLOUD_MAP_Y[7]))< CLOUD_MAP_S[7]*CLOUD_MAP_S[7]) rgb_nxt = CLOUD_COLOR;</pre>
           else if(((xpos - CLOUD_MAP_X[8])*(xpos - CLOUD_MAP_X[8]) +
(YRES -1 - vcount in - CLOUD MAP Y[8])*(YRES -1 - vcount in -
CLOUD MAP Y[8])) < CLOUD MAP S[8] *CLOUD MAP S[8]) rgb nxt = CLOUD COLOR;
           else if(((xpos - CLOUD MAP X[9])*(xpos - CLOUD MAP X[9]) +
(YRES -1 - vcount in - CLOUD MAP Y[9])*(YRES -1 - vcount in -
CLOUD MAP Y[9]) < CLOUD MAP S[9] *CLOUD MAP S[9]) rgb nxt = CLOUD COLOR;
           else rgb nxt = rgb in;
        end
     end
endmodule
`timescale 1 ns / 1 ps
///////
// Company:
                   AGH UST
// Engineer: Wojciech Gredel, Hubert Górowski
// Create Date:
// Design Name:
// Module Name:
                   DrawBackground
// Project Name:
                   DOS Mario
// Target Devices: Basys3
// Tool versions:
                   Vivado 2016.1
// Description:
//
     This module displays background
//
// Dependencies:
//
// Revision:
// Revision 0.01 - Module created
// Additional Comments:
```

```
module DrawBackground (
     input wire [9:0] hcount in,
     input wire hsync in,
     input wire [9:0] vcount in,
     input wire vsync in,
     input wire blnk in,
     input wire [9:0] xoffset,
     input wire clk,
     input wire rst,
     output reg [9:0] hcount out,
     output reg hsync out,
     output reg [9:0] vcount out,
     output reg vsync out,
     output reg [23:0] rgb out,
     output reg blnk out
 );
    //Colors
     localparam BLACK = 24'h00_00_00;
     localparam WHITE = 24'hff ff ff;
     //Cords
     localparam OFFSET = 100;
     localparam YRES
     reg [7:0] r, g, b;
     reg [23:0] rgb_cl;
     reg [9:0] hcount_cl, vcount cl;
     reg hsync cl, vsync cl, blnk cl;
   wire [9:0] hcount_cl_out, vcount_cl_out;
   wire [23:0] rgb_cl_out;
    Clouds My_Clouds (
       .clk(clk),
       .rst(rst),
       .xoffset(xoffset),
       .hcount in (hcount cl),
       .hsync in(hsync cl),
       .vcount in (vcount cl),
       .vsync in (vsync cl),
       .rqb in(rqb cl),
       .blnk in (blnk cl),
       .hcount out (hcount cl out),
       .hsync out (hsync cl out),
       .vcount out (vcount cl out),
       .vsync out(vsync cl out),
       .rgb out (rgb cl out),
       .blnk out (blnk cl out)
     );
     always @(posedge clk or posedge rst) begin
       if(rst) begin
                      <= #1 0;
           hcount cl
                      <= #1 0;
           hsync cl
           vcount cl <= #1 0;
           vsync cl
                      <= #1 0;
```

```
<= #1 0;
           rgb cl
                      <= #1 0;
           blnk cl
           hcount out \leftarrow #1 0;
           hsync out
                      <= #1 0;
           vcount_out <= #1 0;</pre>
           vsync out <= #1 0;
                      <= #1 0;
           rgb out
                      <= #1 0;
           blnk out
       end
       else begin
           hcount cl <= #1 hcount in;
                      <= #1 hsync in;
           hsync cl
                      <= #1 vcount in;
           vcount cl
                      <= #1 vsync_in;
           vsync cl
                      \leftarrow #1 \{r,g,\overline{b}\};
           rgb cl
                      <= #1 blnk in;
           blnk cl
           hcount out <= #1 hcount cl out;
                      <= #1 hsync cl out;
           hsync out
           vcount out <= #1 vcount cl out;
           vsync_out <= #1 vsync_cl_out;
rgb_out <= #1 rgb_cl_out;</pre>
           blnk out <= #1 blnk cl out;
       end
   end
    //Display gradiented sky
     always @* begin
       if((YRES - vcount in - \frac{1}{1}) < OFFSET) {r,g,b} = WHITE; // any color,
will be overwritten
       else begin
           \{b\} = 8'hff;
           {r} = (vcount_in >> 1);
           \{g\} = (vcount in >> 1);
       end
     end
endmodule
`timescale 1ns / 1ps
///////
// Company:
                  AGH UST
// Engineer: Wojciech Gredel, Hubert Górowski
//
// Create Date:
// Design Name:
// Module Name:
                 DrawMarioScore
// Project Name: DOS Mario
// Target Devices: Basys3
// Tool versions:
                  Vivado 2016.1
// Description:
//
     This module displays mario score, game level and mario lives
//
// Dependencies:
//
// Revision:
// Revision 0.01 - Module created
// Additional Comments:
//
///////
```

```
module DrawMarioScore(
    input wire clk,
    input wire rst,
    input wire [9:0] hcount in,
    input wire hsync in,
    input wire [9:0] vcount in,
    input wire vsync_in,
    input wire [23:0] rgb in,
    input wire blnk_in,
    input wire [7:0] char_pixels,
    output reg [9:0] hcount out,
    output reg hsync out,
    output reg [9:0] vcount out,
    output reg vsync out,
    output reg [23:0] rgb out,
    output reg blnk out,
    output reg [7:0] char xy,
    output reg [3:0] char line
    reg [23:0] rgb nxt;
    localparam XPOS
                        = 40;
    localparam WIDTH = 552
                        = 552;
    localparam HEIGHT = 16;
    always @(posedge clk or posedge rst) begin
        if(rst) begin
            hcount_out <= #1 0;
            vcount_out <= #1 0;
            hsync_out <= #1 0;
            vsync_out <= #1 0;</pre>
            rgb_out <= #1 0;
blnk_out <= #1 0;
        end
        else begin
            hcount out <= #1 hcount in;
            vcount out <= #1 vcount in;</pre>
            hsync out <= #1 hsync in;
            vsync out <= #1 vsync in;</pre>
            rgb_out <= #1 rgb_nxt;
blnk_out <= #1 blnk_in;
        end
    end
    always @* begin
        if ((hoount in >= XPOS) && (hoount in < XPOS + WIDTH) && (vocunt in
>= YPOS) && (vcount in < YPOS + HEIGHT) && (char pixels[(XPOS -
hcount in)]))
        begin
            if(char xy == 8'h20)
               rgb nxt = 24'hff ff 00;
                rgb nxt = 24'hff ff ff;
        end
        else begin
            rgb nxt = rgb in; // pass signal through
        end
    end
```

```
always @* begin
       char xy = (hcount in - XPOS - 1)>>3;
   always @* begin
      char line = vcount in - YPOS;
endmodule
timescale 1 ns / 1 ps
///////
// Company:
                AGH UST
// Engineer: Wojciech Gredel, Hubert Górowski
// Create Date:
// Design Name:
// Module Name:
                 FirstStage
// Project Name:
                 DOS Mario
// Target Devices: Basys3
// Tool versions:
                 Vivado 2016.1
// Description:
// A module which is used connect RAM with saved board and other
modules.
//
// Dependencies:
//
// Revision:
// Revision 0.01 - Module created
// Additional Comments:
//
///////
module FirstStage
(
   input wire [7:0] plane xpos,
   input wire [3:0] plane ypos,
   input wire [5:0] ram read data,
   input wire [5:0] copy read data,
   input wire [5:0] write block,
   input wire save block,
   input wire copy backup,
   input wire clk,
   input wire rst,
   output reg [11:0] ram addr,
   output reg [11:0] copy addr,
   output reg [5:0] block,
   output reg blocking,
   output reg ram we,
   output reg [5:0] ram write data,
   output reg backuped
);
   localparam A
                 = 0;
   localparam B
   localparam C = 2 ;
   localparam D = 3 ;
   localparam E
                 = 4;
```

```
localparam F = 5;
localparam G = 6 ;
localparam H = 7;
localparam I = 8;
localparam J = 9;
localparam K = 10;
localparam L = 11;
localparam M = 12;
localparam N = 13;
localparam 0 = 14;
localparam P = 15;
localparam Q = 16;
localparam R = 17;
localparam S = 18;
localparam T = 19;
localparam U = 20;
localparam V = 21;
localparam W = 22;
localparam X = 23;
localparam Y = 24;
localparam Z = 25;
localparam AY = 26;
localparam IY = 27;
localparam GY = 28;
localparam KY = 29;
localparam PY = 30;
localparam TY = 31;
localparam UY = 32;
localparam WY = 33;
localparam DY = 34;
localparam U = 20;
localparam DY = 34;
reg [1:0] state, state nxt;
localparam NORMAL MODE = 2'b00;
localparam START BACKUP = 2'b01;
localparam BACKUP = 2'b10;
reg backuped nxt;
reg [11:0] saving addr, saving addr nxt;
localparam ROM STAGE SIZE = 2160;
always @(posedge clk or posedge rst) begin
     if(rst) begin
         backuped \leftarrow #1 0;
          state <= #1 START BACKUP;</pre>
          saving addr <= #1 0;</pre>
     end
     else begin
                       <= #1 backuped nxt;
         backuped
         state <= #1 state nxt;
         saving addr <= #1 saving addr nxt;</pre>
     end
end
always @* begin
     case (state)
          NORMAL MODE: begin
              backuped nxt = 0;
              copy_addr = 0;
              saving addr nxt = 0;
               ram addr = (11-plane ypos)*180 + plane xpos;
               if(save block) begin
```

```
ram_write_data = write_block;
                   ram_we = 1;
                   block = ram_read_data;
                   blocking= 0;
               end
               else begin
                   ram_write_data = write_block;
                   ram we = 0;
                   block = ram_read_data;
                   if((block == \overline{A}) || (block == D) || (block == C) || (block
== S) || (block == L) || (block == N) || (block == J) || (block == M) || (block
== P) || (block == WY) || (block == DY)) begin
                     blocking = 1;
                   end
                   else begin
                     blocking = 0;
                end
                if(copy backup)
                   state nxt = START BACKUP;
                   state nxt = NORMAL MODE;
            end
            START BACKUP: begin
               backuped_nxt = 0;
copy_addr = 0;
               saving_addr_nxt = 0;
               ram_addr = 0;
               ram write data = 0;
               ram_we = 0;
               block
                              = 0;
               blocking = 0;
state_nxt = BACKUP;
            end
           BACKUP: begin
               copy_addr = saving_addr;
               saving_addr_nxt = saving_addr + 1;
               ram_addr = saving_addr;
               ram write data = copy read data;
               if(saving addr == ROM STAGE SIZE) begin
                   backuped nxt= 1;
                   ram we = 0;
                   state nxt = NORMAL MODE;
               end
               else begin
                   backuped nxt= 0;
                   ram we = 1;
                   state nxt = BACKUP;
               block = 0;
               blocking= 0;
           end
            default: begin
               backuped nxt
                             = 0;
               copy_addr = 0;
               saving_addr_nxt = 0;
               ram addr = 0;
               ram write data = 0;
               ram\_we = 0; block = 0;
               blocking = 0;
```

```
state nxt
                           = NORMAL MODE;
          end
       endcase
   end
endmodule
timescale 1 ns / 1 ps
///////
// Company:
                AGH UST
// Engineer: Wojciech Gredel, Hubert Górowski
// Create Date:
// Design Name:
// Module Name:
                MarioScore24x1
// Project Name:
                 DOS Mario
// Target Devices: Basys3
// Tool versions: Vivado 2016.1
// Description:
// This is the engine module, it controlls:
   *Mario movement
// *Mario lives
  *Background movement
//
// *Hitting the blocks
//
//
// Dependencies:
//
// Revision:
// Revision 0.01 - Module created
// Additional Comments:
//
///////
module GameEngine
(
   input wire clk,
   input wire game_clk,
   input wire rst,
     input wire L ALT,
   input wire R ALT,
   input wire L CTRL,
   input wire R CTRL,
   input wire SPACE,
   input wire L SHIFT,
   input wire R SHIFT,
   input wire ESC,
   input wire D ARROW,
   input wire L ARROW,
   input wire R ARROW,
   input wire blocking in,
   input wire [5:0] block in,
   input wire stage restarted,
   output reg [7:0] block xpos,
   output reg [3:0] block ypos,
   output reg [9:0] bcgr xpos,
   output reg [7:0] plane xpos,
   output reg [5:0] plane xpos ofs,
   output reg [9:0] player xpos,
   output reg [8:0] player ypos,
```

```
output reg player dir,
  output reg monster 1 dir,
  output reg [1:0] player speed,
  output reg gameover,
  output reg [5:0] write block,
  output reg block we,
  output reg restartgame,
  output reg [3:0] player_life,
  output reg [11:0] player_points,
  output reg [3:0] lvl_number
);
   always @(posedge clk) begin
      lvl number <=1 ;</pre>
  reg gameover nxt;
   reg restartgame nxt;
   reg player xpos restarted;
   reg player ypos restarted;
  reg bcgr restarted;
  reg board restarted;
  reg player xpos restarted nxt;
  reg player ypos restarted nxt;
  reg bcgr restarted nxt;
  reg board restarted nxt;
   //******
   //State Machine for player
   //********
  reg [3:0] player life nxt;
   reg [1:0] player hor state;
  reg [1:0] player_ver_state;
  reg [1:0] player_hor_state_nxt;
  reg [1:0] player_ver_state_nxt;
  reg [7:0] jump_height;
   reg [7:0] jump_height_nxt;
   reg [9:0] player_xpos_nxt;
  reg [8:0] player ypos nxt;
  reg player dir nxt;
   reg [1:0] player speed nxt;
   localparam PL_STOP = 2'b00;
   localparam PL_RIGHT = 2'b01;
   localparam PL LEFT = 2'b10;
   localparam PL JUMP = 2'b01;
   localparam PL FALL = 2'b10;
   localparam DIR RIGHT = 1'b0;
   localparam DIR LEFT = 1'b1;
   localparam PL STAND = 2'b00;
   localparam PL WALK = 2'b01;
   localparam PL RUN = 2'b10;
   localparam PLAYER WIDTH = 40;
   localparam PLAYER XPOS MAX = 480;
   localparam PLAYER XPOS MIN = 120;
   localparam PLAYER YPOS MAX = 480;
  //Speed
   always @* begin
       if(L ARROW | | R ARROW) begin
           if(L CTRL || R CTRL)
               player speed nxt = PL RUN;
```

```
else
                player_speed_nxt = PL_WALK;
        end
        else
            player_speed_nxt = PL STAND;
    end
    //Lifes
   always @(posedge clk or posedge rst) begin
        if(rst) begin
            player_life <= 3;</pre>
            gameover <= 0;
            restartgame <= 0;
        end
        else begin
            player life <= player life nxt;</pre>
            gameover <= gameover nxt;</pre>
            restartgame <= restartgame nxt;</pre>
        end
    end
   reg [11:0] player points nxt;
    reg [11:0] player_points_latched;
    reg [11:0] player points latched nxt;
    //Points
   always @(posedge clk or posedge rst) begin
        if(rst) begin
            player points <= 0;</pre>
            player points latched <= 0;</pre>
        end
        else begin
            player points <= player points nxt;//player points nxt;</pre>
            player_points_latched <= player_points_latched_nxt;</pre>
        end
   end
    always @* begin
        if((new point) && (player points latched == player points)) begin
            player points nxt = player points + 1;
            player points latched nxt = player points latched;
        end
        else begin
            player points nxt = player points;
            if(new point)
                player points latched nxt = player points latched;
            else
                player points latched nxt = player points;
        end
   end
    //loosing lifes, gameover
   always @* begin
        if(gameover) begin
            restartgame nxt = 1;
            player life nxt = 0;
            gameover nxt = 1;
        end
        else if(restartgame) begin
            if((player xpos restarted) && (player ypos restarted) &&
(board restarted) && (bcgr restarted) && (stage restarted)) begin
```

```
restartgame nxt = 0;
            player_life_nxt = player_life;
            gameover nxt = gameover;
        end
        else begin
            restartgame nxt = 1;
            player_life_nxt = player_life;
            gameover_nxt = gameover;
        end
    end
    else if(player_ypos == 0) begin
        if(player life == 1) begin
            gameover nxt = 1;
            player life nxt = 0;
            restartgame nxt = 1;
        end
        else begin
            player life nxt = player life - 1;
            gameover nxt = 0;
            restartgame nxt = 1;
        end
    end
    else begin
        gameover nxt = 0;
        player life nxt = player life;
        restartgame nxt = 0;
    end
end
//Player horizontal movement
always @* begin
    if(restartgame) begin
        player xpos nxt
                           = 120;
        player_hor_state_nxt= 0;
        player_dir_nxt = 0;
        player_xpos_restarted_nxt = 1;
    end
    else begin
        player xpos restarted nxt = 0;
        case(player hor state)
            PL STOP: begin
                if(L ARROW) begin
                    if(blocking[1]) player hor state nxt = PL STOP;
                    else player hor state nxt = PL LEFT;
                end
                else if (R ARROW) begin
                    if(blocking[0]) player hor state nxt = PL STOP;
                    else player hor state nxt = PL RIGHT;
                else begin
                    player hor state nxt = PL STOP;
                player xpos nxt = player xpos;
                player dir nxt = player dir;
            end
            PL RIGHT: begin
                if(L ARROW) begin
                    if(blocking[1]) player hor state nxt = PL STOP;
                    else player hor state nxt = PL LEFT;
                end
                else if(R ARROW) begin
```

```
if(blocking[0]) player_hor_state_nxt = PL_STOP;
                         else player_hor_state_nxt = PL_RIGHT;
                     end
                     else begin
                         player_hor_state_nxt = PL_STOP;
                     if(((player xpos) < PLAYER XPOS MAX)&&(blocking[0]==0))</pre>
begin
                         player_xpos_nxt = player_xpos + 1;
                         player_dir_nxt = DIR_RIGHT;
                     end
                     else begin
                         player xpos nxt = player xpos;
                         player dir nxt = DIR RIGHT;
                     end
                end
                PL LEFT: begin
                     if(L ARROW) begin
                         if(blocking[1]) player hor state nxt = PL STOP;
                         else player hor state nxt = PL LEFT;
                     end
                     else if (R ARROW) begin
                         if(blocking[0]) player hor state nxt = PL STOP;
                         else player hor state nxt = PL RIGHT;
                     end
                     else begin
                         player_hor_state_nxt = PL_STOP;
                     end
                     if(((player_xpos) > PLAYER_XPOS_MIN)&&(blocking[1] ==
0)) begin
                         player xpos nxt = player xpos - 1;
                         player dir nxt = DIR LEFT;
                     end
                     else begin
                         player_xpos_nxt = player_xpos;
                         player_dir_nxt = DIR_LEFT;
                     end
                end
                default: begin
                    player hor state nxt = PL STOP;
                    player xpos nxt = player xpos;
                    player dir nxt = player dir;
                end
            endcase
        end
    end
    //Player vertical movement
    always @* begin
        if(restartgame) begin
                                = 100;
            player ypos nxt
            player ver state_nxt= 0;
            jump height nxt
                               = 0;
            player_ypos_restarted_nxt = 1;
        end
        else begin
        player ypos restarted nxt = 0;
            case(player ver state)
                PL STOP: begin
                    if(L ALT || R ALT) begin
                         if(blocking[2])
```

```
player_ver_state_nxt = PL_STOP;
                     else
                         player_ver_state_nxt = PL JUMP;
                end
                else if(blocking[3] == 0)
                    player_ver_state_nxt = PL FALL;
                else
                     player_ver_state_nxt = PL_STOP;
                player_ypos_nxt = player_ypos;
                 jump_height_nxt = 0;
            end
            PL JUMP: begin
                if((jump height < 200) && (blocking[2] == 0)) begin</pre>
                     player ver state nxt = PL JUMP;
                     jump height nxt = jump height + 1;
                     if(player ypos == PLAYER YPOS MAX)
                         player ypos nxt = player ypos;
                     else
                         player ypos nxt = player ypos + 1;
                end
                else begin
                     player_ver_state_nxt = PL FALL;
                     jump_height_nxt = jump_height;
                     player ypos nxt = player ypos;
                end
            end
            PL FALL: begin
                if((blocking[3] == 0)) begin
                     player_ver_state_nxt = PL FALL;
                     jump_height_nxt = jump_height - 1;
                     player ypos nxt = player ypos - 1;
                end
                else begin
                     player_ver_state_nxt = PL_STOP;
                     jump_height_nxt = jump_height;
                     player_ypos_nxt = player_ypos;
                end
            end
            default: begin
                player ver state nxt = PL STOP;
                jump height nxt = jump height;
                player ypos nxt = player ypos;
            end
        endcase
    end
end
always @ (posedge game clk or posedge rst) begin
    if(rst) begin
                         <= 100;
        player ypos
        player ver state<= 0;</pre>
        jump height
                        <= 0;
        player_ypos_restarted <= 0;</pre>
    end
    else begin
        player ypos
                         <= player ypos nxt;</pre>
        player ver state<= player ver state nxt;</pre>
                        <= jump height nxt;
        jump height
        player ypos restarted <= player ypos restarted nxt;</pre>
    end
```

```
end
```

```
reg [1:0] clk hor divider;
    always @(posedge game clk or posedge rst) begin
       if(rst) begin
           player_xpos_restarted <= 0;</pre>
           player_xpos <= 120;</pre>
           player_hor_state<= 0;</pre>
           player_dir
                      <= 0;
           clk_hor_divider <= 2'b00;</pre>
       end
       else begin
           if((clk hor divider == 2'b01)&&(player speed == PL RUN)) begin
               player_xpos <= player_xpos_nxt;</pre>
               player hor state<= player hor state nxt;</pre>
               player dir <= player dir nxt;</pre>
               clk hor divider <= 2'b10;</pre>
               player xpos restarted <= 0;
           end
           else if(clk hor divider == 2'b11) begin
               player xpos <= player xpos nxt;</pre>
               player hor state<= player hor state nxt;</pre>
               player_dir <= player_dir_nxt;</pre>
               clk hor divider <= 2'b00;
               player xpos restarted <= player xpos restarted nxt;</pre>
           end
           else
               clk hor divider <= clk hor divider + 1;</pre>
       end
   end
    always @(posedge game clk or posedge rst) begin
       if(rst)
           player_speed <= PL STOP;</pre>
       else
           if(clk_hor_divider == 2'b11)
               player_speed
                              <= player speed nxt;</pre>
   end
    //********
    // End of State Machine for player
    //*********
    //***********
   // State Machine for board and background movement (uses player states
because depends on player movement)
    //**********
    reg [9:0] bcgr xpos nxt;
   localparam XRES = 640;
   always @* begin
       if(restartgame) begin
           bcgr xpos nxt = 0;
           bcgr restarted nxt = 1;
       end
       else begin
       bcgr restarted nxt = 0;
           case(player_hor state)
               PL STOP:
                   bcgr xpos nxt = bcgr xpos;
               PL RIGHT:
```

```
if((player xpos == PLAYER XPOS MAX)&&((plane xpos ofs
!= MAX_OFFSET) | | (plane_xpos != BOARD_END) ))
                        bcgr xpos nxt = (bcgr xpos + 2*player speed) %
(XRES -1);
                    else
                        bcgr xpos nxt = bcgr xpos;
                PL LEFT:
                    if((player_xpos == PLAYER_XPOS_MIN)&&((plane_xpos_ofs
!= 0) | | (plane xpos != 0)))
                         if(bcgr xpos < 2*player speed)</pre>
                             bcgr_xpos_nxt = ((XRES-1) + bcgr_xpos -
2*player speed);
                         else
                             bcgr xpos nxt = bcgr xpos - 2*player speed;
                    else
                       bcgr xpos nxt = bcgr xpos;
                default:
                    bcgr xpos nxt = bcgr xpos;
            endcase
        end
    end
    reg [7:0] plane xpos nxt;
    reg [5:0] plane_xpos_ofs_nxt;
    localparam MAX OFFSET = 39;
    localparam BOARD END = 163;
    always @* begin
        if(restartgame) begin
            plane xpos ofs nxt = 0;
            plane xpos nxt = 0;
            board restarted nxt = 1;
        end
        else begin
            board_restarted_nxt = 0;
            case(player_hor_state)
                PL STOP: begin
                    plane_xpos_ofs_nxt = plane_xpos_ofs;
                    plane xpos nxt = plane xpos;
                end
                PL RIGHT:
                    if(player xpos == PLAYER XPOS MAX) begin
                         if(blocking[0] == 0) begin
                             if(plane xpos ofs == MAX OFFSET) begin
                                 if(plane xpos == BOARD END) begin
                                     plane xpos ofs nxt = MAX OFFSET;
                                     plane xpos nxt = BOARD END;
                                 end
                                 else begin
                                     plane xpos ofs nxt = 0;
                                     plane xpos nxt = plane xpos + 1;
                                 end
                             end
                             else begin
                                 plane_xpos_ofs_nxt = plane_xpos_ofs + 1;
                                 plane xpos nxt = plane xpos;
                             end
                        end
                         else begin
                             plane xpos ofs nxt = plane xpos ofs;
                             plane xpos nxt = plane xpos;
```

```
end
                end
                else begin
                     plane xpos ofs nxt = plane xpos ofs;
                     plane xpos nxt = plane xpos;
                 end
            PL LEFT: begin
                if(player_xpos == PLAYER_XPOS_MIN) begin
                     if(blocking[1] == 0) begin
                         if(plane_xpos_ofs == 0) begin
                             if(plane_xpos == 0) begin
                                 plane xpos ofs nxt = 0;
                                 plane xpos nxt = 0;
                             end
                             else begin
                                 plane xpos ofs nxt = MAX OFFSET;
                                 plane xpos nxt = plane xpos - 1;
                         end
                         else begin
                             plane xpos ofs nxt = plane xpos ofs - 1;
                             plane xpos nxt = plane xpos;
                     end
                     else begin
                         plane_xpos_ofs_nxt = plane_xpos_ofs;
                         plane_xpos_nxt = plane_xpos;
                     end
                 end
                 else begin
                     plane_xpos_ofs_nxt = plane_xpos_ofs;
                     plane xpos nxt = plane xpos;
                 end
            end
            default: begin
                plane_xpos_ofs_nxt = plane_xpos_ofs;
                plane_xpos_nxt = plane_xpos;
            end
        endcase
    end
end
always @(posedge game clk or posedge rst) begin
    if(rst) begin
        plane xpos ofs <= 0;
        plane xpos
                        <= 0;
        board restarted <= 0;
    end
    else begin
        if((clk hor divider == 2'b01) && (player speed == PL RUN)) begin
            plane xpos ofs <= plane xpos ofs nxt;</pre>
            plane xpos
                           <= plane xpos nxt;</pre>
            board restarted <= 0;</pre>
        end
        else if (clk hor divider == 2'b11) begin
            plane_xpos_ofs <= plane_xpos_ofs_nxt;</pre>
            plane xpos
                        <= plane xpos nxt;</pre>
            board restarted <= board restarted nxt;</pre>
        end
    end
end
```

```
always @(posedge game_clk or posedge rst) begin
       if(rst) begin
                         <= 0;
           bcgr xpos
           bcgr restarted <= 0;</pre>
       end
       else begin
           if(clk_hor_divider == 2'b11) begin
               bcgr xpos <= bcgr xpos nxt;</pre>
               bcgr_restarted <= bcgr_restarted_nxt;</pre>
           end
       end
   end
//**********
// End of State Machine for board and background
//***********
//**************
// Logic for blocking the player and checking special blocks
//********************
reg [7:0] block xpos nxt;
reg [3:0] block ypos nxt;
reg [3:0] blocking;//D:U:L:R
reg [3:0] blocking nxt;
reg [47:0] modi block; //DL:DR:UL:UR:LD:LU:RD:RU
reg [47:0] modi_block_nxt;
reg [3:0] blocking_state;
reg [3:0] blocking_state_nxt;
reg [1:0] special;
reg [1:0] special nxt;
reg [5:0] write_block nxt;
reg block we nxt;
reg [1:0] writing_phase;
reg [1:0] writing phase nxt;
reg position_changed;
reg position changed nxt;
localparam SPECIAL = 2'b01;
localparam BLOCKING = 2'b00;
localparam MOD_BLOCK = 2'b11;
localparam PREPARE = 4'b0000;
localparam START = 4'b0001;
localparam DOWN L = 4'b0010;
localparam DOWN R = 4'b0011;
localparam UP_L = 4'b0100;
localparam UP_R = 4'b0101;
localparam LEFT D = 4'b0110;
localparam LEFT U = 4'b0111;
localparam RIGHT D = 4'b1000;
localparam RIGHT U = 4'b1001;
//position changed
reg [7:0] old plane xpos;
reg [5:0] old plane xpos ofs;
reg [9:0] old player_xpos;
reg [8:0] old player_ypos;
reg [7:0] old_plane_xpos_nxt;
reg [5:0] old plane xpos ofs nxt;
reg [9:0] old player xpos nxt;
reg [8:0] old player ypos nxt;
reg position_changed1;
reg position changed1 nxt;
```

```
always @* begin
        old plane xpos nxt = plane xpos;
        old plane xpos ofs nxt = plane xpos ofs;
        old player xpos nxt = player xpos;
        old player ypos nxt = player ypos;
        if((old_plane_xpos != plane_xpos) || (old_plane_xpos_ofs !=
plane_xpos_ofs) || (old_player_xpos != player_xpos) || (old_player_ypos !=
player_ypos))
            position_changed1 nxt = 1;
         else
            position changed1 nxt = 0;
    end
    always @(posedge clk or posedge rst) begin
        if(rst) begin
            old plane xpos <= 0;
             old plane xpos ofs <= 0;
             old player xpos <= 0;
             old player ypos <= 0;
            position changed1 <= 0;
        end
        else begin
             position changed1 <= position changed1 nxt;
             old plane xpos <= old plane xpos nxt;
             old plane xpos ofs <= old plane xpos ofs nxt;
             old_player_xpos <= old_player_xpos_nxt;</pre>
            old player ypos <= old player ypos nxt;
        end
    end
    //rest of blocking
    always @(posedge clk or posedge rst) begin
        if(rst) begin
            special <= 0;</pre>
            blocking_state <= PREPARE;</pre>
            block_ypos <= 0;</pre>
            block xpos <= 0;
            blocking <= 4'b1000;
            modi block <= 0;
            write block <= 0;
            writing phase <= 2'b00;
            old block <=0 ;
            position changed <= 0;
        end
        else begin
            modi block <= modi block nxt;</pre>
             special <= special nxt;</pre>
            blocking state <= blocking state nxt;</pre>
            block xpos <= block xpos nxt;
            block ypos <= block ypos nxt;</pre>
            blocking <= blocking nxt;
            write block <= write block nxt;</pre>
            writing phase <= writing phase nxt;</pre>
            old block <= old block nxt;
            position changed <= position changed nxt;</pre>
        end
    end
    //And here we are, the code below does the job, it really does
    always @* begin
```

```
if (position changed) begin
        case(special)
            BLOCKING: begin
                left right block = 0;
                position changed nxt = position changed;
                up direction = 0;
                writing_phase_nxt = 2'b00;
                write_block nxt = 0;
                block we = 0;
                old block nxt = 0;
                modi block nxt = modi block;
                case(blocking state)
                    PREPARE: begin
                        blocking state nxt = START;
                        block xpos nxt = plane xpos + (player xpos +
plane xpos ofs)/40;
                        block ypos nxt = player ypos/40;
                        blocking nxt = blocking;
                        special nxt = BLOCKING;
                    end
                    START: begin
                        blocking state nxt = DOWN L;
                        block xpos nxt = block xpos;
                        block_ypos_nxt = block ypos - 1;
                        blocking nxt = blocking;
                        special nxt = BLOCKING;
                    end
                    DOWN L: begin
                        special nxt = BLOCKING;
                        if((player xpos + plane xpos ofs)%40 == 0) begin
                            blocking state nxt = UP L;
                            block xpos nxt = block xpos;
                            block ypos nxt = block ypos + 2;
                        end
                        else begin
                            block_xpos_nxt = block_xpos + 1;
                            block_ypos_nxt = block_ypos;
                            blocking_state_nxt = DOWN_R;
                        end
                        if(player ypos % 40 == 0)
                            blocking nxt[3] = blocking in;
                        else
                            blocking nxt[3] = 0;
                        blocking nxt[2] = blocking[2];
                        blocking nxt[1] = blocking[1];
                        blocking nxt[0] = blocking[0];
                    end
                    DOWN R: begin
                        special nxt = BLOCKING;
                        blocking state nxt = UP L;
                        block xpos nxt = block xpos - 1;
                        block_ypos_nxt = block_ypos + 2;
                        if(player ypos % 40 == 0)
                            blocking nxt[3] = (blocking in || blocking[3]);
                        else
                            blocking nxt[3] = 0;
                        blocking nxt[2] = blocking[2];
                        blocking nxt[1] = blocking[1];
                        blocking nxt[0] = blocking[0];
```

```
end
UP L: begin
    special nxt = BLOCKING;
    if((player xpos + plane xpos ofs)%40 == 0) begin
        blocking state nxt = LEFT D;
        block xpos nxt = block xpos - 1;
        block_ypos_nxt = block_ypos - 1;
    end
    else begin
        block_xpos_nxt = block_xpos + 1;
        blocking_state_nxt = UP R;
        block ypos nxt = block ypos;
    end
    blocking nxt[3] = blocking[3];
    if(player ypos % 40 == 0)
        blocking nxt[2] = blocking in;
    else
        blocking nxt[2] = 0;
    blocking nxt[1] = blocking[1];
    blocking nxt[0] = blocking[0];
end
UP R: begin
    special nxt = BLOCKING;
    blocking state nxt = LEFT D;
    block_xpos_nxt = block_xpos - 2;
    block_ypos_nxt = block_ypos - 1;
    blocking nxt[3] = blocking[3];
    if(player ypos % 40 == 0)
        blocking nxt[2] = (blocking in || blocking[2]);
    else
       blocking nxt[2] = 0;
    blocking_nxt[1] = blocking[1];
    blocking_nxt[0] = blocking[0];
end
LEFT_D: begin
    special nxt = BLOCKING;
    if(player ypos % 40 == 0) begin
        blocking state nxt = RIGHT D;
        block xpos nxt = block xpos + 2;
        block ypos nxt = block ypos;
    end
    else begin
        blocking state nxt = LEFT U;
        block xpos nxt = block xpos;
        block ypos nxt = block ypos + 1;
    blocking nxt[3] = blocking[3];
    blocking nxt[2] = blocking[2];
    if((player_xpos + plane_xpos_ofs) % 40 == 0)
        blocking_nxt[1] = blocking_in;
        blocking nxt[1] = 0;
    blocking nxt[0] = blocking[0];
end
LEFT U: begin
    special nxt = BLOCKING;
    blocking state nxt = RIGHT D;
```

```
block xpos nxt = block xpos + 2;
            block_ypos_nxt = block_ypos - 1;
            blocking_nxt[3] = blocking[3];
            blocking nxt[2] = blocking[2];
            if((player xpos + plane xpos ofs) % 40 == 0)
                blocking nxt[1] = (blocking in || blocking[1]);
            else
                blocking nxt[1] = 0;
            blocking_nxt[0] = blocking[0];
        end
        RIGHT D: begin
            if(player ypos % 40 == 0) begin
                blocking state nxt = PREPARE;
                special nxt = SPECIAL;
                block xpos nxt = 0;
                block ypos nxt = 0;
            else begin
                special nxt = BLOCKING;
                blocking state nxt = RIGHT U;
                block_xpos_nxt = block_xpos;
                block_ypos_nxt = block ypos + 1;
            end
            blocking nxt[3] = blocking[3];
            blocking_nxt[2] = blocking[2];
            blocking_nxt[1] = blocking[1];
            if((player_xpos + plane_xpos_ofs) % 40 == 0)
                blocking_nxt[0] = blocking_in;
            else
                blocking nxt[0] = 0;
        end
        RIGHT U: begin
            special nxt = SPECIAL;
            blocking state nxt = PREPARE;
            block_xpos_nxt = 0;
            block_ypos_nxt = 0;
            blocking_nxt[3] = blocking[3];
            blocking_nxt[2] = blocking[2];
            blocking nxt[1] = blocking[1];
            if((player xpos + plane xpos ofs) % 40 == 0)
                blocking nxt[0] = (blocking in || blocking[0]);
            else
                blocking nxt[0] = 0;
        end
        default: begin
            blocking state nxt = PREPARE;
            block xpos nxt = 0;
            block ypos nxt = 0;
            blocking nxt = blocking;
            special nxt = BLOCKING;
    endcase
end
SPECIAL: begin
    left right block = 0;
    position changed nxt = position changed;
   up_direction = 0;
   writing_phase_nxt = 2'b00;
   block we = 0;
   old block nxt = 0;
    write block nxt = 0;
```

```
case(blocking state)
                    PREPARE: begin
                        blocking state nxt = START;
                        block xpos nxt = plane xpos + (player xpos +
plane xpos ofs)/40;
                        block ypos nxt = player ypos/40;
                        blocking nxt = blocking;
                         special nxt = SPECIAL;
                         modi_block_nxt = 0;
                    end
                    START: begin
                        blocking state nxt = DOWN L;
                         block xpos nxt = block xpos;
                        block ypos nxt = block ypos - 1;
                        blocking nxt = blocking;
                         special nxt = SPECIAL;
                        modi block nxt = 0;
                    end
                    DOWN L: begin
                         special nxt = SPECIAL;
                         blocking nxt = blocking;
                         if((player_xpos + plane_xpos_ofs)%40 == 0) begin
                             blocking state nxt = UP L;
                             block_xpos_nxt = block_xpos;
                             block_ypos_nxt = block_ypos + 2;
                         end
                         else begin
                             block_xpos_nxt = block_xpos + 1;
                             blocking state nxt = DOWN R;
                             block ypos nxt = block ypos;
                         end
                         if(player ypos % 40 == 0)
                            modi block nxt[47:42] = block in;
                         else
                             modi block nxt[47:42] = 0;
                        modi block nxt[41:0] = 0;
                    end
                    DOWN R: begin
                         special nxt = SPECIAL;
                        blocking nxt = blocking;
                        blocking state nxt = UP L;
                        block xpos nxt = block xpos - 1;
                        block ypos nxt = block ypos + 2;
                         if(player ypos % 40 == 0)
                             modi block nxt[41:36] = block in;
                         else
                             modi block nxt[41:36] = 0;
                         modi block nxt[47:42] = modi block[47:42];
                        modi block nxt[35:0] = 0;
                    end
                    UP L: begin
                         special nxt = SPECIAL;
                         blocking nxt = blocking;
                         if((player_xpos + plane_xpos_ofs)%40 == 0) begin
                             blocking state nxt = LEFT D;
                             block xpos nxt = block xpos - 1;
                             block ypos nxt = block ypos - 1;
                         end
                         else begin
```

```
block xpos nxt = block xpos + 1;
        blocking_state_nxt = UP_R;
        block ypos nxt = block ypos;
    end
    if(player ypos % 40 == 0)
       modi_block_nxt[35:30] = block_in;
        modi_block_nxt[35:30] = 0;
    modi_block_nxt[47:36] = modi_block[47:36];
    modi block nxt[29:0] = 0;
end
UP R: begin
   special nxt = SPECIAL;
   blocking nxt = blocking;
   blocking state nxt = LEFT D;
   block_xpos_nxt = block xpos - 2;
   block ypos nxt = block ypos - 1;
    if(player ypos % 40 == 0)
       modi block nxt[29:24] = block in;
    else
       modi block nxt[29:24] = 0;
   modi_block_nxt[47:30] = modi_block[47:30];
   modi block nxt[23:0] = 0;
end
LEFT D: begin
   special_nxt = SPECIAL;
   blocking nxt = blocking;
    if(player ypos % 40 == 0) begin
        blocking state nxt = RIGHT D;
        block_xpos_nxt = block_xpos + 2;
        block_ypos_nxt = block_ypos;
    end
    else begin
       blocking_state_nxt = LEFT_U;
        block_xpos_nxt = block_xpos;
        block ypos nxt = block ypos + 1;
    end
    if((player xpos + plane xpos ofs) % 40 == 0)
        modi block nxt[23:18] = block in;
    else
        modi block nxt[23:18] = 0;
    modi block nxt[47:24] = modi block[47:24];
   modi block nxt[17:0] = 0;
end
LEFT U: begin
    special nxt = SPECIAL;
   blocking nxt = blocking;
   blocking state nxt = RIGHT D;
   block xpos nxt = block xpos + 2;
   block_ypos_nxt = block_ypos - 1;
    if((player_xpos + plane_xpos_ofs) % 40 == 0)
        modi_block_nxt[17:12] = block in;
    else
        modi block nxt[17:12] = 0;
   modi block nxt[47:18] = modi block[47:18];
   modi block nxt[11:0] = 0;
```

```
RIGHT D: begin
                        blocking nxt = blocking;
                        if(player ypos % 40 == 0) begin
                            blocking_state_nxt = PREPARE;
                            special nxt = MOD BLOCK;
                            block xpos nxt = 0;
                            block_ypos_nxt = 0;
                        end
                        else begin
                            special_nxt = SPECIAL;
                            blocking state nxt = RIGHT U;
                            block xpos nxt = block xpos;
                            block ypos nxt = block ypos + 1;
                        end
                        if((player xpos + plane xpos ofs) % 40 == 0)
                            modi block nxt[11:6] = block in;
                        else
                            modi block nxt[11:6] = 0;
                        modi block nxt[47:12] = modi block[47:12];
                        modi block nxt[5:0] = 0;
                    end
                    RIGHT U: begin
                        blocking nxt = blocking;
                        blocking state nxt = PREPARE;
                        special_nxt = MOD_BLOCK;
                        block_xpos_nxt = 0;
                        block ypos nxt = 0;
                        if((player_xpos + plane_xpos_ofs) % 40 == 0)
                            modi block nxt[5:0] = block in;
                        else
                            modi block nxt[5:0] = block in;
                        modi_block_nxt[47:6] = modi_block[47:6];
                    end
                    default: begin
                        special nxt = BLOCKING;
                        blocking state nxt = PREPARE;
                        block xpos nxt = 0;
                        block ypos nxt = 0;
                        blocking nxt = blocking;
                        modi block nxt = modi block;
                    end
                endcase
            end
            MOD BLOCK: begin
                case(blocking state)
                    PREPARE: begin
                        left right block = 0;
                        position changed nxt = position changed;
                        up direction = 0;
                        blocking state nxt = START;
                        block xpos nxt = plane xpos + (player xpos +
plane_xpos_ofs)/40;
                        block_ypos_nxt = player_ypos/40;
                        blocking nxt = blocking;
                        special nxt = MOD BLOCK;
                        modi block nxt = modi block;
                        writing_phase_nxt = 2'b00;
                        old block nxt = 0;
```

end

```
block we = 0;
    write block nxt = 0;
end
START: begin
   left right block = 0;
    position changed nxt = position changed;
    up direction = 0;
    blocking_state_nxt = DOWN L;
    block_xpos_nxt = block_xpos;
    block_ypos_nxt = block_ypos - 1;
    blocking_nxt = blocking;
    special nxt = MOD BLOCK;
    modi block nxt = modi block;
    write block nxt = new block;
    old block nxt = modi block[47:42];
    writing_phase_nxt = \frac{1}{2}'b00;
    block we = 0;
end
DOWN L: begin
    left right block = 0;
    position changed nxt = position changed;
    up direction = 0;
    special nxt = MOD BLOCK;
    blocking nxt = blocking;
    modi block nxt = modi block;
    write block nxt = new block;
    case(writing_phase)
        2'b00: begin
            writing phase nxt = 2'b01;
            block we = new block we;
            block xpos nxt = block xpos;
            blocking state nxt = blocking state;
            block_ypos_nxt = block_ypos;
            old block nxt = modi block[47:42];
        end
        2'b01: begin
            writing_phase nxt = 2'b11;
            block_we = new_block_we;
            block xpos nxt = block xpos;
            blocking state nxt = blocking state;
            block ypos nxt = block ypos;
            old block nxt = modi block[47:42];
        end
        2'b11: begin
            writing phase nxt = 2'b00;
            block we = 0;
            block xpos nxt = block xpos + 1;
            blocking state nxt = DOWN R;
            block ypos nxt = block ypos;
            old block nxt = modi block[41:36];
        default: begin
            writing phase nxt = 2'b00;
            block we = 0;
            block xpos nxt = 0;
            blocking_state_nxt = PREPARE;
            block ypos nxt = 0;
            old block nxt = 0;
        end
    endcase
end
```

```
DOWN R: begin
    left right block = 1;
    position changed nxt = position changed;
    up direction = 0;
    special nxt = MOD BLOCK;
   blocking nxt = blocking;
   modi_block_nxt = modi_block;
    write_block_nxt = new_block;
    case(writing_phase)
        2'b00: begin
            writing_phase_nxt = 2'b01;
            block we = new block we;
            block xpos nxt = block xpos;
            blocking state nxt = blocking state;
            block ypos nxt = block ypos;
            old block nxt = modi block[41:36];
        end
        2'b01: begin
            writing_phase nxt = 2'b11;
            block_we = new_block_we;
            block xpos nxt = block xpos;
            blocking_state_nxt = blocking state;
            block_ypos_nxt = block ypos;
            old block nxt = modi block[41:36];
        end
        2'b11: begin
            writing_phase_nxt = 2'b00;
            block_we = 0;
            block xpos nxt = block xpos - 1;
            blocking state nxt = UP L;
            block ypos nxt = block ypos + 2;
            old block nxt = modi block[35:30];
        end
        default: begin
            writing_phase_nxt = 2'b00;
            block we = 0;
            block_xpos_nxt = 0;
            blocking_state_nxt = PREPARE;
            block ypos nxt = 0;
            old block nxt = 0;
        end
    endcase
end
UP L: begin
   left right block = 0;
   position changed nxt = position changed;
   up direction = 1;
   special nxt = MOD BLOCK;
   blocking nxt = blocking;
   modi block nxt = modi block;
   write block nxt = new block;
    case(writing phase)
        2'b00: begin
            writing phase nxt = 2'b01;
            block_we = new_block_we;
            block_xpos_nxt = block_xpos;
            blocking state nxt = blocking state;
            block ypos nxt = block ypos;
            old block nxt = modi block[35:30];
        end
        2'b01: begin
```

```
writing phase nxt = 2'b11;
            block we = new block we;
            block xpos nxt = block xpos;
            blocking state nxt = blocking state;
            block ypos nxt = block ypos;
            old block nxt = modi block[35:30];
        end
        2'b11: begin
            writing_phase_nxt = 2'b00;
            block_we = 0;
            block_xpos_nxt = block_xpos + 1;
            blocking state nxt = UP R;
            block ypos nxt = block ypos;
            old block nxt = modi block[29:24];
        end
        default: begin
            writing phase nxt = 2'b00;
            block we = 0;
            block xpos nxt = 0;
            blocking state nxt = PREPARE;
            block ypos nxt = 0;
            old block nxt = 0;
    endcase
end
UP R: begin
   left_right_block = 1;
   position changed nxt = position changed;
   up direction = 1;
    special nxt = MOD BLOCK;
   blocking nxt = blocking;
   modi block nxt = modi block;
   write block nxt = new block;
    case(writing_phase)
        2'b00: begin
            writing_phase_nxt = 2'b01;
            block_we = new_block we;
            block_xpos_nxt = block_xpos;
            blocking state nxt = blocking state;
            block ypos nxt = block ypos;
            old block nxt = modi block[29:24];
        end
        2'b01: begin
            writing phase nxt = 2'b11;
            block we = new block we;
            block xpos nxt = block xpos;
            blocking state nxt = blocking state;
            block ypos nxt = block ypos;
            old block nxt = modi block[29:24];
        end
        2'b11: begin
            writing phase nxt = 2'b00;
            block we = 0;
            block xpos nxt = block xpos - 2;
            blocking state nxt = LEFT D;
            block_ypos_nxt = block_ypos - 1;
            old block nxt = modi block[23:18];
        end
        default: begin
            writing phase nxt = 2'b00;
            block we = 0;
```

```
block xpos nxt = 0;
            blocking_state_nxt = PREPARE;
            block ypos nxt = 0;
            old block nxt = 0;
        end
    endcase
end
LEFT D: begin
    left_right_block = 0;
    position_changed_nxt = position changed;
    up direction = 0;
    special_nxt = MOD BLOCK;
    blocking nxt = blocking;
    modi block nxt = modi block;
    write block nxt = new block;
    case(writing phase)
        2'b00: begin
            writing phase nxt = 2'b01;
            block_we = new_block_we;
            block xpos nxt = block xpos;
            blocking_state_nxt = blocking_state;
            block ypos nxt = block ypos;
            old block nxt = modi block[23:18];
        end
        2'b01: begin
            writing_phase_nxt = 2'b11;
            block_we = new_block_we;
            block_xpos_nxt = block_xpos;
            blocking state nxt = blocking state;
            block ypos nxt = block ypos;
            old block nxt = modi block[23:18];
        end
        2'b11: begin
            writing_phase_nxt = 2'b00;
            block we = 0;
            block_xpos_nxt = block_xpos;
            blocking_state_nxt = LEFT_U;
            block_ypos_nxt = block_ypos + 1;
            old block nxt = modi block[17:12];
        end
        default: begin
            writing phase nxt = 2'b00;
            block we = 0;
            block xpos nxt = 0;
            blocking state nxt = PREPARE;
            block ypos nxt = 0;
            old block nxt = 0;
        end
    endcase
end
LEFT U: begin
    left right block = 0;
    position changed nxt = position changed;
    up direction = 0;
    special nxt = MOD BLOCK;
    blocking_nxt = blocking;
    modi block nxt = modi block;
    write block nxt = new block;
    case(writing phase)
        2'b00: begin
            writing phase nxt = 2'b01;
```

```
block we = new_block_we;
            block_xpos_nxt = block_xpos;
            blocking state nxt = blocking state;
            block ypos nxt = block ypos;
            old block nxt = modi block[17:12];
        end
        2'b01: begin
            writing_phase_nxt = 2'b11;
            block_we = new_block_we;
            block_xpos_nxt = block_xpos;
            blocking_state_nxt = blocking_state;
            block ypos nxt = block ypos;
            old block nxt = modi block[17:12];
        end
        2'b11: begin
            writing phase nxt = 2'b00;
            block we = 0;
            block xpos nxt = block xpos + 2;
            blocking state nxt = RIGHT D;
            block ypos nxt = block ypos - 1;
            old block nxt = modi block[11:6];
        default: begin
            writing phase nxt = 2'b00;
            block we = 0;
            block_xpos_nxt = 0;
            blocking_state_nxt = PREPARE;
            block_ypos_nxt = 0;
            old block nxt = 0;
        end
    endcase
end
RIGHT D: begin
   left right block = 1;
    position changed nxt = position changed;
   up direction = 0;
    special_nxt = MOD_BLOCK;
   blocking_nxt = blocking;
   modi block nxt = modi block;
   write block nxt = new block;
    case(writing phase)
        2'b00: begin
            writing phase nxt = 2'b01;
            block we = new block we;
            block xpos nxt = block xpos;
            blocking state nxt = blocking state;
            block ypos nxt = block ypos;
            old block nxt = modi block[11:6];
        2'b01: begin
            writing phase nxt = 2'b11;
            block we = new block we;
            block xpos nxt = block xpos;
            blocking state nxt = blocking state;
            block ypos_nxt = block_ypos;
            old block nxt = modi block[11:6];
        2'b11: begin
            writing phase nxt = 2'b00;
            block we = 0;
            block xpos nxt = block xpos;
```

```
blocking_state_nxt = RIGHT U;
            block_ypos_nxt = block_ypos + 1;
            old block nxt = modi block[5:0];
        end
        default: begin
            writing_phase_nxt = 2'b00;
            block_we = 0;
            block_xpos_nxt = 0;
            blocking_state_nxt = PREPARE;
            block_ypos_nxt = 0;
            old_block_nxt = 0;
        end
    endcase
end
RIGHT U: begin
    left right block = 1;
    position changed nxt = 0;
    up direction = 0;
    blocking nxt = blocking;
    modi block nxt = modi block;
    write block nxt = new block;
    case(writing_phase)
        2'b00: begin
            special nxt = MOD BLOCK;
            writing_phase_nxt = 2'b01;
            block_we = new_block_we;
            block_xpos_nxt = block_xpos;
            blocking_state_nxt = blocking_state;
            block ypos nxt = block ypos;
            old block nxt = modi block[5:0];
        end
        2'b01: begin
            special nxt = MOD BLOCK;
            writing_phase_nxt = 2'b11;
            block_we = new_block_we;
            block_xpos_nxt = block_xpos;
            blocking_state_nxt = blocking state;
            block_ypos_nxt = block_ypos;
            old block nxt = modi block[5:0];
        end
        2'b11: begin
            special nxt = BLOCKING;
            writing phase nxt = 2'b00;
            block we = 0;
            block xpos nxt = block xpos;
            blocking state nxt = PREPARE;
            block ypos nxt = block ypos;
            old block nxt = old block;
        default: begin
            special nxt = BLOCKING;
            writing phase nxt = 2'b00;
            block we = 0;
            block xpos nxt = 0;
            blocking state nxt = PREPARE;
            block_ypos_nxt = 0;
            old block nxt = 0;
        end
    endcase
end
default: begin
```

```
left right block = 0;
                    position changed nxt = 0;
                    up direction = 0;
                    write block nxt = 0;
                    special nxt = BLOCKING;
                    blocking_state_nxt = PREPARE;
                    block_xpos_nxt = 0;
                    block_ypos_nxt = 0;
                    blocking_nxt = blocking;
                    modi_block_nxt = modi_block;
                    writing_phase_nxt = 2'b00;
                    old block nxt = 0;
                    block we = 0;
                end
            endcase
        end
        default: begin
            left right block = 0;
            position changed nxt = 0;
            up direction = 0;
            write block nxt = 0;
            block we = 0;
            modi block nxt = modi block;
            special nxt = BLOCKING;
            blocking_state_nxt = PREPARE;
            block_xpos_nxt = 0;
            block_ypos_nxt = 0;
            blocking_nxt = blocking;
            writing_phase_nxt = 2'b00;
            old block nxt = 0;
        end
    endcase
    end
    else begin
        if(position_changed1)
           position_changed_nxt = 1;
        else
           position changed nxt = 0;
        left right block = 0;
        up direction = 0;
        write block nxt = 0;
        block we = 0;
        modi block nxt = modi block;
        special nxt = BLOCKING;
        blocking state nxt = PREPARE;
        block xpos nxt = 0;
        block ypos nxt = 0;
        blocking nxt = blocking;
        writing phase nxt = 2'b00;
        old block nxt = 0;
    end
end
//*********
// End of logic for blocking the player
//Breaking the locks and getting points
wire [5:0] new block;
reg [5:0] old block, old block nxt;
```

```
reg up direction, left right block;
   new block my new block (
       .block in (old block),
       .up direction(up direction),
       .direction(left right block),
       .block out (new block),
       .relative_xpos(player_xpos + plane_xpos_ofs),
       .relative_ypos(player_ypos),
       .write enable (new block we),
       .new point(new_point)
   );
endmodule
timescale 1 ns / 1 ps
///////
// Company:
                 AGH UST
// Engineer: Wojciech Gredel, Hubert Górowski
// Create Date:
// Design Name:
// Module Name:
                 Gameover
// Project Name:
                 DOS Mario
// Target Devices: Basys3
// Tool versions: Vivado 2016.1
// Description:
//
    This module displays GAME OVER text
//
// Dependencies:
//
// Revision:
// Revision 0.01 - Module created
// Additional Comments:
///////
module Gameover
   input wire gameover,
   input wire [9:0] hcount in,
   input wire hsync in,
   input wire [9:0] vcount in,
   input wire vsync in,
   input wire [23:0] rgb in,
   input wire blnk in,
   input wire clk,
   input wire rst,
   output reg [9:0] hcount out,
   output reg hsync out,
   output reg [9:0] vcount_out,
   output reg vsync out,
   output reg [23:0] rgb out,
   output reg blnk out
   );
   localparam LETTER XS = 11;
   localparam LETTER YS = 10;
   //first letter
```

```
localparam X1 CENTER = 170;
localparam Y1 CENTER = 180;
localparam SIZE1 = 9;
localparam X1 OFFSET = X1 CENTER + LETTER XS*SIZE1/2;
localparam Y1 OFFSET = Y1 CENTER + LETTER YS*SIZE1/2;
//second letter
localparam X2 CENTER = 270;
localparam Y2 CENTER = 180;
localparam SIZE2 = 9;
localparam X2 OFFSET = X2 CENTER + LETTER XS*SIZE2/2;
localparam Y2_OFFSET = Y2_CENTER + LETTER_YS*SIZE2/2;
//third letter
localparam X3 CENTER = 370;
localparam Y3 CENTER = 180;
localparam SIZE3 = 9;
localparam X3 OFFSET = X3 CENTER + LETTER XS*SIZE3/2;
localparam Y3 OFFSET = Y3 CENTER + LETTER YS*SIZE3/2;
//fourth letter
localparam X4 CENTER = 470;
localparam Y4 CENTER = 180;
localparam SIZE4 = 9;
localparam X4 OFFSET = X4 CENTER + LETTER XS*SIZE4/2;
localparam Y4 OFFSET = Y4 CENTER + LETTER YS*SIZE4/2;
//fifth letter
localparam X5_CENTER = 170;
localparam Y5 CENTER = 300;
localparam SIZE5 = 9;
localparam X5_OFFSET = X5_CENTER + LETTER_XS*SIZE5/2;
localparam Y5 OFFSET = Y5 CENTER + LETTER YS*SIZE5/2;
//sixth letter
localparam X6 CENTER = 270;
localparam Y6 CENTER = 300;
localparam SI\overline{Z}E6 = 9;
localparam X6 OFFSET = X6 CENTER + LETTER XS*SIZE6/2;
localparam Y6 OFFSET = Y6 CENTER + LETTER YS*SIZE6/2;
//seventh letter
localparam X7 CENTER = 370;
localparam Y7 CENTER = 300;
localparam SIZE7 = 9;
localparam X7_OFFSET = X7 CENTER + LETTER XS*SIZE7/2;
localparam Y7 OFFSET = Y7 CENTER + LETTER YS*SIZE7/2;
//eighth letter
localparam X8 CENTER = 470;
localparam Y8 CENTER = 300;
localparam SIZE8 = 9;
localparam X8 OFFSET = X8 CENTER + LETTER XS*SIZE8/2;
localparam Y8 OFFSET = Y8 CENTER + LETTER YS*SIZE8/2;
//Letters
localparam reg [10:0] V LETTER [9:0] ={
    11'b10000000001,
   11'b01000000010,
   11'b01000000010,
   11'b00100000100,
   11'b00100000100,
   11'b00010001000,
   11'b00010001000,
   11'b00001010000,
   11'b00001010000,
    11'b00000100000);
```

```
localparam reg [10:0] A_LETTER [9:0] ={
    11'b001111111100,
    11'b01000000010,
    11'b01000000010,
    11'b01000000010,
    11'b01111111110,
    11'b01000000010,
    11'b01000000010,
    11'b01000000010,
    11'b01000000010,
    11'b0100000010};
localparam reg [10:0] M LETTER [9:0] ={
    11'b10000000001,
    11'b11000000011,
    11'b10100000101,
    11'b10010001001,
    11'b10001010001,
    11'b10000100001,
    11'b10000000001,
    11'b10000000001,
    11'b10000000001,
    11'b10000000001};
localparam reg [10:0] E_LETTER [9:0] ={
    11'b01111111110,
    11'b01000000000,
    11'b01000000000,
    11'b01000000000,
    11'b01111111110,
    11'b01000000000,
    11'b01000000000,
    11'b01000000000,
    11'b01000000000,
    11'b01111111110};
localparam reg [10:0] O_LETTER [9:0] ={
   11'b001111111100,
    11'b01000000010,
    11'b01000000010,
    11'b01000000010,
    11'b01000000010,
    11'b01000000010,
    11'b01000000010,
    11'b01000000010,
    11'b01000000010,
    11'b001111111100};
localparam reg [10:0] R LETTER [9:0] ={
    11'b01111111100,
    11'b01000000010,
    11'b01000000010,
    11'b01000000010,
    11'b01111111100,
    11'b01000100000,
    11'b01000010000,
    11'b01000001000,
    11'b01000000100,
    11'b01000000010};
localparam reg [10:0] G LETTER [9:0] ={
```

```
11'b000111111000,
        11'b00100000100,
        11'b01000000010,
        11'b01000000010,
        11'b01000000000,
        11'b01000011110,
        11'b01000000010,
        11'b01000000010,
        11'b00100000100,
        11'b00011111000};
    localparam YRES = 480;
    //Colors
    localparam BLACK = 24'h00 00 00;
    localparam WHITE = 24'hff ff ff;
    reg [23:0] rgb nxt = 0;
    always @(posedge clk or posedge rst) begin
        if(rst) begin
            hcount out \leftarrow #1 0;
            hsync out
                        <= #1 0;
            vcount out <= #1 0;
            vsync_out <= #1 0;</pre>
                        <= #1 0;
            rgb out
            blnk out
                        <= #1 0;
        end
        else begin
            hcount out <= #1 hcount in;
            hsync_out <= #1 hsync_in;</pre>
            vcount out <= #1 vcount in;
            vsync_out <= #1 vsync_in;</pre>
            rgb out
                       <= #1 rgb nxt;
            blnk out <= #1 blnk in;
        end
    end
    always @* begin
        if(gameover) begin
             //G
            if(((Y1 OFFSET - vcount in) <= LETTER YS*SIZE1 - 1) &&</pre>
((Y1 OFFSET - vcount in) >= 0) && ((X1 OFFSET - hcount in) <= LETTER XS *
SIZE\overline{1} - 1) && ((X1 OFFSET - hount in) >= 0)) begin
                 if(G LETTER[(Y1 OFFSET - vcount in)/SIZE1][(X1 OFFSET -
hcount in)/SIZE1] == 1)
                    begin
                         rgb nxt = WHITE;
                else rgb nxt = BLACK;
            end
            //A
            else if(((Y2 OFFSET - vcount in) <= LETTER YS*SIZE2 - 1) &&</pre>
((Y2 OFFSET - vcount in) >= 0) && ((X2 OFFSET - hcount in) <= LETTER XS *
SIZE2 - 1) && ((X2_{OFFSET} - hount in) >= 0)) begin
                if(A LETTER[(Y2 OFFSET - vcount in)/SIZE2][(X2 OFFSET -
hcount in)/SIZE2] == 1)
                     begin
                         rgb_nxt = WHITE;
                         end
                     else rgb nxt = BLACK;
            end
```

```
//M
            else if(((Y3 OFFSET - vcount in) <= LETTER YS*SIZE3 - 1) &&</pre>
((Y3 OFFSET - vcount in) >= 0) && ((X3 OFFSET - hcount in) <= LETTER XS *
SIZE3 - 1) && ((X3 OFFSET - hount in) \geq 0)) begin
                 if(M LETTER[(Y3 OFFSET - vcount in)/SIZE3][(X3 OFFSET -
hcount in)/SIZE3] == 1)
                    begin
                         rgb_nxt = WHITE;
                         end
                     else rgb_nxt = BLACK;
            end
            //E
            else if(((Y4 OFFSET - vcount in) <= LETTER YS*SIZE4 - 1) &&</pre>
((Y4 OFFSET - vcount in) >= 0) && ((X4 OFFSET - hcount in) <= LETTER XS *
SIZE\overline{4} - 1) && ((X4 OFFSET - hcount_in) >= 0)) begin
                if(E LETTER[(Y4 OFFSET - vcount in)/SIZE4][(X4 OFFSET -
hcount in)/SIZE4] == 1)
                    begin
                         rgb_nxt = WHITE;
                         end
                     else rgb nxt = BLACK;
            end
            //0
            else if(((Y5 OFFSET - vcount in) <= LETTER YS*SIZE5 - 1) &&</pre>
((Y5_OFFSET - vcount_in) >= 0) && ((X5_OFFSET - hcount_in) <= LETTER XS *
SIZE5 - 1) && ((X5 OFFSET - hount in) \geq 0)) begin
                if(O_LETTER[(Y5_OFFSET - vcount_in)/SIZE5][(X5_OFFSET -
hcount in)/SIZE5] == 1)
                   begin
                        rgb_nxt = WHITE;
                        end
                    else rgb_nxt = BLACK;
            end
            //V
            else if(((Y6 OFFSET - vcount in) <= LETTER YS*SIZE6 - 1) &&</pre>
((Y6 OFFSET - vcount_in) >= 0) && ((X6_OFFSET - hcount_in) <= LETTER_XS *
SIZE6 - 1) && ((X6_OFFSET - hcount_in) \geq= 0)) begin
                 if(V_LETTER[(Y6_OFFSET - vcount_in)/SIZE6][(X6_OFFSET -
hcount in)/SIZE6] == 1)
                    begin
                         rgb nxt = WHITE;
                         end
                     else rgb nxt = BLACK;
            end
            else if(((Y7 OFFSET - vcount in) <= LETTER YS*SIZE7 - 1) &&</pre>
((Y7 OFFSET - vcount in) >= 0) && ((X7 OFFSET - hcount in) <= LETTER XS *
SIZE7 - 1) && ((X7 OFFSET - hcount in) \geq= 0)) begin
                 if(E LETTER[(Y7 OFFSET - vcount in)/SIZE7][(X7 OFFSET -
hcount in)/SIZE7] == 1)
                    begin
                         rgb_nxt = WHITE;
                         end
                     else rgb nxt = BLACK;
            end
            //R
            else if(((Y8 OFFSET - vcount in) <= LETTER YS*SIZE8 - 1) &&</pre>
((Y8 OFFSET - vcount in) >= 0) && ((X8_OFFSET - hcount_in) <= LETTER_XS *
SIZE8 - 1) && ((X8 OFFSET - hcount_in) \geq= 0)) begin
                 if(R LETTER[(Y8 OFFSET - vcount in)/SIZE8][(X8 OFFSET -
hcount in)/SIZE8] == 1)
```

```
begin
                     rgb_nxt = WHITE;
                    end
                 else rgb_nxt = BLACK;
          end
          else begin
             rgb_nxt = BLACK;
          end
       end
       else begin
         rgb_nxt = rgb_in;
       end
   end
endmodule
timescale 1ns / 1ps
///////
// Company:
                 AGH UST
// Engineer: Montvydas Klumbys, modified by Wojciech Gredel
// Create Date:
// Design Name:
// Project Name: Keyboard
// Target D
                 DOS Mario
// Target Devices: Basys3
// Tool versions: Vivado 2016.1
// Description:
// A module which is used to receive the DATA from PS2 type keyboard and
translate that data into sensible codeword.
// Dependencies:
//
// Revision:
// Revision 0.02 - Added keys differentiation
// Additional Comments:
///////
module Keyboard(
                         //board clock
     input wire CLK,
     input wire PS2 CLK,
                          //keyboard clock and data signals
     input wire PS2 DATA,
     input wire rst,
     output reg L ALT,
     output reg R ALT,
     output reg L CTRL,
     output reg R CTRL,
     output reg SPACE,
     output reg L SHIFT,
     output reg R SHIFT,
     output reg ESC,
     output reg D ARROW,
     output reg L_ARROW,
     output reg R ARROW
     );
   localparam reg [7:0] ALT CODE
                                = 8'h11;
   localparam reg [7:0] CTRL CODE
                                 = 8'h14;
```

```
localparam reg [7:0] SPACE CODE
                                    = 8'h29;
   localparam reg [7:0] L SHIFT CODE = 8'h12;
   localparam reg [7:0] R_SHIFT CODE = 8'h59;
   localparam reg [7:0] ESC CODE
                                  = 8'h76;
     localparam reg [7:0] D_ARROW CODE = 8'h72;
     localparam reg [7:0] L ARROW CODE = 8'h6B;
     localparam reg [7:0] R ARROW CODE = 8'h74;
   localparam reg [7:0] BREAK_CODE = 8'hF0;
     localparam reg [7:0] EXTENDED CODE = 8'hE0;
     localparam reg [7:0] R_GUI
                                    = 8'h27;
     localparam reg [7:0] APPS
                                    = 8'h2F;
   reg EXTENDED nxt;
   reg BREAK nxt;
     reg L ALT nxt;
   reg R ALT nxt;
   reg L CTRL nxt;
   reg R CTRL nxt;
   reg SPACE nxt;
   reg L SHIFT nxt;
   reg R SHIFT nxt;
   reg ESC nxt;
   reg D ARROW nxt;
   reg L ARROW nxt;
   reg R ARROW nxt;
     reg read;
                                    //this is 1 if still waits to receive
more bits
   reg [11:0] count reading; //this is used to detect how much time
passed since it received the previous codeword
     reg PREVIOUS STATE; //used to check the previous state of the
keyboard clock signal to know if it changed
     //this tells about error
     reg scan err;
                           //this stores only the DATA codeword
     reg [7:0] CODEWORD;
                                    //this is triggered when full 11 bits
     reg TRIG ARR;
are received
     reg [3:0]BIT COUNTER;  //tells how many bits were received until
now (from 0 to 11)
     reg EXTENDED;
     reg BREAK;
     always @(posedge CLK or posedge rst) begin
       if(rst) begin
           count reading <= #1 0;
       end
       else begin
           if (read)
                                                             //if it
still waits to read full packet of 11 bits, then (read == 1)
               count reading <= #1 count reading + 1;  //and it counts</pre>
up this variable
                                                             //and
           else
later if check to see how big this value is.
              count_reading <= #1 0;</pre>
                                                            //if it is
too big, then it resets the received data
       end
     end
     always @(posedge CLK or posedge rst) begin
       if(rst) begin
```

```
PREVIOUS STATE <= #1 1;
            read <= #1 0;
scan_err <= #1 0;
scan_code <= #1 11'b00000000000;
BIT_COUNTER <= #1 0;
TRIG_ARR <= #1 0;
        end
        else begin
            if (PS2 CLK != PREVIOUS STATE) begin
                                                                    //if the
state of Clock pin changed from previous state
                if (!PS2_CLK) begin
                                                       //and if the keyboard
clock is at falling edge
                    read <= #1 1;
                                                       //mark down that it is
still reading for the next bit
                                                             //no errors
                     scan err <= #1 0;
                     scan code[10:0] \leftarrow #1 \{PS2 DATA, scan code[10:1]\};
      //add up the data received by shifting bits and adding one new bit
                    BIT COUNTER <= #1 BIT COUNTER + 1;
                end
            end
                                                                    //if it
            else if (BIT COUNTER == 11) begin
already received 11 bits
                BIT COUNTER <= #1 0;
                                                      //mark down that
                read <= #1 0;
reading stopped
                TRIG ARR <= #1 1;
                                                              //trigger out
that the full pack of 11bits was received
                //calculate scan err using parity bit
                if (!scan code[10] || scan code[0] ||
!(scan code[1]^scan code[2]^scan code[3]^scan code[4]
                     ^scan code[5]^scan code[6]^scan code[7]^scan code[8]
                     ^scan code[9]))
                     scan err <= #1 1;
                else
                    scan err \leftarrow= #1 0;
            end
                                                      //if it yet not
            else begin
received full pack of 11 bits
                TRIG ARR <= #1 0;
                                                              //tell that the
packet of 11bits was not received yet
                if (BIT COUNTER < 11 && count reading >= 4000) begin
      //and if after a certain time no more bits were received, then
                    BIT COUNTER <= #1 0;
                                                              //reset the
number of bits received
                                                      //and wait for the
                    read <= #1 0;
next packet
                end
            end
            PREVIOUS STATE <= #1 PS2 CLK;
                                                                    //mark
down the previous state of the keyboard clock
        end
      end
      always @(posedge CLK or posedge rst) begin
        if(rst) begin
            CODEWORD <= #1 8'd0;
        end
        else begin
            if (TRIG ARR) begin
                                              //and if a full packet
of 11 bits was received
```

```
if (scan err) begin
                                             //BUT if the packet was NOT
OK
                   CODEWORD <= #1 8'd0;
                                             //then reset the codeword
register
               end
                else begin
                   CODEWORD <= scan code[8:1]; //else drop down the
unnecessary bits and transport the 7 DATA bits to CODEWORD reg
                                        //notice, that the codeword is
               end
also reversed! This is because the first bit to received
           end
                                        //is supposed to be the last bit
in the codeword...
           else CODEWORD <= #1 8'd0;</pre>
                                                          //not a full
packet received, thus reset codeword
       end
      end
      always @ (posedge CLK or posedge rst) begin
        if(rst) begin
           EXTENDED<= #1 0;
           BREAK <= #1 0;
           L ALT
                   <= #1 0;
           R ALT
                   <= #1 0;
           R CTRL <= #1 0;
           L SHIFT <= #1 0;
           R SHIFT <= #1 0;
           SPACE <= #1 0;
           ESC
                   <= #1 0;
           D ARROW <= #1 0;
           L ARROW <= #1 0;
           R ARROW <= #1 0;
        end
        else begin
           EXTENDED<= #1 EXTENDED nxt;
           BREAK <= #1 BREAK_nxt;
           L_ALT <= #1 L_ALT_nxt;
           R_ALT <= #1 R_ALT_nxt;</pre>
           L CTRL <= #1 L CTRL nxt;
           R CTRL <= #1 R CTRL nxt;
           L SHIFT <= #1 L SHIFT nxt;
           R SHIFT <= #1 R SHIFT nxt;
           SPACE <= #1 SPACE nxt;
           ESC <= #1 ESC nxt;
           D ARROW <= #1 D ARROW nxt;
           L ARROW <= #1 L ARROW nxt;
           R ARROW <= #1 R ARROW nxt;
        end
     end
    always @* begin
        if(scan err == 0) begin
            if(CODEWORD == EXTENDED CODE) begin
               EXTENDED nxt= 1;
               BREAK nxt = 0;
               L_ALT_nxt = L ALT;
               R ALT nxt = R ALT;
               L CTRL nxt = L CTRL;
               R CTRL nxt = R CTRL;
               L SHIFT nxt = L SHIFT;
               R SHIFT nxt = R SHIFT;
```

```
SPACE_nxt = SPACE;
    ESC_nxt = ESC;
    D ARROW nxt = D ARROW;
    L ARROW nxt = L ARROW;
    R ARROW nxt = R ARROW;
end
else if(CODEWORD == BREAK CODE) begin
    EXTENDED_nxt= EXTENDED;
    BREAK_nxt = 1;
L_ALT_nxt = L_ALT;
R_ALT_nxt = R_ALT;
    L_CTRL_nxt = L_CTRL;
R_CTRL_nxt = R_CTRL;
    L SHIFT nxt = L SHIFT;
    R SHIFT nxt = R SHIFT;
    SPACE_nxt = SPACE;
ESC_nxt = ESC;
    D ARROW nxt = D ARROW;
    L ARROW nxt = L ARROW;
    R ARROW nxt = R ARROW;
end
else if(EXTENDED) begin
    if(CODEWORD == R ARROW CODE) begin
        if(BREAK) begin
           R_ARROW nxt = 0;
        end
        else begin
           R ARROW nxt = 1;
        end
        EXTENDED nxt= 0;
        BREAK_nxt = 0;

R_ALT_nxt = R_ALT;

R_CTRL_nxt = R_CTRL;
    end
    else if(CODEWORD == ALT CODE) begin
        if(BREAK) begin
           R_ALT_nxt = 0;
        end
        else begin
         R ALT nxt = 1;
        end
        BREAK nxt = 0;
        EXTENDED nxt= 0;
        R CTRL nxt = R CTRL;
        R ARROW nxt = R ARROW;
    end
    else if(CODEWORD == CTRL CODE) begin
        if(BREAK) begin
            R CTRL nxt = 0;
        end
        else begin
          R CTRL nxt = 1;
        BREAK nxt = 0;
        EXTENDED nxt= 0;
        R ALT nxt = R ALT;
        R_ARROW_nxt = R ARROW;
    end
    else begin
        EXTENDED nxt= EXTENDED;
        BREAK nxt
                      = BREAK;
```

```
R_ALT_nxt = R_ALT;
R_CTRL_nxt = R_CTRL;
        R ARROW nxt = R ARROW;
    end
    L ALT nxt
                 = L_ALT;
    L_CTRL_nxt = L CTRL;
    L_SHIFT_nxt = L_SHIFT;
    R_SHIFT_nxt = R_SHIFT;
    SPACE_nxt = SPACE;
ESC_nxt = ESC;
    D_ARROW_nxt = D_ARROW;
    L ARROW nxt = L ARROW;
end
else if(CODEWORD == D ARROW CODE) begin
    if(BREAK) begin
        D ARROW nxt = 0;
    else begin
      D ARROW nxt = 1;
    EXTENDED nxt= 0;
    BREAK_nxt = 0;
    L_ALT_nxt
                  = L ALT;
    L_ALT_nxt = L_ALT;

R_ALT_nxt = R_ALT;

L_CTRL_nxt = L_CTRL;

R_CTRL_nxt = R_CTRL;
    L_SHIFT_nxt = L_SHIFT;
    R_SHIFT_nxt = R_SHIFT;
    SPACE_nxt = SPACE;
ESC_nxt = ESC;
    L ARROW nxt = L ARROW;
    R ARROW nxt = R ARROW;
end
else if(CODEWORD == L_ARROW_CODE) begin
    if(BREAK) begin
        L ARROW nxt = 0;
    end
    else begin
        L ARROW nxt = 1;
    end
    EXTENDED nxt= 0;
    BREAK nxt = 0;
    L_ALT_nxt = L_ALT;
R_ALT_nxt = R_ALT;
    L CTRL nxt = L CTRL;
    R CTRL nxt = R CTRL;
    L SHIFT nxt = L SHIFT;
    R SHIFT nxt = R SHIFT;
    SPACE nxt = SPACE;
    ESC nxt = ESC;
    D ARROW nxt = D ARROW;
    R ARROW nxt = R ARROW;
end
else if(CODEWORD == ALT CODE) begin
    if(BREAK) begin
        L_ALT nxt = 0;
    end
    else begin
        L ALT nxt = 1;
    EXTENDED nxt= 0;
```

```
BREAK_nxt = 0;
R_ALT_nxt = R_ALT;
    L_CTRL_nxt = L CTRL;
    R CTRL nxt = R CTRL;
    L SHIFT nxt = L SHIFT;
    R SHIFT nxt = R SHIFT;
    SPACE_nxt = SPACE;
    ESC_nxt
                 = ESC;
    D_ARROW_nxt = D_ARROW;
    L ARROW_nxt = L_ARROW;
    R_ARROW_nxt = R_ARROW;
end
else if(CODEWORD == CTRL CODE) begin
    if(BREAK) begin
        L CTRL nxt = 0;
    else begin
       L CTRL nxt = 1;
    EXTENDED nxt= 0;
   BREAK_nxt = 0;

L_ALT_nxt = L_ALT;

R_ALT_nxt = R_ALT;

R_CTRL_nxt = R_CTRL;
    L_SHIFT_nxt = L_SHIFT;
    R SHIFT nxt = R SHIFT;
    SPACE_nxt = SPACE;
ESC_nxt = ESC;
    D_ARROW_nxt = D ARROW;
    L ARROW nxt = L ARROW;
    R ARROW nxt = R ARROW;
end
else if(CODEWORD == L SHIFT CODE) begin
    if(BREAK) begin
        L_SHIFT_nxt = 0;
    end
    else begin
        L SHIFT_nxt = 1;
    end
    EXTENDED nxt= 0;
    BREAK nxt = 0;
    L_ALT_nxt = L_ALT;
R_ALT_nxt = R_ALT;
    L CTRL nxt = L CTRL;
    R CTRL nxt = R CTRL;
    R SHIFT nxt = R SHIFT;
    SPACE nxt = SPACE;
    ESC nxt = ESC;
    D ARROW nxt = D ARROW;
    L ARROW nxt = L ARROW;
    R ARROW nxt = R ARROW;
end
else if(CODEWORD == R SHIFT CODE) begin
    if (BREAK) begin
        R_SHIFT nxt = 0;
    end
    else begin
       R SHIFT nxt = 1;
    EXTENDED_nxt= 0;
    BREAK nxt = 0;
```

```
L_ALT_nxt = L_ALT;
R_ALT_nxt = R_ALT;
    L_CTRL_nxt = L CTRL;
    R CTRL nxt = R CTRL;
    L SHIFT nxt = L SHIFT;
                 = SPACE;
    SPACE nxt
                 = ESC;
    ESC nxt
    D_ARROW_nxt = D_ARROW;
    L_ARROW_nxt = L_ARROW;
    R_ARROW_nxt = R_ARROW;
end
else if(CODEWORD == SPACE CODE) begin
    if(BREAK) begin
        SPACE nxt = 0;
    end
    else begin
      SPACE nxt = 1;
    EXTENDED nxt= 0;
    BREAK_nxt = 0;
    L_ALT_nxt = L_ALT;

R_ALT_nxt = R_ALT;

L_CTRL_nxt = L_CTRL;

R_CTRL_nxt = R_CTRL;
    L_ALT_nxt
                  = L ALT;
    L_SHIFT_nxt = L_SHIFT;
    R_SHIFT_nxt = R_SHIFT;
    ESC_nxt = ESC;
    D_ARROW_nxt = D_ARROW;
    L ARROW nxt = L ARROW;
    R ARROW nxt = R ARROW;
end
else if(CODEWORD == ESC CODE) begin
    if(BREAK) begin
       ESC nxt = 0;
    end
    else begin
        ESC_nxt = 1;
    end
    EXTENDED nxt= 0;
    BREAK nxt = 0;
    L_ALT_nxt = L_ALT;
R_ALT_nxt = R_ALT;
    L CTRL nxt = L CTRL;
    R CTRL nxt = R CTRL;
    L SHIFT nxt = L SHIFT;
    R SHIFT nxt = R SHIFT;
    SPACE nxt = SPACE;
    D ARROW nxt = D ARROW;
    L ARROW nxt = L ARROW;
    R ARROW nxt = R ARROW;
end
else begin
    EXTENDED nxt= EXTENDED;
    BREAK_nxt = BREAK;
    L_ALT_nxt = L_ALT;
R_ALT_nxt = R_ALT;
                = L CTRL;
    L CTRL nxt
    R CTRL nxt = R CTRL;
    L SHIFT nxt = L SHIFT;
    R SHIFT nxt = R SHIFT;
    SPACE nxt = SPACE;
```

```
ESC nxt
                             = ESC;
                D \overline{ARROW} nxt = D \overline{ARROW};
                L ARROW nxt = L ARROW;
                R ARROW nxt = R ARROW;
            end
        end
        else begin
            EXTENDED_nxt= 0;
            BREAK_nxt = 0;
                       = L_ALT;
            L_ALT_nxt
            R_ALT_nxt = R ALT;
            L CTRL nxt = L CTRL;
            R CTRL nxt = R CTRL;
            L SHIFT nxt = L SHIFT;
            R SHIFT nxt = R SHIFT;
            SPACE nxt = \overline{SPACE};
                        = ESC;
            ESC nxt
            D ARROW nxt = D ARROW;
            L ARROW nxt = L ARROW;
            R ARROW nxt = R ARROW;
        end
    end
endmodule
// File: main.v
// This is the top level design of DOS mario for fpga
// The `timescale directive specifies what the
// simulation time units are (1 ns here) and what
// the simulator time step should be (1 ps here).
`timescale 1 ns / 1 ps
// Declare the module and its ports. This is
// using Verilog-2001 syntax.
module main (
      input wire clk,
      input wire btnC,
      input wire PS2Clk,
      input wire PS2Data,
    output wire speaker,
      output wire [7:0] vga2Red,
      output wire [7:0] vga2Green,
      output wire [7:0] vga2Blue,
      output wire vga2Hsync,
      output wire vga2Vsync,
      output wire vga2Blank,
      output wire vga2Clk
      assign vga2Clk = clk 25M;
      wire rst;
      assign rst = btnC;
    //Clocks generation
    wire clk 25M, clk 100M, clk 600, clk 100k, locked;
    clk_wiz_0 my_clk_wiz_0(
```

```
.clk in(clk),
        .reset(rst),
        .clk 25M(clk 25M),
        .clk 100M(clk 100M),
        .locked(locked)
    );
    clk divider #(.FREQ(200000)) keyboard clk divider (
            .clk100MHz(clk 100M),
            .rst(rst),
            .clk div(clk 100k)
        );
      clk divider #(.FREQ(600)) game clk divider (
        .clk100MHz(clk 100M),
        .rst(rst),
        .clk div(clk 600)
    );
    clk divider #(.FREQ(50000000)) board divider (
        .clk100MHz(clk 100M),
        .rst(rst),
        .clk_div(clk_50M)
    );
   wire [10:0] rom addr;
    wire [23:0] rom data;
     //End of clock generation
   wire [9:0] bcgr xpos;
    wire [9:0] hcount, hcount_out_bg, hcount_out_brd, hcount_out_player,
hcount_out_monster_1, hcount_out_score, hcount_out;
   wire [9:0] vcount, vcount out bg, vcount out brd, vcount out player,
vcount out monster 1, vcount out score, vcount out;
   wire hsync, hsync out bg, hsync out brd, hsync out player,
hsync out score, hsync out gameover;
   wire vsync, vsync out bg, vsync out brd, vsync out player,
vsync out score, vsync out gameover;
   wire blnk, blnk out bg, blnk out brd, blnk out player, blnk out score,
blnk out gameover, blnk out;
    wire [23:0] rgb out bg, rgb out brd, rgb out player, rgb out monster 1,
rgb out score, rgb out gameover;
    wire [9:0] mario x, monster 1 x;
   wire [8:0] mario y, monster 1 y;
   wire [5:0] plane xpos ofs;
   wire [7:0] plane xpos;
   wire [3:0] plane_ypos;
   wire [15:0] blocking player;
   wire mario dir, blocking, monster 1 dir;
   wire [5:0] block display;
   wire [7:0] engine xpos, block display xpos;
   wire [3:0] engine ypos, block display ypos;
```

```
wire [5:0] engine write block, engine block;
      VgaTiming My VgaTiming (
          .pclk(clk 25M),
          .rst(rst),
          .hcount (hcount),
          .hsync(hsync),
          .vcount (vcount),
          .vsync(vsync),
          .blnk(blnk)
      );
//******BEGINING OF GRAPHICS******
      DrawBackground My DrawBackground (
          .hcount in (hcount),
          .hsync_in(hsync),
          .vcount_in(vcount),
          .vsync in (vsync),
          .blnk in (blnk),
          .xoffset(bcgr xpos),
          .clk(clk 25M),
          .rst(rst),
          .hcount out (hcount out bg),
          .hsync_out(hsync_out_bg),
          .vcount_out(vcount_out_bg),
          .vsync out (vsync out bg),
          .rgb out(rgb out bg),
          .blnk out (blnk out bg)
      );
    Board My Board (
        .hcount_in(hcount_out_bg),
        .hsync_in(hsync_out_bg),
        .vcount_in(vcount_out_bg),
        .vsync_in(vsync_out_bg),
        .rgb in(rgb out bg),
        .blnk in (blnk out bg),
        .plane xpos(plane xpos),
        .plane xpos ofs(plane xpos ofs),
        .block(block display),
        .pclk(clk 25M),
        .clk(clk 50M),
        .rst(rst),
        .block xpos (block display xpos),
        .block ypos (block display ypos),
          .hcount out (hcount out brd),
        .hsync out (hsync out brd),
        .vcount out (vcount out brd),
        .vsync out (vsync out brd),
        .rgb out (rgb out brd),
        .blnk out (blnk out brd)
    );
    Player My Player (
        .hcount in (hcount out brd),
        .hsync in (hsync out brd),
        .vcount in (vcount out brd),
```

```
.vsync in (vsync out brd),
    .rgb_in(rgb_out_brd),
    .blnk in (blnk out brd),
    .xpos(mario x),
    .ypos (mario y),
    .direction (mario dir),
    .size(0),
    .fire(0),
    .clk(clk_25M),
    .rst(rst),
    .rom_data(rom_data),
    .rom addr(rom addr),
    .hcount out (hcount out player),
    .hsync out (hsync out player),
    .vcount out (vcount out player),
    .vsync out (vsync out player),
    .rgb out (rgb out player),
    .blnk out (blnk out player)
DrawMarioScore My DrawMarioScore(
    .clk(clk 25M),
    .rst(rst),
    .hcount in (hcount out player),
    .hsync_in(hsync_out_player),
    .vcount_in(vcount_out_player),
    .vsync_in(vsync_out_player),
    .rgb in(rgb out player),
    .blnk in (blnk out player),
    .char pixels(char line pixels),
    .hcount out (hcount out score),
    .hsync_out(hsync_out_score),
    .vcount out (vcount out score),
    .vsync_out(vsync_out_score),
    .rgb_out(rgb_out_score),
    .blnk_out(blnk_out_score),
    .char xy(char xy),
    .char line(char line)
);
Gameover My Gameover (
    .hcount in (hcount out score),
    .hsync in (hsync out score),
    .vcount in (vcount out score),
    .vsync in (vsync out score),
    .rgb in (rgb out score),
    .blnk in (blnk out score),
    .gameover(gameover),
    .rst(rst),
    .clk(clk 25M),
    .hcount out (hcount out) ,
    .hsync_out(hsync_out_gameover),
    .vcount_out(vcount_out),
    .vsync out (vsync out gameover),
    .rgb out (rgb out gameover),
    .blnk out (blnk out gameover)
);
```

```
Change2Negedge My Change2Negedge (
        .hsync_in(hsync_out_gameover),
        .vsync in (vsync out gameover),
        .blnk in (blnk out gameover),
        .rgb in(rgb out gameover),
        .clk(clk 25M),
        .rst(rst),
        .hsync_out(vga2Hsync),
        .vsync_out(vga2Vsync),
        .blnk_out(vga2Blank),
        .rgb out({vga2Red,vga2Green,vga2Blue})
    );
//*********END OF GRAPHICS*******
//********GAME CONTROLL********
    dist mem gen 0 mario picture(
        .a(rom addr),
        .spo(rom data)
   );
   wire [11:0] fs_ram_a, fs_ram_dpra;
   wire [5:0] fs_ram_d, fs_ram_spo, fs_ram_dpo;
   dist mem gen 1 first stage ram(
       .a(fs_ram_a),
        .d(fs_ram_d),
        .dpra(fs_ram_dpra),
        .clk(clk 100M),
        .we(fs_ram_we),
        .spo(fs_ram_spo),
        .dpo(fs_ram_dpo)
   );
  wire [11:0] fs addr c;
  wire [5:0] fs data c;
   dist mem gen 2 first stage rom (
        .a(fs addr c),
        .spo(fs data c)
    );
   wire [7:0] char line pixels;
   wire [7:0] char code;
   wire [3:0] char line;
   wire [7:0] char xy;
   wire [3:0] mario lifes;
   wire [11:0] player points;
   wire [3:0] lvl_number;
   MarioFontRom My MarioFontRom (
        .addr({char code, char line}),
        .char line pixels(char line pixels)
   );
   MarioScore24x1 My MarioScore24x1(
        .char xy(char xy),
```

```
.mario lives (mario lifes),
    .level(lvl_number),
    .coins(player points),
    .char code (char code)
);
//Music
Music My_Music(
    .clk(clk 25M),
    .speaker(speaker)
);
FirstStage Engine FirstStage(
    .plane xpos(engine xpos),
    .plane ypos (engine ypos),
    .ram read data(fs ram spo),
    .write block (engine write block),
    .save block (engine we),
    .copy_backup(stage_restart),
    .copy_read_data(fs_data_c),
    .clk(clk 100M),
    .rst(rst),
    .blocking (engine blocking),
    .block(engine_block),
    .ram_addr(fs_ram_a),
    .ram_write_data(fs_ram_d),
    .ram we(fs ram we),
    .copy addr(fs addr c),
    .backuped(stage restarted)
);
FirstStage Display_FirstStage(
    .plane_xpos(block_display_xpos),
    .plane_ypos(block_display_ypos),
    .ram_read_data(fs_ram_dpo),
    .save_block(0),
    .copy backup(0),
    .block (block display),
    .ram addr(fs ram dpra)
);
wire [1:0] player speed;
GameEngine My GameEngine (
    .clk(clk \overline{25M}),
    .game clk(clk 600),
    .rst(rst),
    .L ALT(1 alt),
    .R ALT (r alt),
    .L CTRL(1 ctrl),
    .R CTRL(r ctrl),
    .SPACE (space),
    .L_SHIFT(l shift),
    .R_SHIFT(r_shift),
    .ESC(esc),
    .D_ARROW(d_arrow),
    .L ARROW(1 arrow),
    .R ARROW(r arrow),
    .blocking in (engine blocking),
```

```
.block_in(engine_block),
        .gameover(gameover),
        .stage restarted(stage restarted),
        .block xpos (engine xpos),
        .block ypos (engine ypos),
        .player xpos (mario x),
        .player_ypos(mario_y),
        .bcgr_xpos(bcgr_xpos),
        .plane_xpos(plane_xpos),
        .plane_xpos_ofs(plane_xpos_ofs),
        .player dir (mario dir),
        .player speed (player speed),
        .player life (mario lifes),
        .player points (player points),
        .lvl number(lvl number),
        .write block (engine write block),
        .block we (engine we),
       .restartgame(stage restart)
//*****END OF GAME CONTROLL*****
//*******PHERIPERALS*******
    Keyboard my_Keyboard(
       .CLK(clk 100k),
       .rst(rst),
        .PS2_CLK(PS2Clk),
       .PS2 DATA(PS2Data),
       .L ALT(1 alt),
       .R ALT(r alt),
       .L CTRL(1 ctrl),
       .R_CTRL(r_ctrl),
       .SPACE (space),
       .L_SHIFT(l_shift),
       .R_SHIFT(r_shift),
       .ESC(esc),
       .D ARROW(d arrow),
       .L ARROW(1 arrow),
       .R ARROW(r arrow)
   );
endmodule
`timescale 1ns / 1ps
///////
// Company:
                   AGH UST
// Engineer: Wojciech Gredel, Hubert Górowski
//
// Create Date:
// Design Name:
// Module Name:
                  MarioScore24x1
// Project Name: DOS Mario
// Target Devices: Basys3
// Tool versions: Vivado 2016.1
// Description:
//
     This module is ROM with some fancy strings
//
// Dependencies:
//
```

```
// Revision:
// Revision 0.01 - Module created
// Additional Comments:
///////
module MarioScore24x1 (
       input wire [7:0] char_xy,
           input wire [3:0] mario lives,
           input wire [3:0] level,
input wire [11:0] coins,
       output wire [7:0] char code
    );
    reg [7:0] char code nxt;
     reg [3:0] bcd0, bcd1, bcd2;
     reg [3:0] hex1, hex2, hex3, hex4, hex5;
      integer i;
    always @(coins)
   begin
       bcd0 = 0;
       bcd1 = 0;
       bcd2 = 0;
       for ( i = 11; i \ge 0; i = i - 1 )
       begin
           if( bcd0 > 4 ) bcd0 = bcd0 + 3;
            if ( bcd1 > 4 ) bcd1 = bcd1 + 3;
           if( bcd2 > 4 ) bcd2 = bcd2 + 3;
            { bcd2[3:0], bcd1[3:0], bcd0[3:0] } =
            { bcd2[2:0], bcd1[3:0], bcd0[3:0], coins[i] };
       end
    end
     always @(*)
     begin
           case(mario lives)
                 0: hex1 = 8'h00;
                 1: hex1 = 8'h01;
                 2: hex1 = 8'h02;
                 3: hex1 = 8'h03;
                 4: hex1 = 8'h04;
                 5: hex1 = 8'h05;
                 6: hex1 = 8'h06;
                 7: hex1 = 8'h07;
                 8: hex1 = 8'h08;
                 9: hex1 = 8'h09;
                 default: hex1 = 8'h00;
           endcase
           case(level)
                 0: hex2 = 8'h00;
                 1: hex2 = 8'h01;
                 2: hex2 = 8'h02;
                 3: hex2 = 8'h03;
                 4: hex2 = 8'h04;
                 5: hex2 = 8'h05;
```

```
6: hex2 = 8'h06;
              7: hex2 = 8'h07;
              8: hex2 = 8'h08;
              9: hex2 = 8'h09;
              default: hex2 = 8'h00;
        endcase
        case (bcd0)
              0: hex3 = 8'h00;
              1: hex3 = 8'h01;
              2: hex3 = 8'h02;
              3: hex3 = 8'h03;
              4: hex3 = 8'h04;
              5: hex3 = 8'h05;
              6: hex3 = 8'h06;
              7: hex3 = 8'h07;
              8: hex3 = 8'h08;
              9: hex3 = 8'h09;
              default: hex3 = 8'h00;
        endcase
        case (bcd1)
              0: hex4 = 8'h00;
              1: hex4 = 8'h01;
              2: hex4 = 8'h02;
              3: hex4 = 8'h03;
              4: hex4 = 8'h04;
              5: hex4 = 8'h05;
              6: hex4 = 8'h06;
              7: hex4 = 8'h07;
              8: hex4 = 8'h08;
              9: hex4 = 8'h09;
              default: hex4 = 8'h00;
        endcase
        case (bcd2)
              0: hex5 = 8'h00;
              1: hex5 = 8'h01;
              2: hex5 = 8'h02;
              3: hex5 = 8'h03;
              4: hex5 = 8'h04;
              5: hex5 = 8'h05;
              6: hex5 = 8'h06;
              7: hex5 = 8'h07;
              8: hex5 = 8'h08;
              9: hex5 = 8'h09;
              default: hex5 = 8'h00;
        endcase
  end
always @* begin
    case(char_xy)
        8'h00:
                    char code nxt = 8'h0A; // M
        8'h01:
                   char code nxt = 8'h0B; // A
        8'h02: char code nxt = 8'h0C; // R
        8'h03: char code nxt = 8'h0D; // I
        8'h04: char code nxt = 8'h0E; // 0
        8'h05: char code nxt = 8'h0F; //
        8'h06: char code nxt = 8'h10; // x
        8'h07: char code nxt = hex1; // liczba zyc
```

```
8'h08: char code nxt = 8'h0F; //
    8'h09: char code nxt = 8'h0F; //
    8'h0a: char code nxt = 8'h0F; //
    8'h0b: char code nxt = 8'h0F; //
    8'h0c: char code nxt = 8'h0F; //
    8'h0d: char code nxt = 8'h0F; //
8'h0e: char code nxt = 8'h0F; //
8'h0f: char code nxt = 8'h0F; //
8'h10: char_code_nxt = 8'h0F; //
8'h11: char_code_nxt = 8'h0F; //
8'h12: char_code_nxt = 8'h0F; //
8'h13: char code nxt = 8'h0F; //
8'h14: char_code_nxt = 8'h0F; //
8'h15: char_code_nxt = 8'h0F; //
8'h16: char_code_nxt = 8'h0F; //
8'h17: char_code_nxt = 8'h0F; //
8'h17: char_code_nxt = o nor; //
8'h18: char_code_nxt = 8'h0F; //
8'h19: char_code_nxt = 8'h0F; //
8'h1a: char_code_nxt = 8'h0F; //
8'h1b: char_code_nxt = 8'h0F; //
8'h1c: char_code_nxt = 8'h0F; //
8'h1d: char_code_nxt = 8'h0F; //
    8'h1e: char code nxt = 8'h0F; //
    8'h1f: char_code_nxt = 8'h0F; //
    8'h20: char_code_nxt = 8'h11; // moneta
    8'h21: char_code_nxt = 8'h0F; //
    8'h22: char_code_nxt = 8'h10; // x
    8'h23: char_code_nxt = hex5; // liczba monet
    8'h24: char_code_nxt = hex4; // liczba monet
    8'h25: char_code_nxt = hex3; // liczba monet
    8'h26: char_code_nxt = 8'h0F; //
8'h27: char code nxt = 8'h0F; //
8'h28: char_code_nxt = 8'h0F; //
8'h29: char_code_nxt = 8'h0F; //
8'h2a: char_code_nxt = 8'h0F; //
8'h2b: char_code_nxt = 8'h0F; //
8'h2c: char_code_nxt = 8'h0F; //
8'h2d: char code nxt = 8'h0F; //
8'h2e: char code nxt = 8'h0F; //
8'h2f: char code nxt = 8'h0F; //
8'h30: char code nxt = 8'h0F; //
8'h31: char code nxt = 8'h0F; //
8'h32: char code nxt = 8'h0F; //
8'h33: char code nxt = 8'h0F; //
8'h34: char code nxt = 8'h0F; //
8'h35: char code nxt = 8'h0F; //
8'h36: char code nxt = 8'h0F; //
8'h37: char code nxt = 8'h0F; //
8'h38: char code nxt = 8'h0F; //
8'h39: char code nxt = 8'h0F; //
8'h3a: char code nxt = 8'h0F; //
8'h3b: char code nxt = 8'h0F; //
    8'h3c: char code nxt = 8'h0F; //
    8'h3d: char code nxt = 8'h0F; //
    8'h3e: char code nxt = 8'h12; // L
    8'h3f: char code nxt = 8'h13; // E
    8'h40: char code nxt = 8'h14; // V
    8'h41: char code nxt = 8'h13; // E
```

```
8'h42: char code nxt = 8'h12; // L
          8'h43: char code nxt = 8'h0F; //
          8'h44: char_code_nxt = hex2; // numer levelu
          default: char code nxt = 8'hff;
          endcase
     end
   assign char_code = char_code_nxt;
endmodule
timescale 1 ns / 1 ps
///////
// Company:
                 AGH UST
// Engineer: Wojciech Gredel, Hubert Górowski
// Create Date:
// Design Name:
// Module Name:
                 Music
// Project Name:
                 DOS Mario
// Target Devices: Basys3
// Tool versions:
                 Vivado 2016.1
// Description:
//
    This module contains and generates music
//
// Dependencies:
//
// Revision:
// Revision 0.01 - Module created
// Additional Comments:
///////
module Music (
     input wire
                  clk,
     output reg
                  speaker
);
   wire [7:0] fullnote;
   wire [2:0] octave;
   wire [3:0] note;
   reg [30:0] tone;
   reg [8:0] clkdivider;
   reg [8:0] counter note;
   reg [7:0] counter octave;
   music rom my music rom (
       .clk(clk),
       .address(tone[30:22]),
       .note(fullnote)
   );
   divide by12 my divide by12 (
       .numerator(fullnote[5:0]),
       .quotient(octave),
       .remainder(note)
   );
   always @(posedge clk)
```

```
tone
              <= tone+31'd1;</pre>
    always @*
   case (note)
         0: clkdivider = 9'd511; //A
         1: clkdivider = 9'd482; // A#/Bb
         2: clkdivider = 9'd455; //B
         3: clkdivider = 9'd430; //C
         4: clkdivider = 9'd405; // C#/Db
         5: clkdivider = 9'd383; //D
         6: clkdivider = 9'd361; // D#/Eb
         7: clkdivider = 9'd341; //E
         8: clkdivider = 9'd322; //F
         9: clkdivider = 9'd303; // F#/Gb
        10: clkdivider = 9'd286; //G
        11: clkdivider = 9'd270; // G#/Ab
        default: clkdivider = 9'd0;
    endcase
    always @(posedge clk) counter note <= (counter note==0 ? clkdivider :
counter note-9'd1);
    always @(posedge clk) if(counter note==0) counter octave <=</pre>
(counter octave==0 ? 8'd255 >> octave : counter octave-8'd1);
    always @(posedge clk) if(counter note==0 && counter octave==0 &&
fullnote!=0 && tone[21:18]!=0) speaker <= \simspeaker;
   endmodule
    // This module is resposible for prividing divide by 12 operation.
   module divide by12(
        input [5:0] numerator, // value to be divided by 12
        output reg [2:0] quotient,
        output [3:0] remainder
   );
    reg [1:0] remainder3to2;
    always @(numerator[5:2])
   case(numerator[5:2])
                                // look-up table
         0: begin quotient=0; remainder3to2=0; end
         1: begin quotient=0; remainder3to2=1; end
         2: begin quotient=0; remainder3to2=2; end
         3: begin quotient=1; remainder3to2=0; end
         4: begin quotient=1; remainder3to2=1; end
         5: begin quotient=1; remainder3to2=2; end
         6: begin quotient=2; remainder3to2=0; end
         7: begin quotient=2; remainder3to2=1; end
         8: begin quotient=2; remainder3to2=2; end
         9: begin quotient=3; remainder3to2=0; end
        10: begin quotient=3; remainder3to2=1; end
        11: begin quotient=3; remainder3to2=2; end
        12: begin quotient=4; remainder3to2=0; end
        13: begin quotient=4; remainder3to2=1; end
        14: begin quotient=4; remainder3to2=2; end
        15: begin quotient=5; remainder3to2=0; end
    endcase
    assign remainder[1:0] = numerator[1:0]; // the first 2 bits are copied
```

```
through
   assign remainder[3:2] = remainder3to2; // and the last 2 bits come
from the case statement
   endmodule
`timescale 1ns / 1ps
///////
// Company:
// Engineer:
//
// Create Date: 05/29/2016 09:39:31 PM
// Design Name:
// Module Name: new_block
// Project Name:
// Target Devices:
// Tool Versions:
// Description:
//
// Dependencies:
//
// Revision:
// Revision 0.01 - File Created
// Additional Comments:
///////
module new block (
   input wire [5:0] block in,
   input wire up direction,
   input wire direction,
   input wire [9:0] relative_xpos,
   input wire [8:0] relative_ypos,
   output reg [5:0] block_out,
   output reg write_enable,
   output reg new_point
   );
   always @* begin
       if(block in == GY) begin
          new point = 1;
          block out = B;
          write enable = 1;
       else if(((relative xpos % 40) < 20) && (direction == 0)) begin
          if(block in == D) begin
              if (up direction) begin
                 new point = 1;
                 block out = DY;
                 write enable = 1;
              end
              else begin
                 new point = 0;
                 block out = block in;
                 write enable = 0;
              end
          end
          else if(block in == J) begin
```

```
if(up direction) begin
               block out = B;
               write enable = 1;
           end
           else begin
               block_out = block_in;
               write_enable = 0;
           end
       end
       else begin
           new point = 0;
           block out = block in;
           write enable = 0;
       end
    end
    else if(((relative xpos % 40) >= 20) && (direction == 1)) begin
       if(block in == D) begin
           if(up direction) begin
               new point = 1;
               block out = DY;
               write enable = 1;
           end
           else begin
               new point = 0;
               block_out = block_in;
               write_enable = 0;
           end
       end
       else if(block_in == J) begin
           new point = 0;
           if(up direction) begin
               block out = B;
               write_enable = 1;
           end
           else begin
               block_out = block_in;
               write_enable = 0;
           end
       end
       else begin
           new point = 0;
           block out = block in;
           write enable = 0;
       end
   end
    else begin
       new point = 0;
       block out = block in;
       write enable = 0;
   end
end
       localparam
                   A
                    В
       localparam
                    С
       localparam
                    D =
       localparam
                    E =
       localparam
       localparam F =
                              5
                   G =
       localparam
```

 $new_point = 0;$

```
localparam H =
                                                                                                                                                                                                                                                                                                                                                ;
        localparam I
                                                                                                                                                                                                                                                                                                                                                              ;
       localparam
                                                                                                                                                               J =
       localparam K = 10
     localparam L =
                                                                                                                                                                                                                                                                                   11
     localparam M =
                                                                                                                                                                                                                                                                                    12
localparam N = localparam O = localparam P = localparam R = localparam T = localparam U = localparam W = localparam W = localparam X = localparam X = localparam X = localparam I = localp
       localparam N =
                                                                                                                                                                                                                                                                                      13
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27
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;
    localparam KY = localparam PY = localparam TY = localparam UY = localparam = localp
                                                                                                                                                                                                                                                                                                     28
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                                                                                                                                                                                                                                                                                                                                                                       ;
     localparam WY localparam DY
                                                                                                                                                                                                                                                                                                            33
                                                                                                                                                                                                                                                                                                                                                                      ;
                                                                                                                                                                                                                                                                                                      34
                                                                                                                                                                                                                                                                                                                                                                      ;
     localparam BY = 35
                                                                                                                                                                                                                                                                                                                                                                          ;
```

$\verb"endmodule"$

```
`timescale 1 ns / 1 ps
//////
// Company: AGH UST
// Engineer: Wojciech Gredel, Hubert Górowski
//
// Create Date:
// Design Name:
// Module Name: Player
// Project Name: DOS_Mario
// Target Devices: Basys3
// Tool versions: Vivado 2016.1
// Description:
// This module displays player
//
// Dependencies:
//
// Revision:
// Revision 0.01 - Module created
// Additional Comments:
//
///////
module Player
#( parameter
   SMALL = 40,
```

```
BIG = 80
)
(
    input wire [9:0] xpos,
    input wire [8:0] ypos,
    input wire direction,
    input wire size,
    input wire fire,
    input wire [9:0] hcount in,
    input wire hsync_in,
    input wire [9:0] vcount_in,
    input wire vsync in,
    input wire blnk in,
    input wire rst,
    input wire clk,
    input wire [23:0] rgb in,
    input wire [23:0] rom data,
    output reg [10:0] rom addr,
    output reg [9:0] hcount out,
    output reg hsync_out,
    output reg [9:0] vcount out,
    output reg vsync_out,
    output reg [23:0] rgb out,
    output reg blnk out
    );
    localparam ALFA_COLOR = 24'hA3_49_A4;
    localparam YRES = 480;
    localparam PLAYER_WIDTH = 40;
    reg [5:0] player height;
    reg [5:0] player_height_nxt;
    reg [23:0] rgb_nxt;
    reg [10:0] rom_addr_nxt;
    always @(posedge clk or posedge rst) begin
        if(rst)
                        <= #1 0;
            rom addr
        else
                       \leftarrow #1 rom addr nxt;
            rom addr
    end
    always @(posedge clk or posedge rst) begin
        if(rst) begin
                        <= #1 0;
            rgb out
            hcount out \leftarrow #1 0;
            hsync out \leftarrow #1 0;
            vcount out <= #1 0;
            vsync out <= #1 0;
            blnk out <= #1 0;
        end
        else begin
                        <= #1 rgb_nxt;
            rgb out
            hcount out <= #1 hcount in;
            hsync out <= #1 hsync in;
            vcount_out <= #1 vcount in;</pre>
            vsync out <= #1 vsync in;
            blnk out <= #1 blnk in;
        end
    end
```

```
always @* begin
       if(size)
           player height nxt = BIG;
       else
           player height nxt = SMALL;
    end
   always@(posedge clk or posedge rst) begin
       if(rst) begin
           player_height = SMALL;
       end
       else begin
          player height = player height nxt;
       end
    end
   always @* begin
       if(direction) begin
           rom addr nxt = PLAYER WIDTH*(vcount in -(YRES - ypos -
player height)) + (PLAYER WIDTH - 1 - (hcount_in - xpos + 1));
       else begin
           rom addr nxt = PLAYER WIDTH*(vcount in -(YRES - ypos -
player height)) + ((hcount in - xpos + 1));
       end
   end
   always @*
     begin
       if(((YRES - 1 - vcount in) < (ypos + player height)) && ((YRES - 1</pre>
- vcount in) >= ypos) && (hcount in < (xpos+PLAYER WIDTH) ) && ((xpos) <=
hcount in)) begin
           if(rom data == ALFA COLOR)
               rgb nxt = rgb in;
           else
               rgb_nxt = rom_data;
       end
       else begin
           rgb nxt = rgb in;
       end
   end
endmodule
`timescale 1 ns / 1 ps
///////
// Company:
                  AGH UST
// Engineer: Wojciech Gredel, Hubert Górowski
// Create Date:
// Design Name:
// Module Name:
                  VgaTiming
// Project Name: DOS Mario
// Target Devices: Basys3
// Tool versions: Vivado 2016.1
// Description:
// This module creates timing for vga
// VGA 640x480 60Hz 25.175MHz pixel clock, horizontal pulse - negative,
vertical pulse - negative
//
// Dependencies:
```

```
//
// Revision:
// Revision 0.01 - Module created
// Additional Comments:
//
///////
module VgaTiming (
      input wire pclk,
      input wire rst,
     output reg [9:0] hcount,
     output reg hsync,
     output reg [9:0] vcount,
     output reg vsync,
     output reg blnk
     );
//
     localparam H TOTAL TIME = 1056;
//
      //localparam H ADDR TIME
11
     localparam H BLNK START = 800;
//
      //localparam H_BLNK_TIME = 160;
     localparam H_SYNC_START = 840;
localparam H_SYNC_TIME = 128;
//
//
      localparam H_SYNC_TIME
//
     localparam H SYNC POL
                             = 1; //0 - negative, 1 - positive
     localparam V TOTAL TIME = 628;
//
//
      //localparam V ADDR TIME
//
     localparam V BLNK START = 600;
     //localparam V_BLNK_TIME = 28;
//
     localparam V_SYNC_START = 601;
localparam V_SYNC_TIME = 4;
//
//
//
     localparam V SYNC POL
                                     //0 - negative, 1 - positive
                             = 1;
     localparam H TOTAL TIME = 800;
      //localparam H ADDR TIME
     localparam H BLNK START = 640;
      //localparam H BLNK TIME = 160;
     localparam H SYNC START = 656;
     localparam H SYNC TIME = 96;
     localparam H SYNC POL = 0; //0 - negative, 1 - positive
     localparam V TOTAL TIME = 525;
      //localparam V ADDR TIME
     localparam V BLNK START = 480;
      //localparam V BLNK TIME
     localparam V SYNC START = 490;
     localparam V SYNC TIME = 2;
     localparam V SYNC POL = 0; //0 - negative, 1 - positive
     reg [10:0] vcount nxt;
     reg vsync nxt;
     reg [10:0] hcount_nxt;
     reg hsync nxt;
     reg vblnk, hblnk;
     reg vblnk nxt, hblnk nxt;
    always @(posedge pclk or posedge rst) begin
           if (rst) begin
                 hcount <= #1 0;
```

```
hsync <= #1 0;
             vcount <= #1 0;
          vsync \leftarrow #1 0;
          hblnk <= #1 0;
          vblnk <= #1 0;
    end
        else begin
             hcount <= #1 hcount nxt;
        hsync <= #1 hsync_nxt;
             vcount <= #1 vcount nxt;
        vsync <= #1 vsync nxt;</pre>
        hblnk <= #1 hblnk nxt;
        vblnk <= #1 vblnk_nxt;</pre>
    end
end
always @* begin
   blnk = \sim (hblnk \mid vblnk);
 always @* begin
  //horizontal
    if(hcount==H BLNK START - 1) begin
          hcount nxt = hcount+1;
          if(H_SYNC_POL) hsync_nxt=0;
          else hsync nxt = 1;
          hblnk nxt=1;
    end
    else if(hcount==H SYNC START - 1) begin
          hcount nxt = hcount+1;
          if(H SYNC POL) hsync nxt=1;
          else hsync nxt = 0;
        hblnk nxt=1;
    end
    else if (hcount==H SYNC START + H SYNC TIME - 1) begin
          hcount nxt = hcount+1;
          if(H_SYNC_POL) hsync_nxt=0;
          else hsync_nxt = 1;
        hblnk nxt=1;
    end
    else if (hcount==H TOTAL TIME - 1) begin
         hcount nxt = 0;
        if(H SYNC POL) hsync nxt=0;
        else hsync nxt = 1;
        hblnk nxt=0;
    end
    else begin
         hcount nxt = hcount+1;
        hsync nxt = hsync;
        hblnk nxt = hblnk;
        //vertical
    if(hcount==H TOTAL TIME - 1) begin
              if(vcount==V BLNK START - 1) begin
                    vcount nxt = vcount+1;
                    if(V SYNC_POL) vsync_nxt=0;
              else vsync nxt = 1;
              vblnk nxt=1;
        end
              else if (vcount==V SYNC START - 1) begin
                    vcount nxt = vcount+1;
```

```
if(V_SYNC_POL) vsync_nxt=1;
            else vsync_nxt = 0;
            vblnk_nxt = 1;
      end
            else if (vcount==V_SYNC_START + V_SYNC_TIME - 1) begin
                  vcount nxt = vcount+1;
                  if(V_SYNC_POL) vsync_nxt=0;
            else vsync_nxt = 1;
                  vblnk_nxt = 1;
      end
            else if (vcount==V_TOTAL_TIME - 1) begin
                  vcount nxt = 0;
                  if(V_SYNC_POL) vsync_nxt=0;
          else vsync \bar{n}xt = 1;
                  vblnk nxt = 0;
      end
            else begin
                  vcount nxt = vcount+1;
                  vsync nxt = vsync;
                 vblnk nxt = vblnk;
      end
  end
  else begin
        vcount nxt = vcount;
        vsync_nxt = vsync;
        vblnk_nxt = vblnk;
  end
end
```

endmodule