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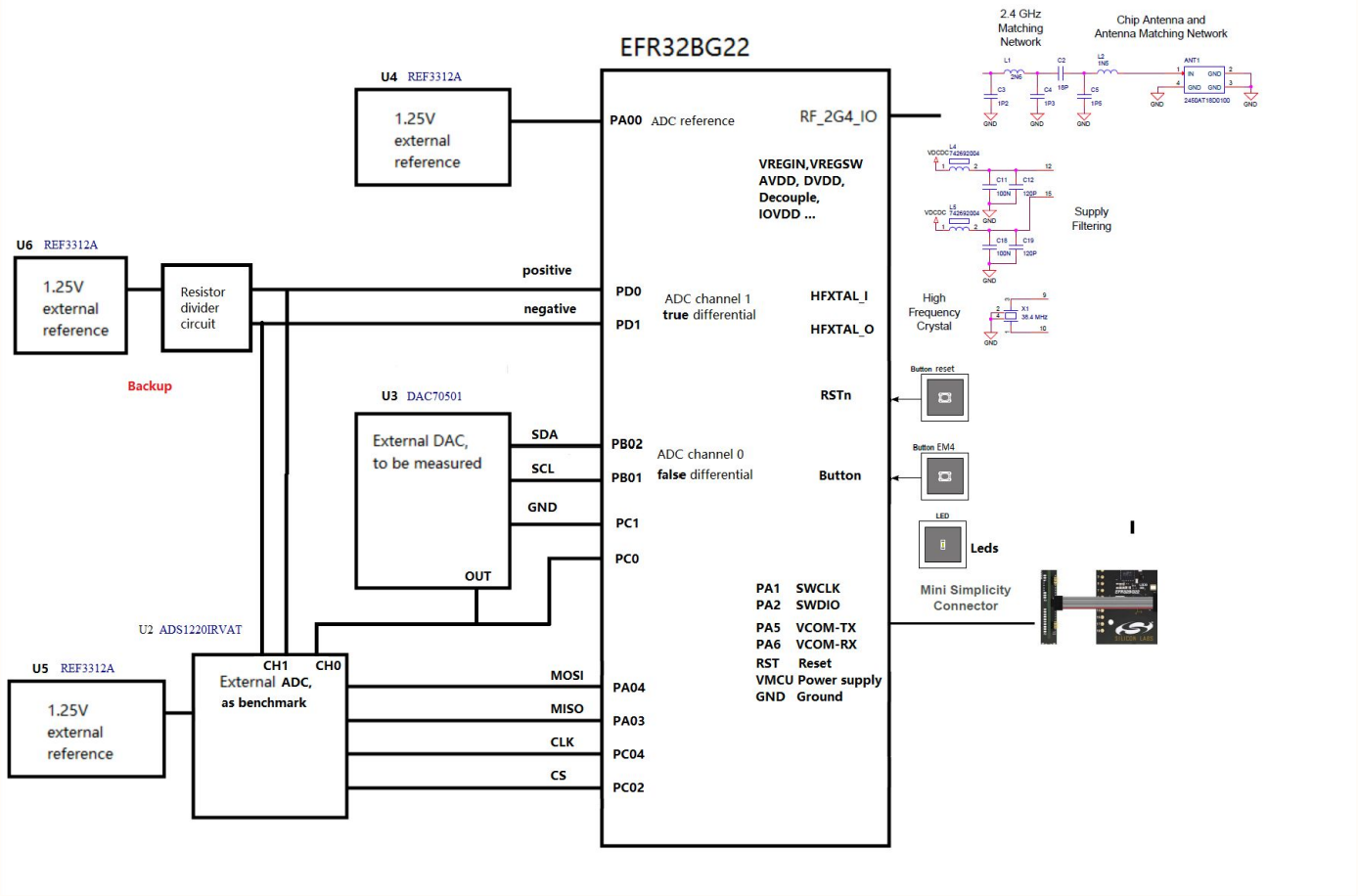

Version	Changes
V1.0	Initial Release

SILICON LABS

Schematic Title	EFR32BG22 CGM Board
Page Title	Title Page
Designed: Jun Fan	Approved: Cheng Yuan
Size: A3	BOM Doc No:
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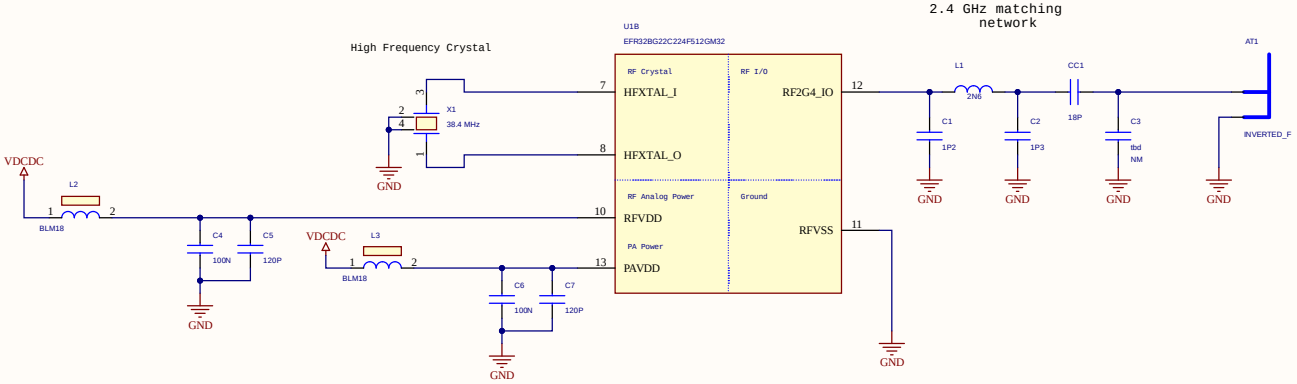
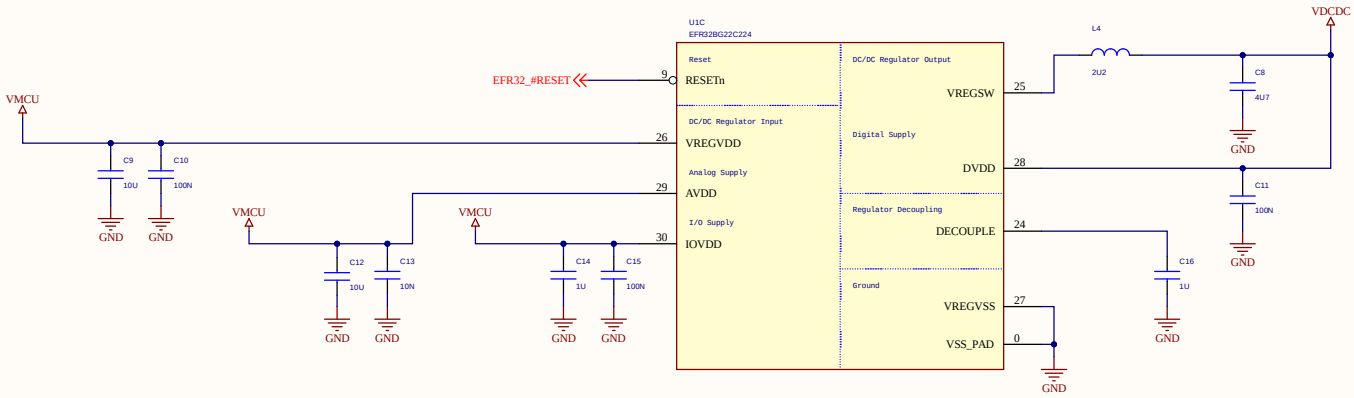
Antenna & Radio Interface



Power & Decoupling



Schematic Title	EFR32BG22 CGM Board
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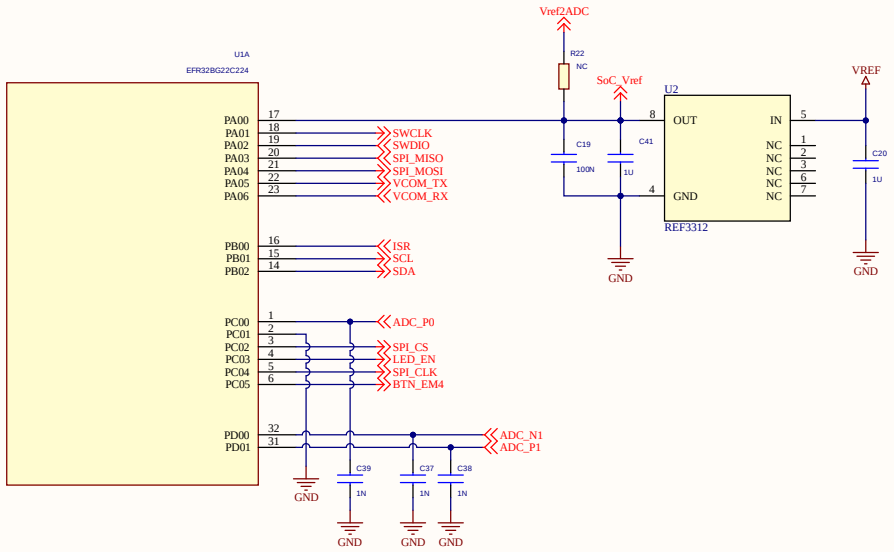
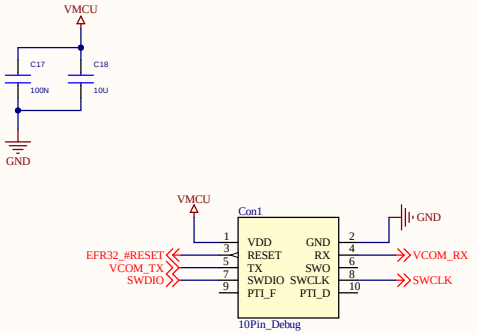
EFR32 I/O Port Pins

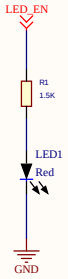


Debug Connector



Reset Button & Wakeup Button

LED



SILICON LABS

Schematic Title	EFR32BG22 CGM Board
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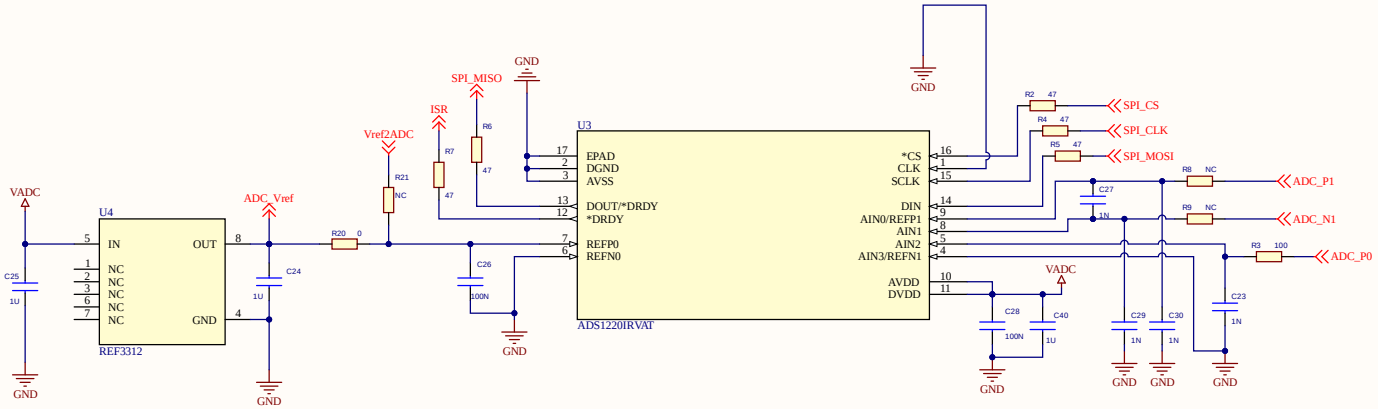
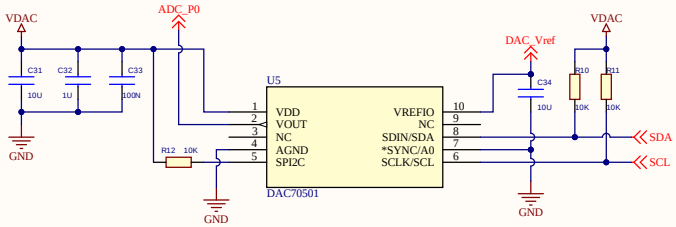
ADC

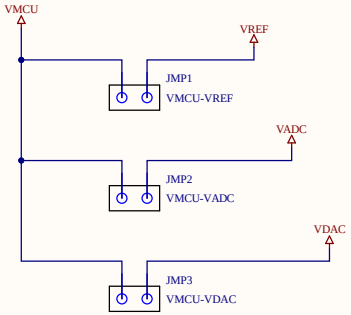


DAC Source



Test Points

Divided Power Domains



Schematic Title	EFR32BG22 CGM Board
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Designed: Jun Fan	Approved: Cheng Yuan
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