

[https://github.com/theguydanish/CM4\\_MATX](https://github.com/theguydanish/CM4_MATX)

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Sheet: /

File: CM4\_MATX.sch

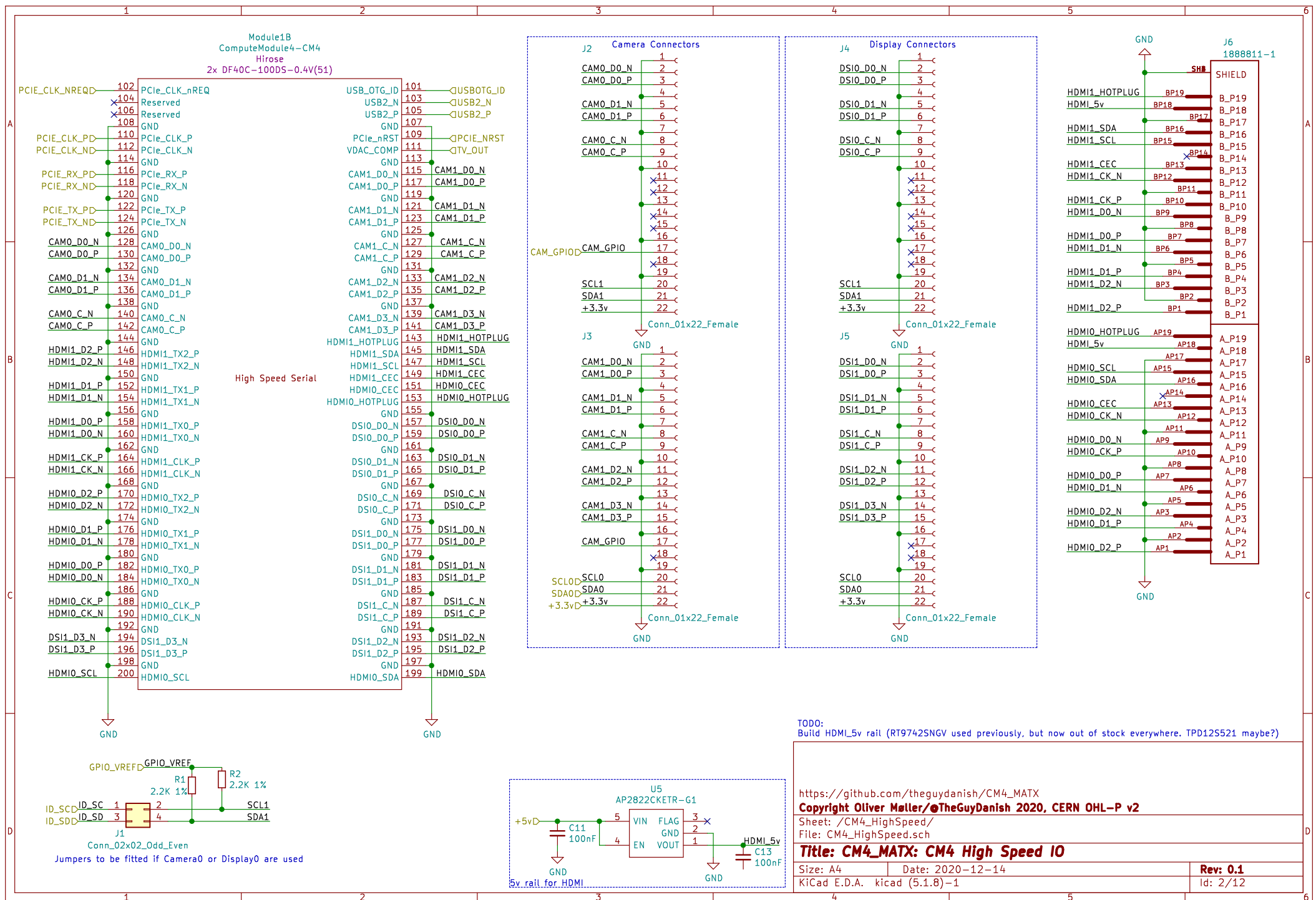
### Title: CM4\_MATX Root Sheet

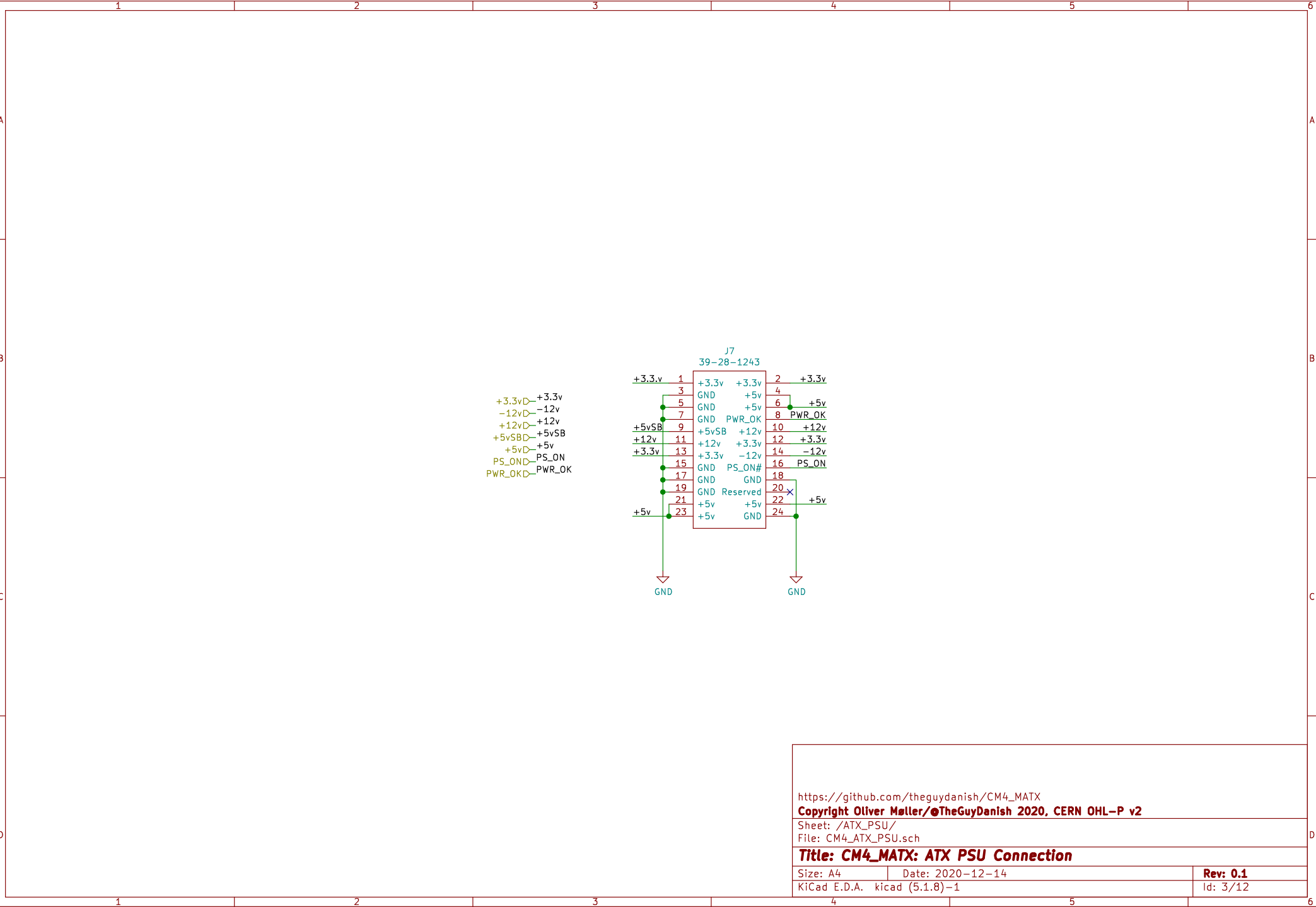
Size: A4 Date: 2020-12-14

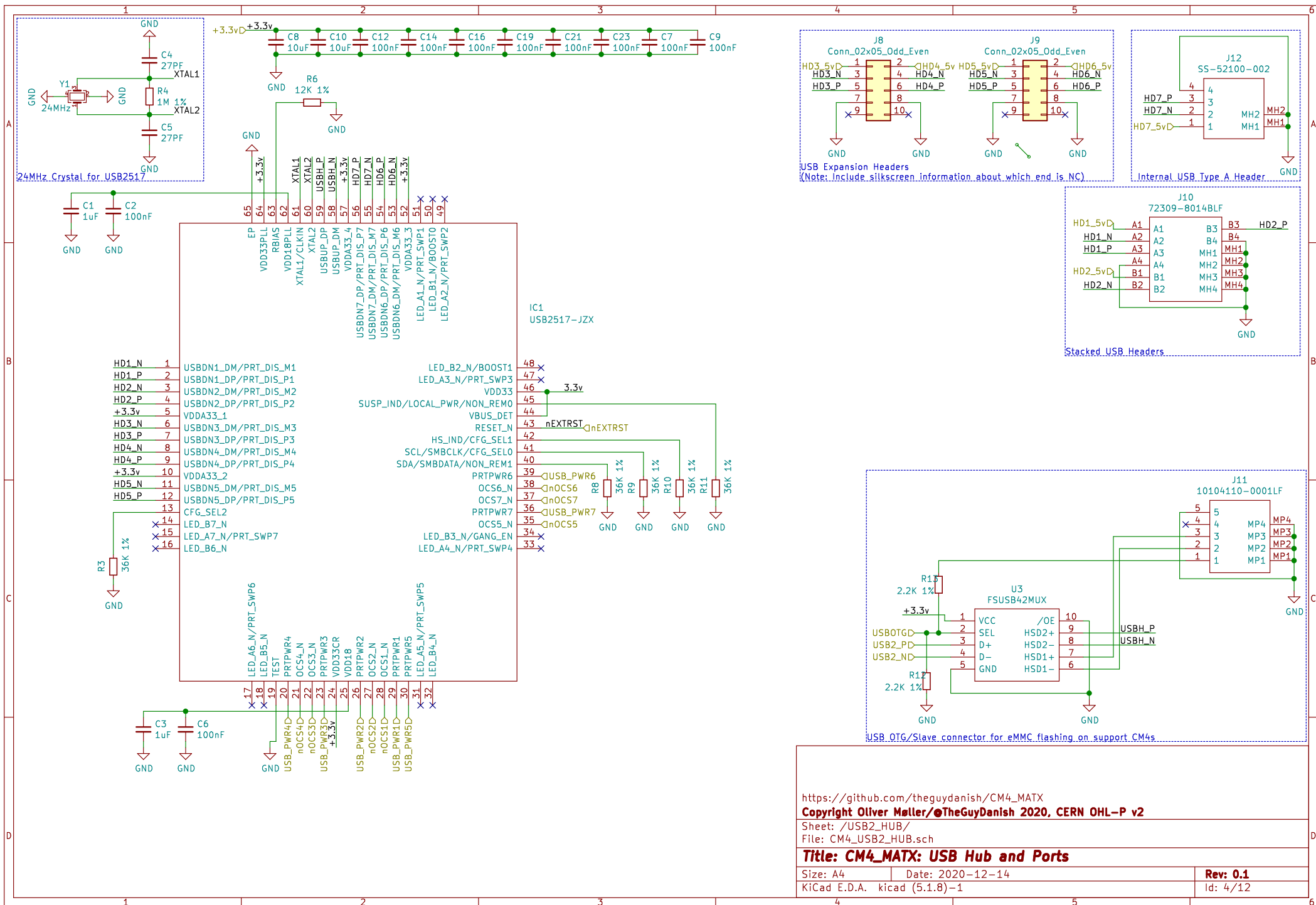
KiCad E.D.A. kicad (5.1.8)-1

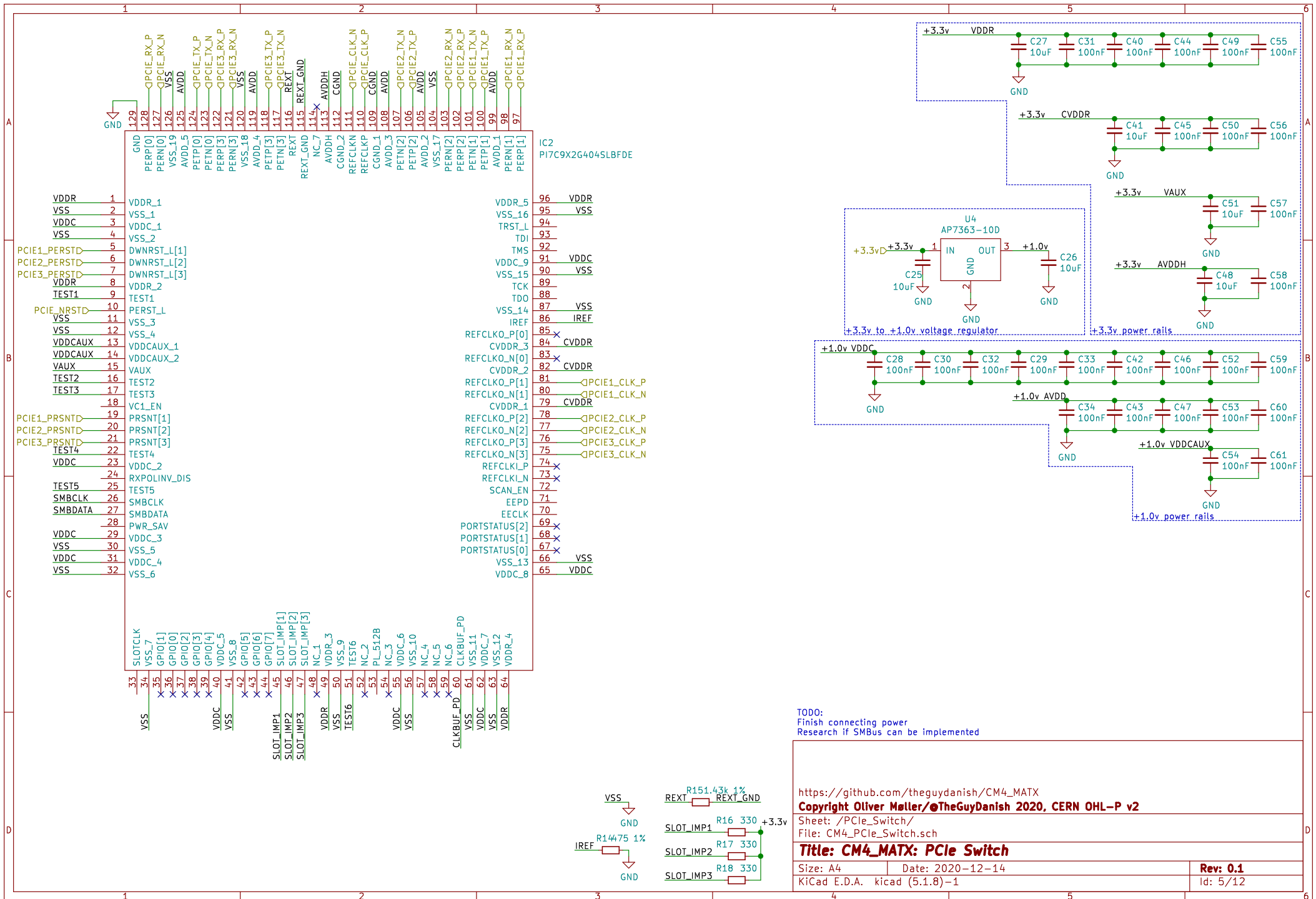
Rev: 0.1

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TODO:  
Finish connecting power  
Research if SMBus can be implemented

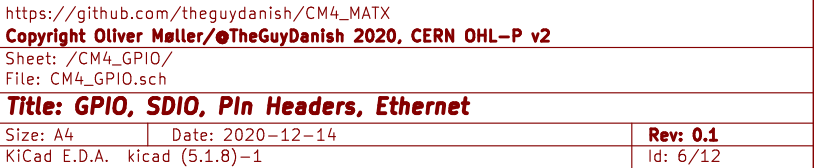
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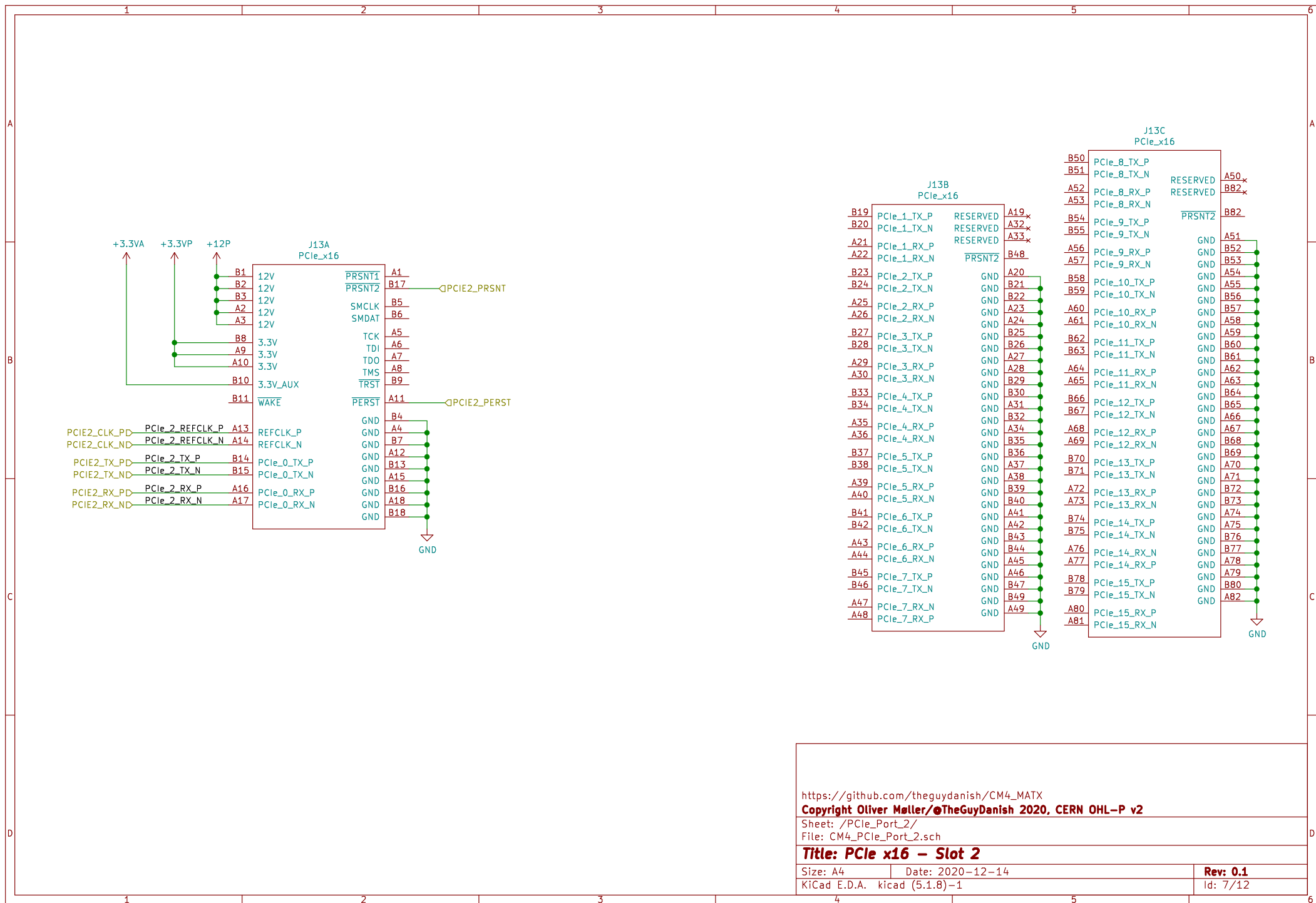
Sheet: /PCle\_Switch/  
File: CM4\_PcCle\_Switch.sch

**Title: CM4\_MATX: PCIe Switch**

Size: A4 Date: 2020-12-14  
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Sheet: /PCle\_Port\_2/

File: CM4\_PClle\_Port\_2.sch

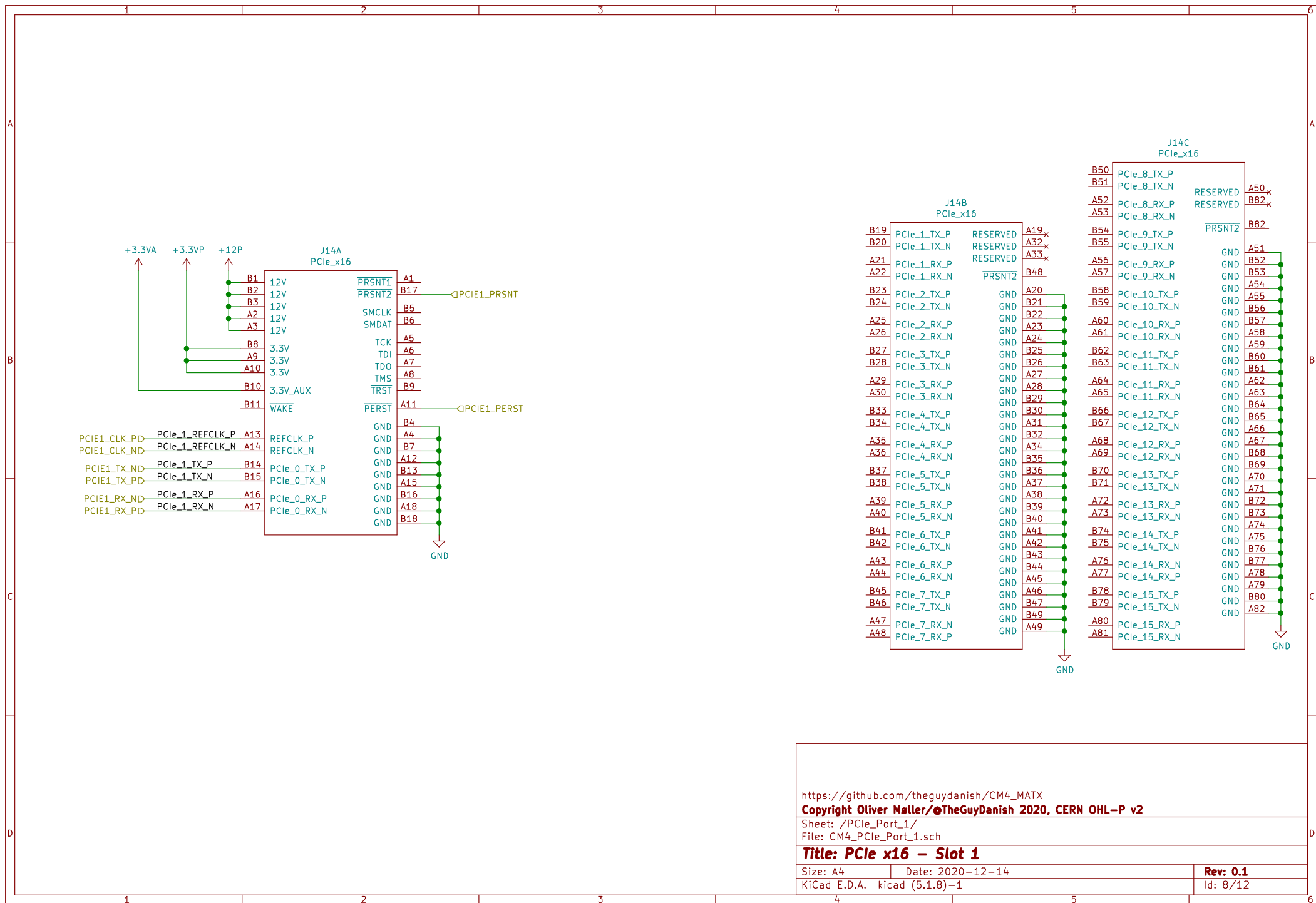
**Title: PCle x16 – Slot 2**

Size: A4 Date: 2020-12-14

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Sheet: /PCIe\_Port1/

File: CM4\_PCIe\_Port1.sch

**Title: PCIe x16 – Slot 1**

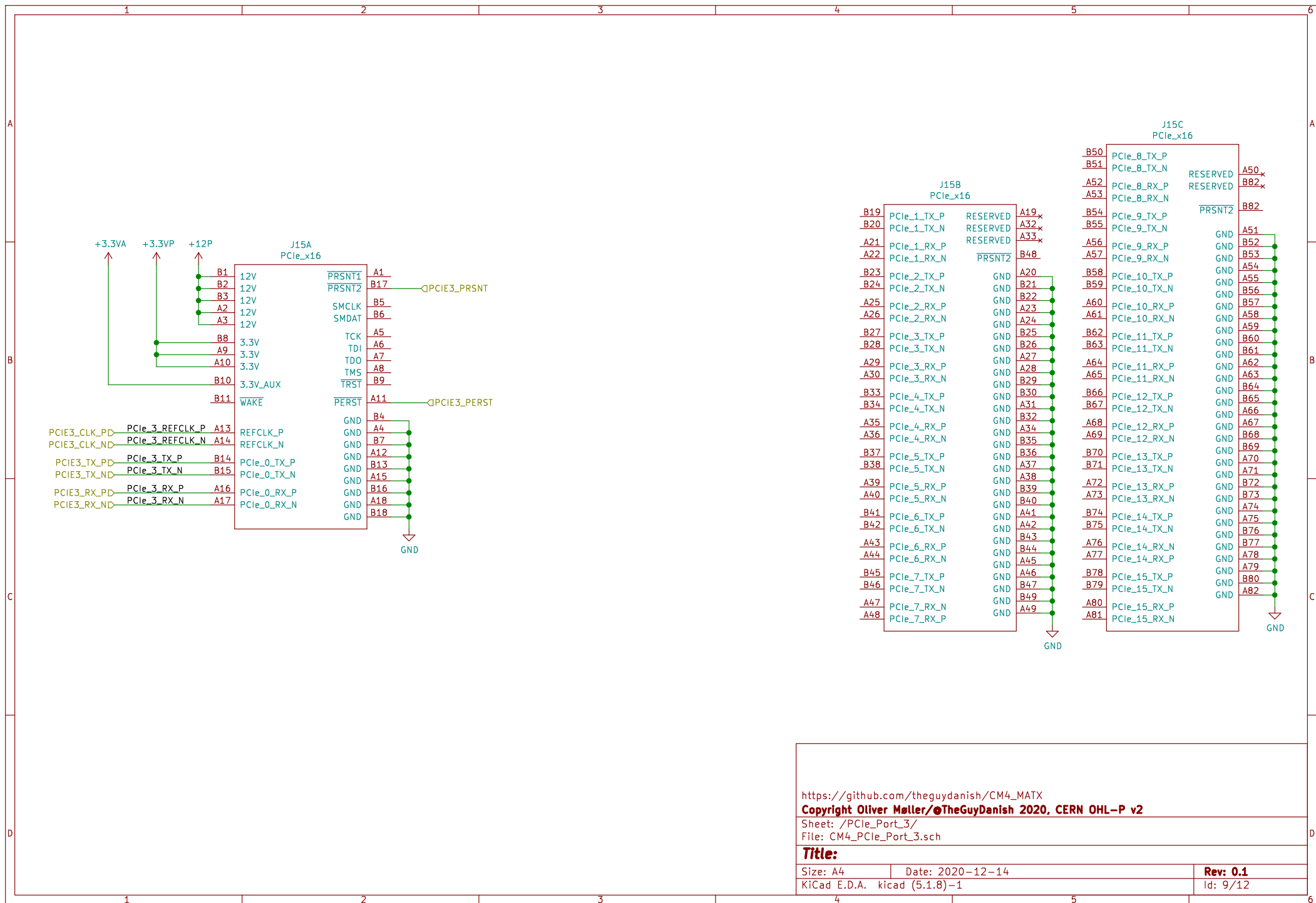
Size: A4 Date: 2020-12-14

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Sheet: /PCIe\_Port\_3/

File: CM4\_PCIe\_Port\_3.sch

Title:

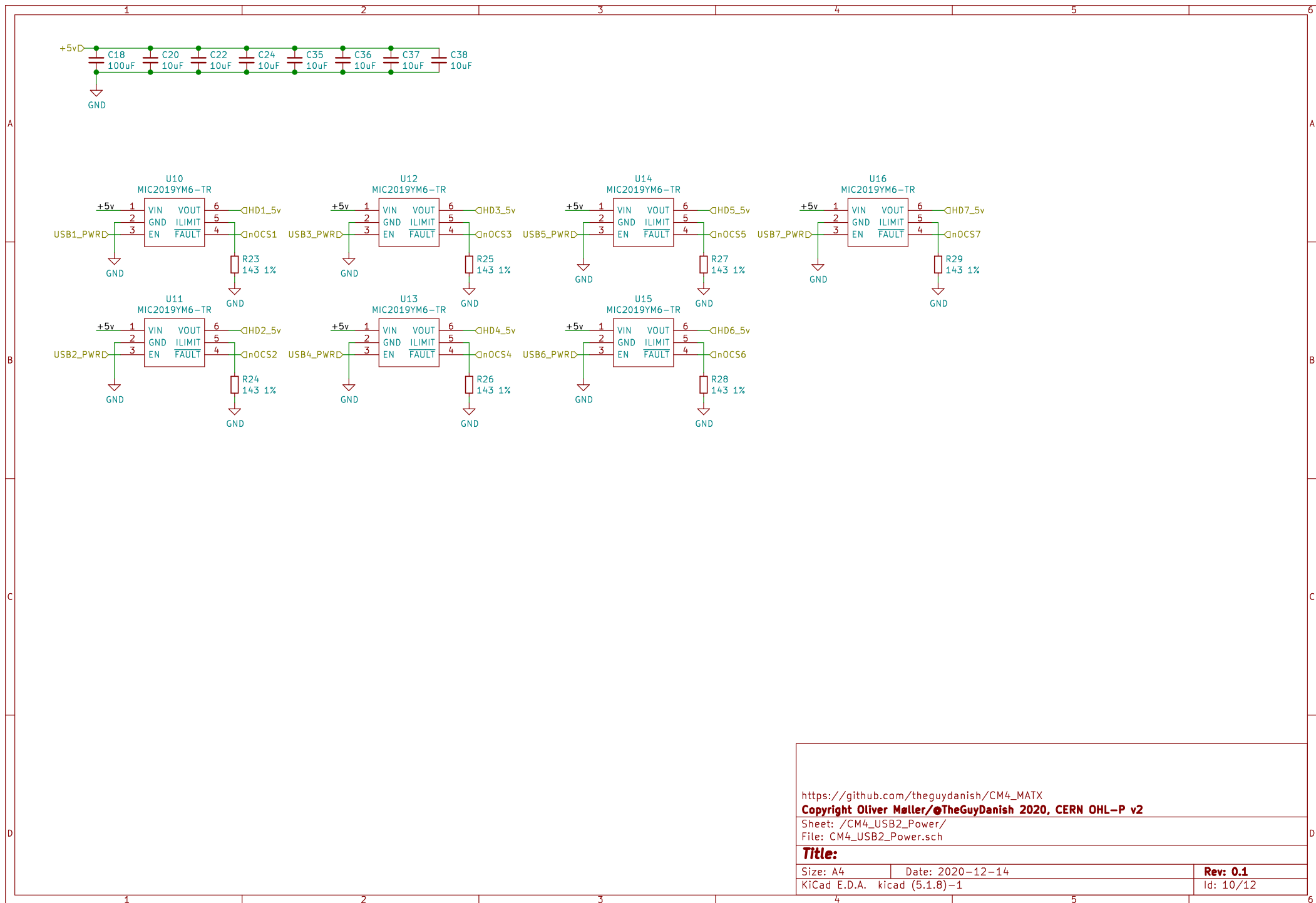
Size: A4

Date: 2020-12-14

Rev: 0.1

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Sheet: /CM4\_USB2\_Power/

File: CM4\_USB2\_Power.sch

**Title:**

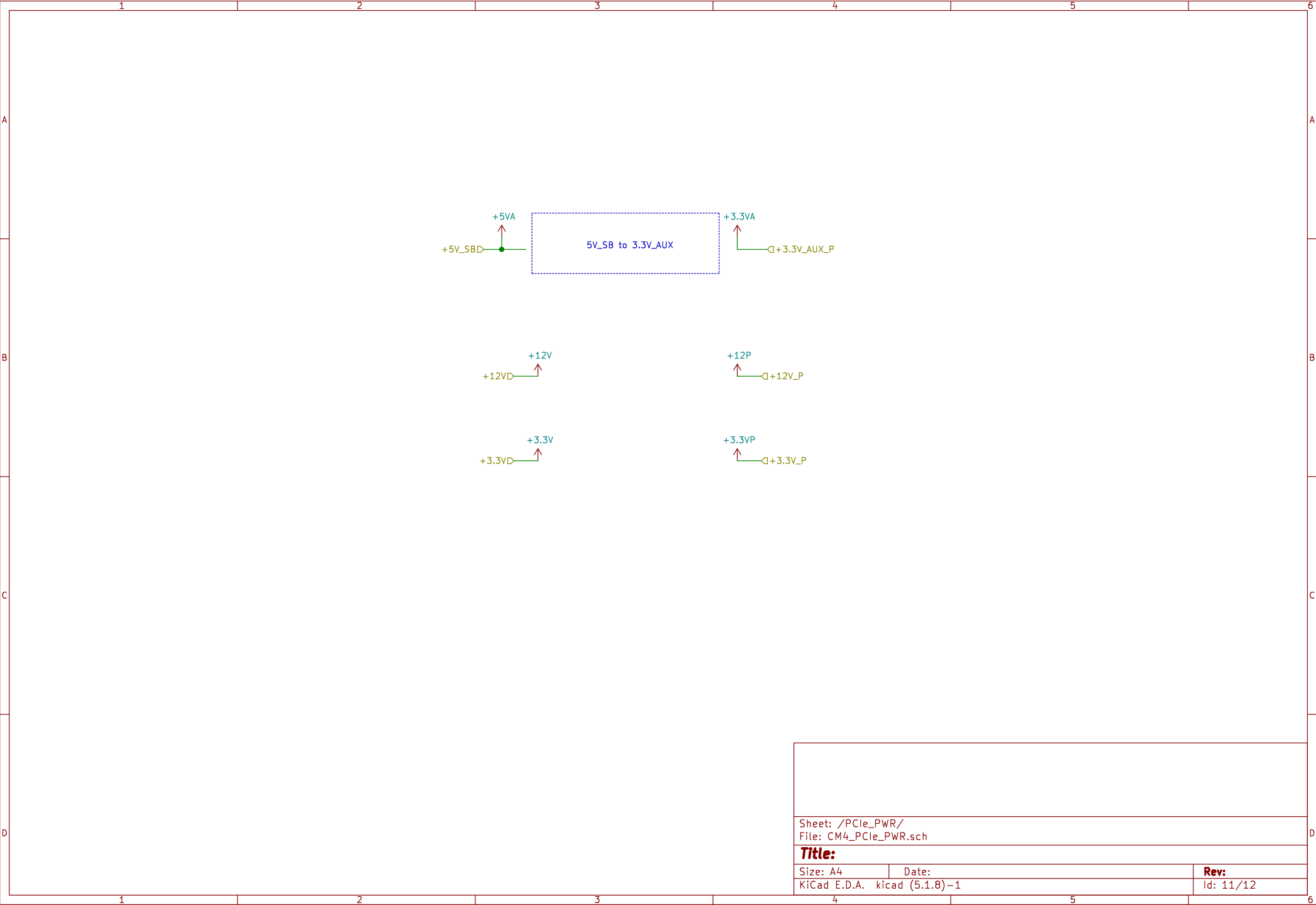
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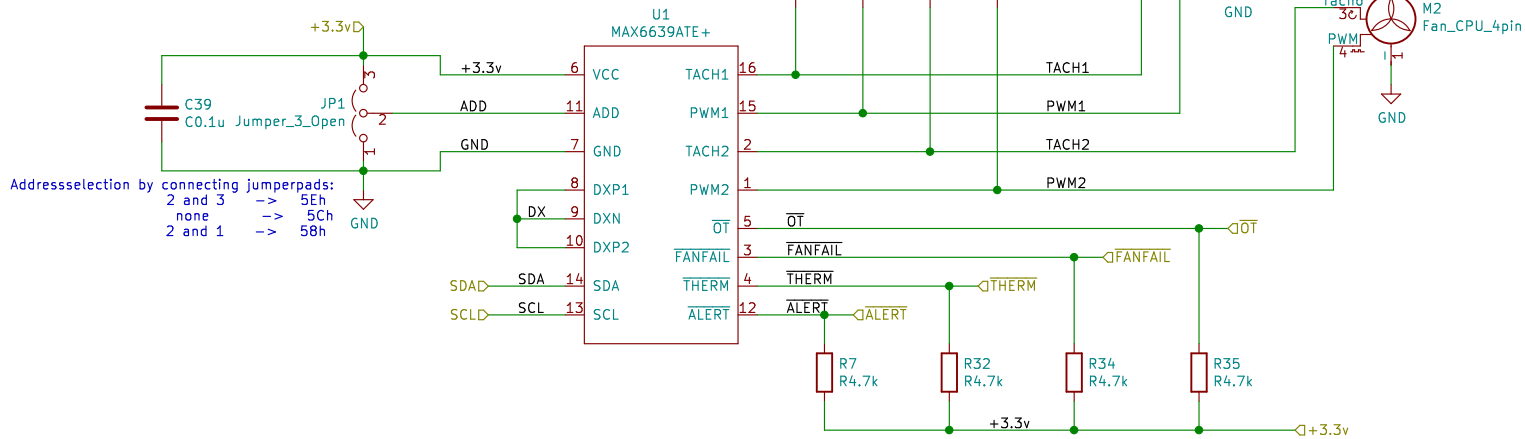
Rev: 0.1

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Sheet: /PCIe_PWR/ File: CM4_PCIe_PWR.sch		
<b>Title:</b>		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.8)-1	Id: 11/12	



Sheet: /FanCTRL\_MAX6639/  
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**Title:**

Size: A4  
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Date:

Rev:  
 Id: 12/12