

https://github.com/theguydanish/CM4_MATX

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Sheet: /ATX_PSU/

File: CM4_ATX_PSU.sch

Title: CM4_MATX: ATX PSU Connection

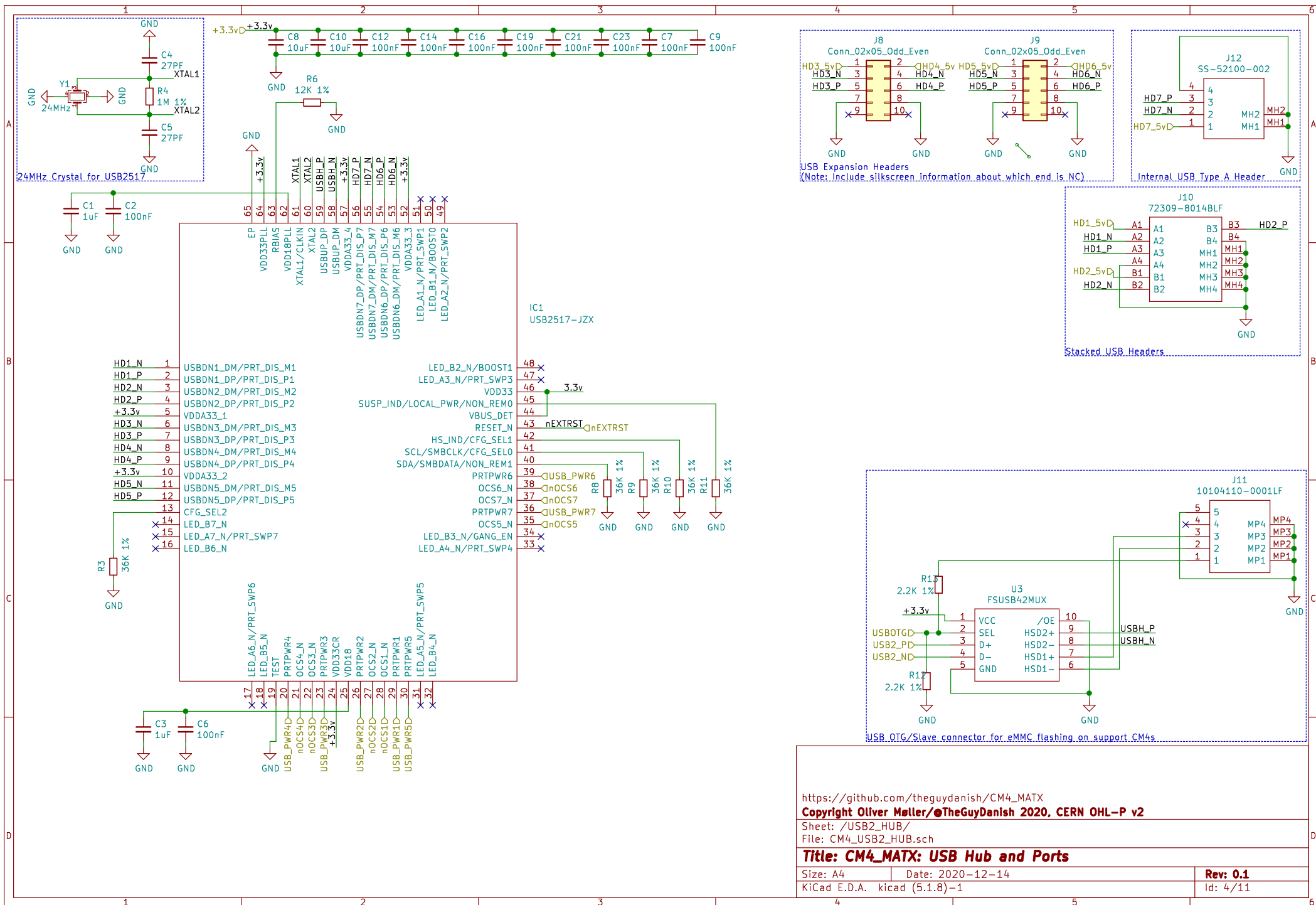
Size: A4

Date: 2020-12-14

Rev: 0.1

KiCad E.D.A. kicad (5.1.8)-1

Id: 3/11



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Sheet: /USB2_HUB/

File: CM4_USB2_HUB.sch

Title: CM4_MATX: USB Hub and Ports

Size: A4

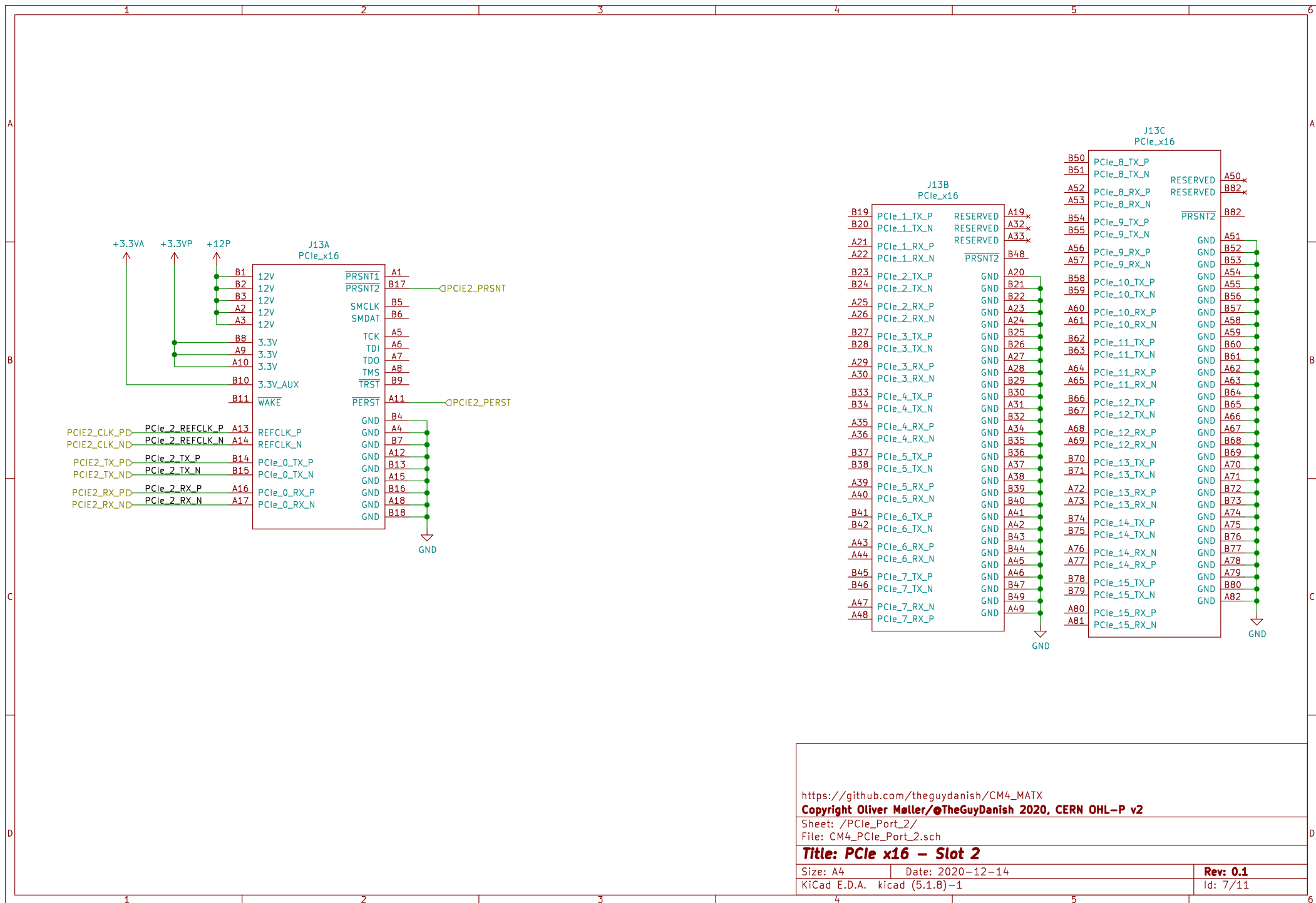
Date: 2020-12-14

Rev: 0.1

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Id: 4/11





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Sheet: /PCle_Port_2/

File: CM4_PCLE_Port_2.sch

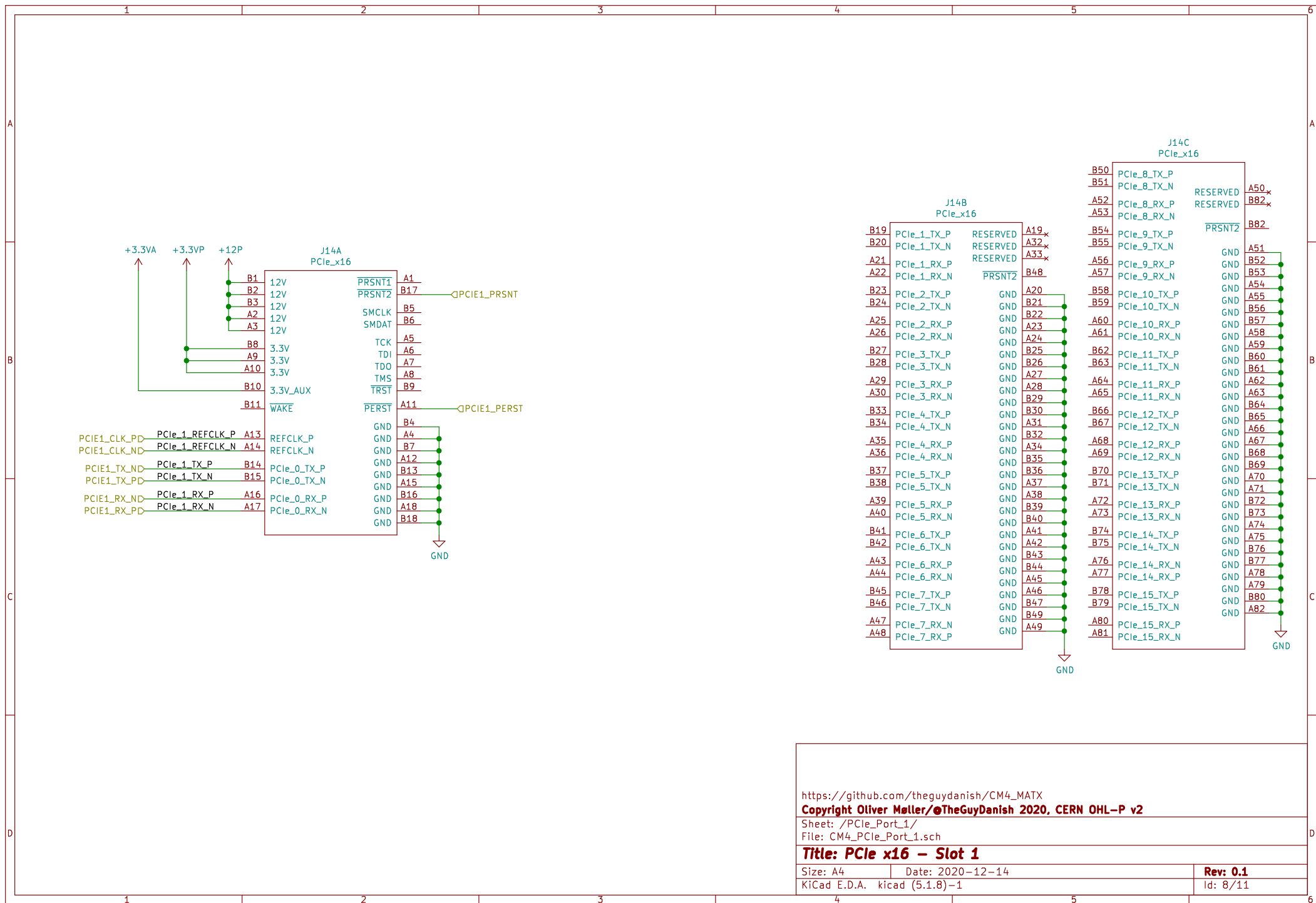
Title: PCIe x16 – Slot 2

Size: A4 Date: 2020-12-14

KiCad E.D.A. kicad (5.1.8)-1

Rev: 0.1

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Sheet: /PCie_Port1/

File: CM4_Pcie_Port1.sch

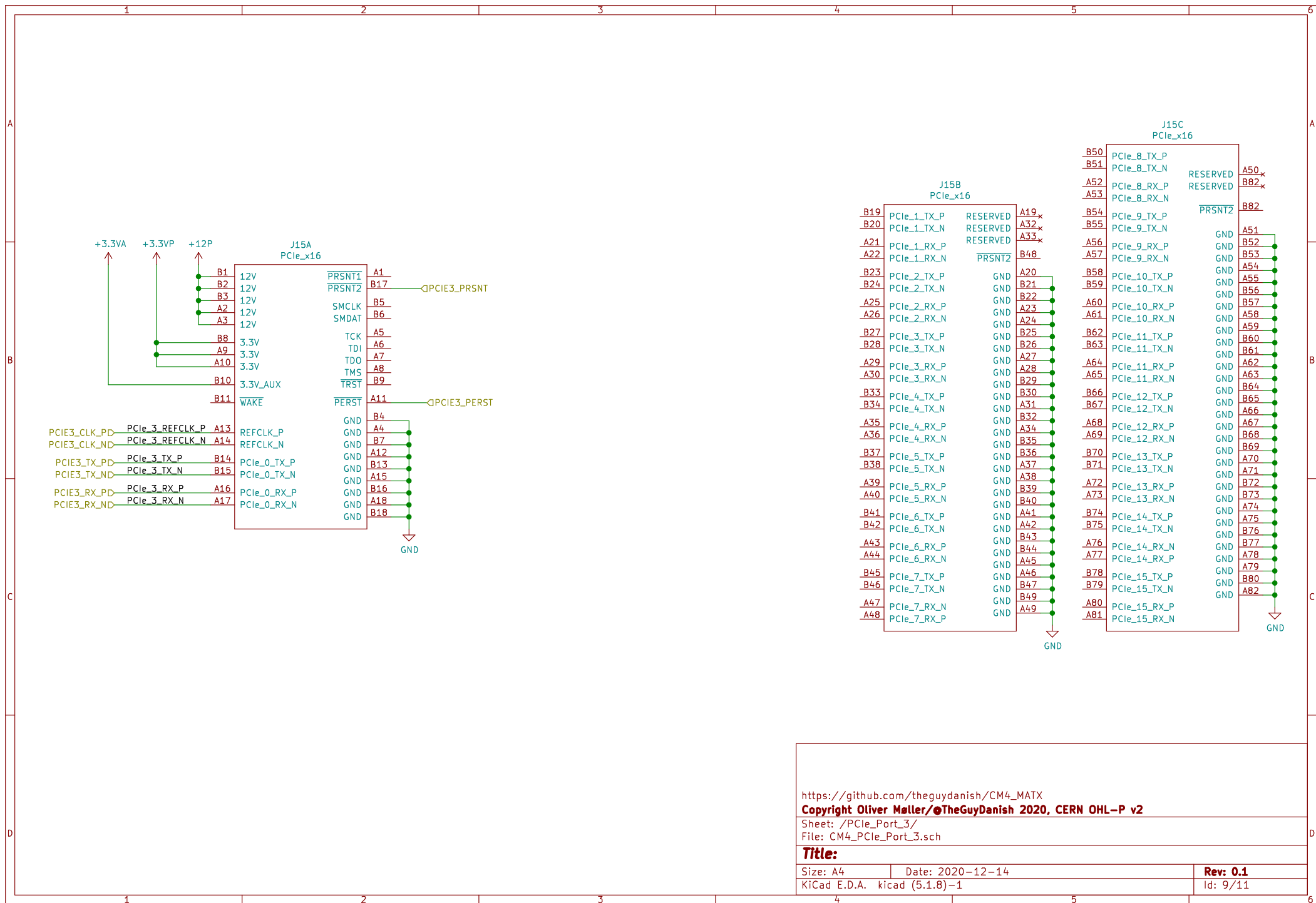
Title: PCie x16 – Slot 1

Size: A4 Date: 2020-12-14

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Rev: 0.1

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Sheet: /PCIe_Port_3/

File: CM4_PCIe_Port_3.sch

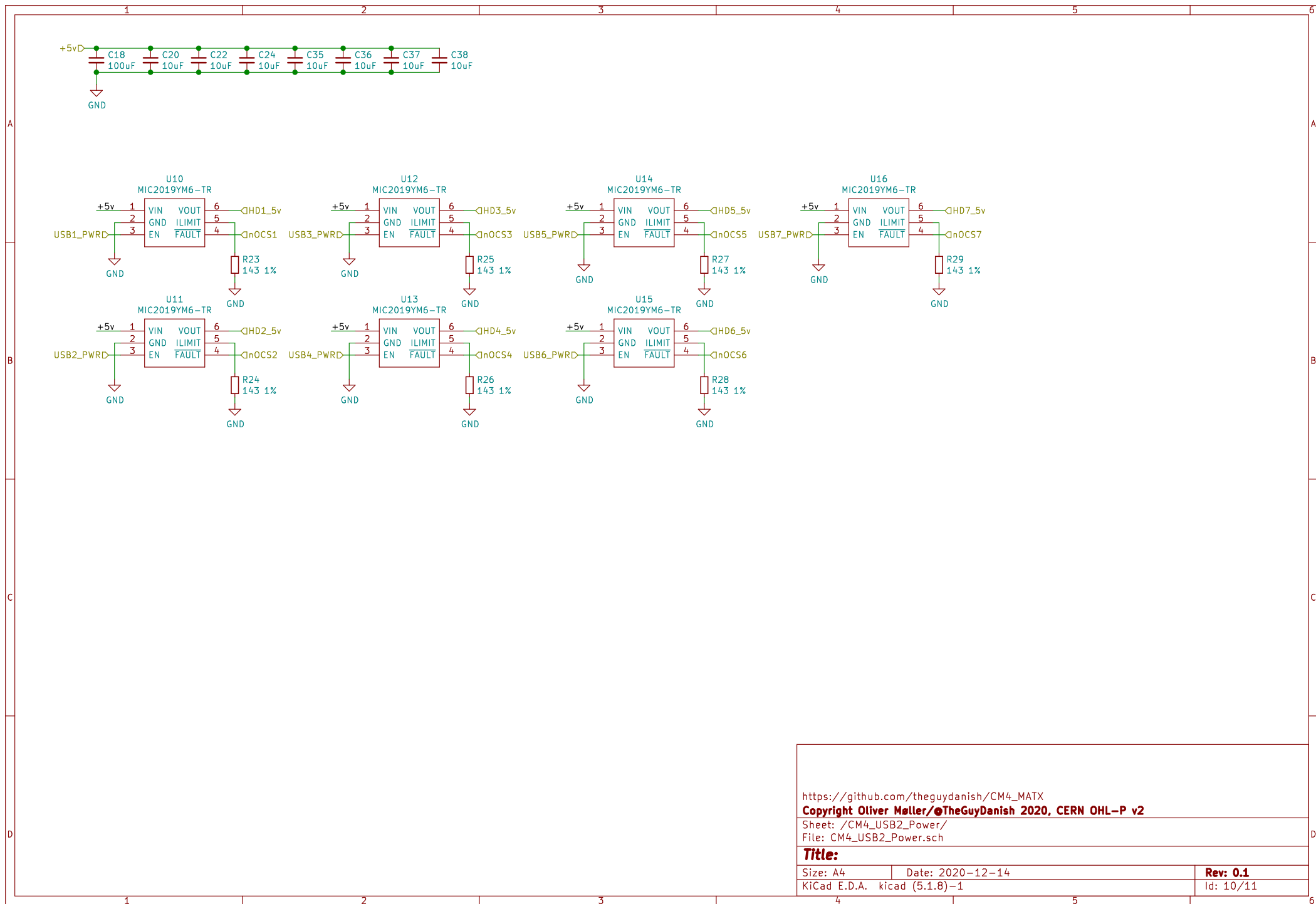
Title:

Size: A4 Date: 2020-12-14

KiCad E.D.A. kicad (5.1.8)-1

Rev: 0.1

Id: 9/11



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Sheet: /CM4_USB2_Power/

File: CM4_USB2_Power.sch

Title:

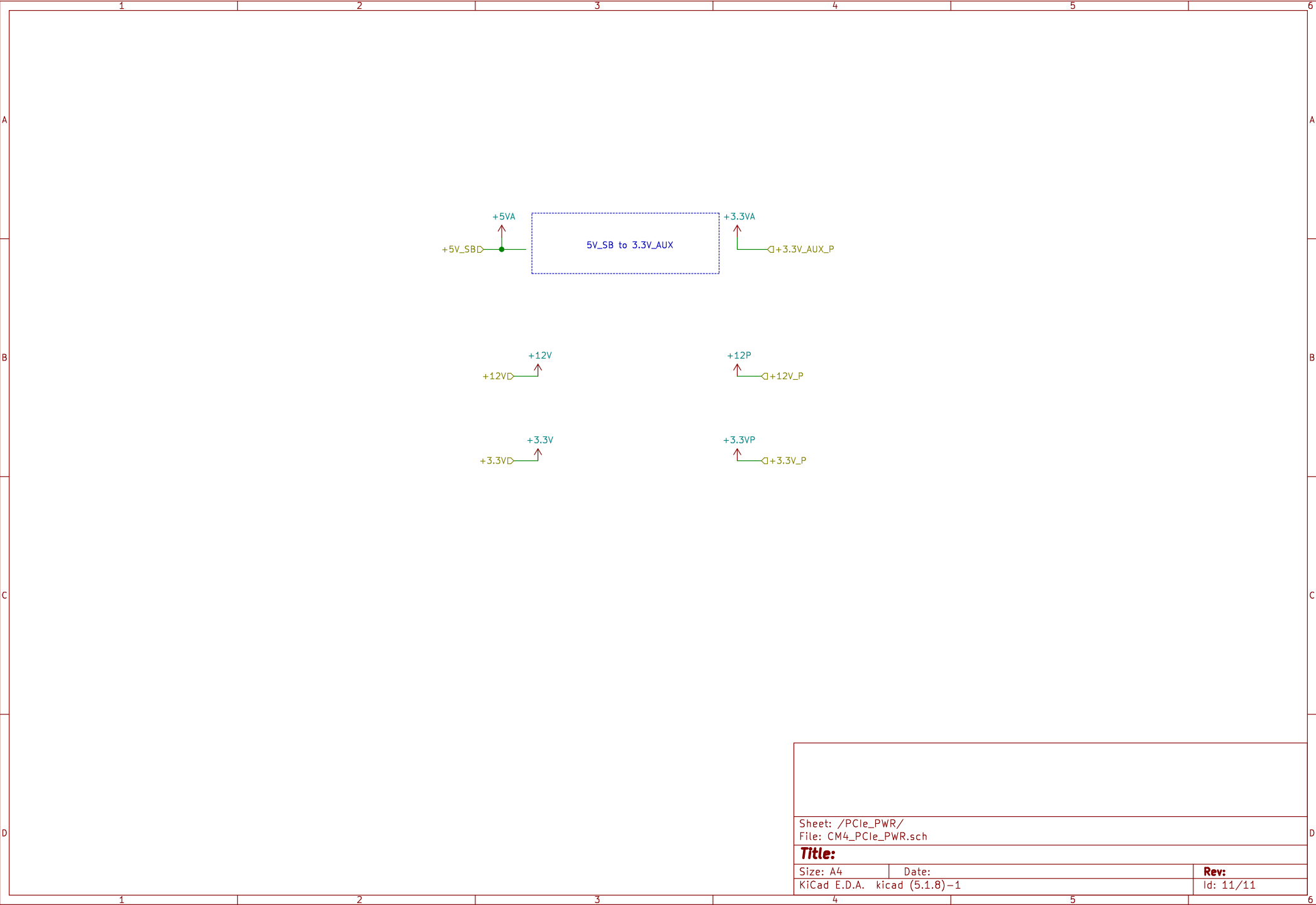
Size: A4

Date: 2020-12-14

Rev: 0.1

KiCad E.D.A. kicad (5.1.8)-1

Id: 10/11



Sheet: /PCIe_PWR/ File: CM4_PCIe_PWR.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.8)-1		Id: 11/11