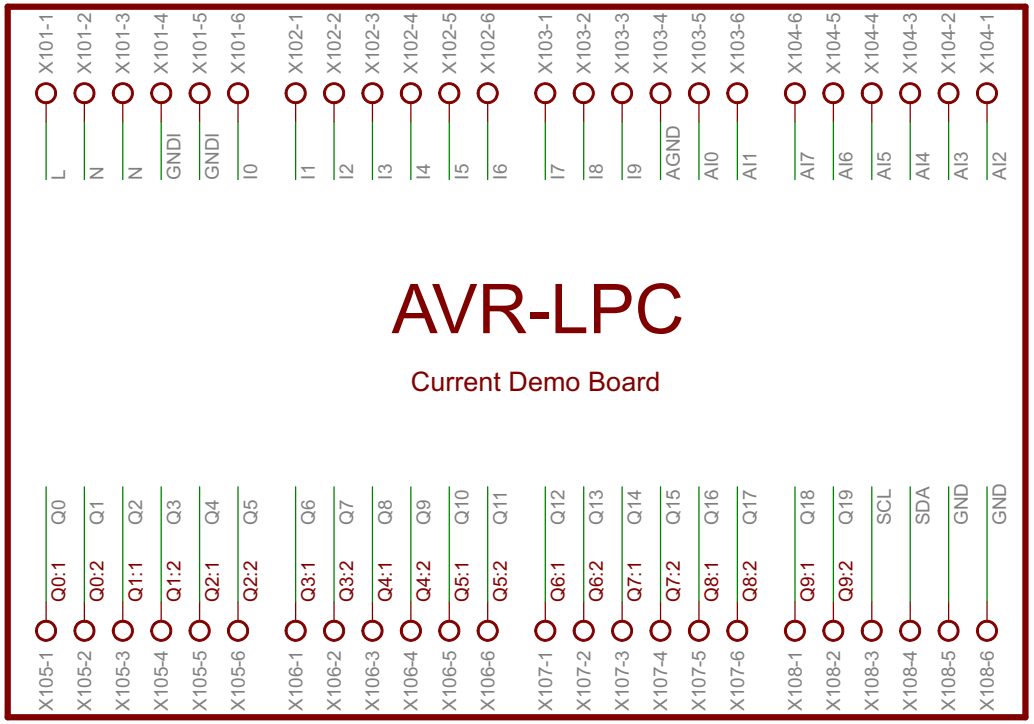

IO Connectors By The^Day^Of^DooM	AVR-LPC
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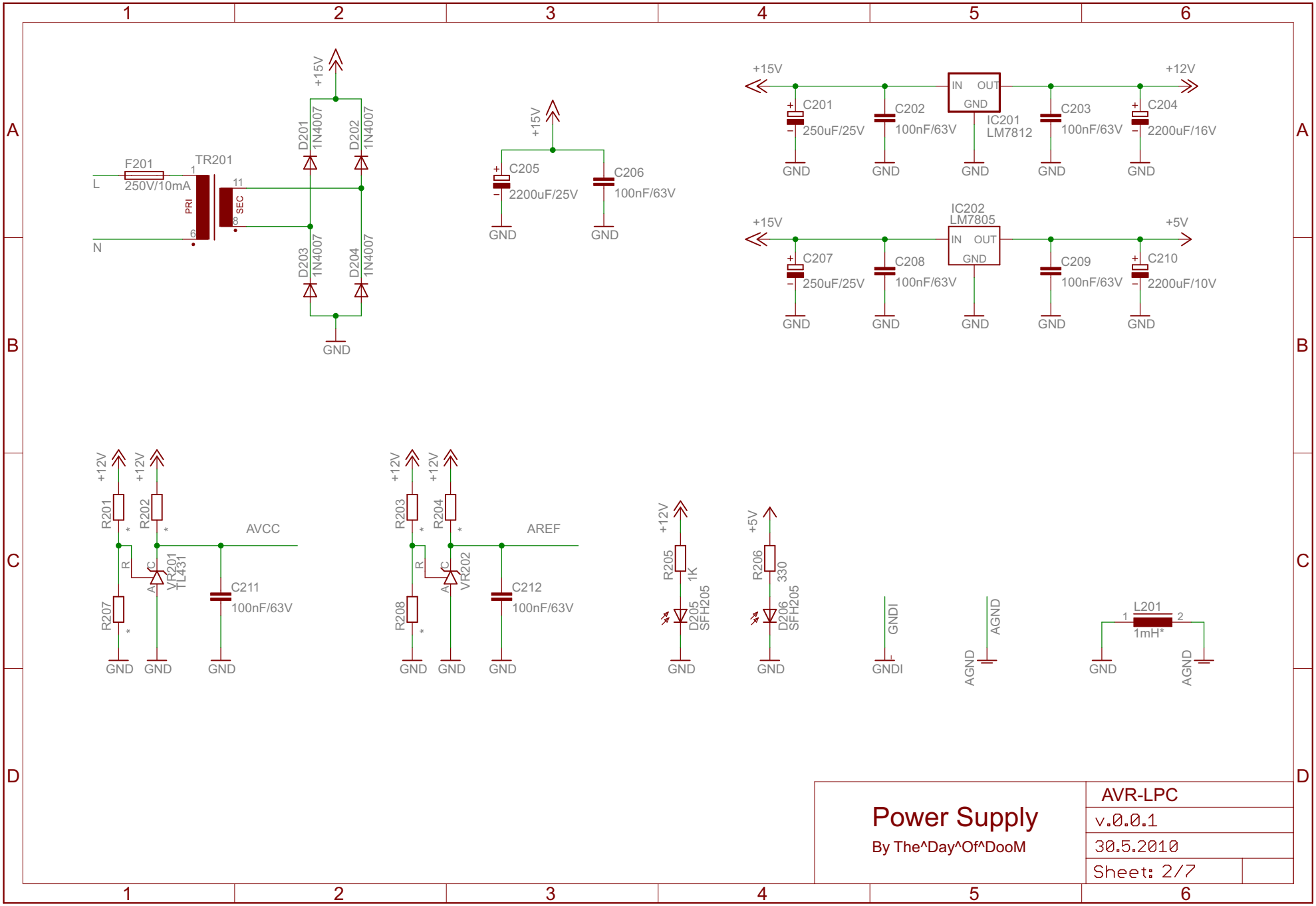
1
2
3
4
5
6
A
B
C
D
F201
250V/10mA
L
N
TR201
PRI
SEC
D201 1N4007
D202 1N4007
D203 1N4007
D204 1N4007
+15V
GND
C205
2200uF/25V
C206
100nF/63V
+15V
+12V
C201
250uF/25V
C202
100nF/63V
IC201
LM7812
IN
OUT
GND
C203
100nF/63V
C204
2200uF/16V
IC202
LM7805
+5V
C207
250uF/25V
C208
100nF/63V
C209
100nF/63V
C210
2200uF/10V
R201
R202
R207
R208
R203
R204
VR201
TL431
VR202
AVCC
AREF
C211
100nF/63V
C212
100nF/63V
R205
1K
D205
SFH205
R206
330
D206
SFH205
GNDI
AGND
L201
1mH*
Power Supply
By The^Day^Of^DooM
AVR-LPC
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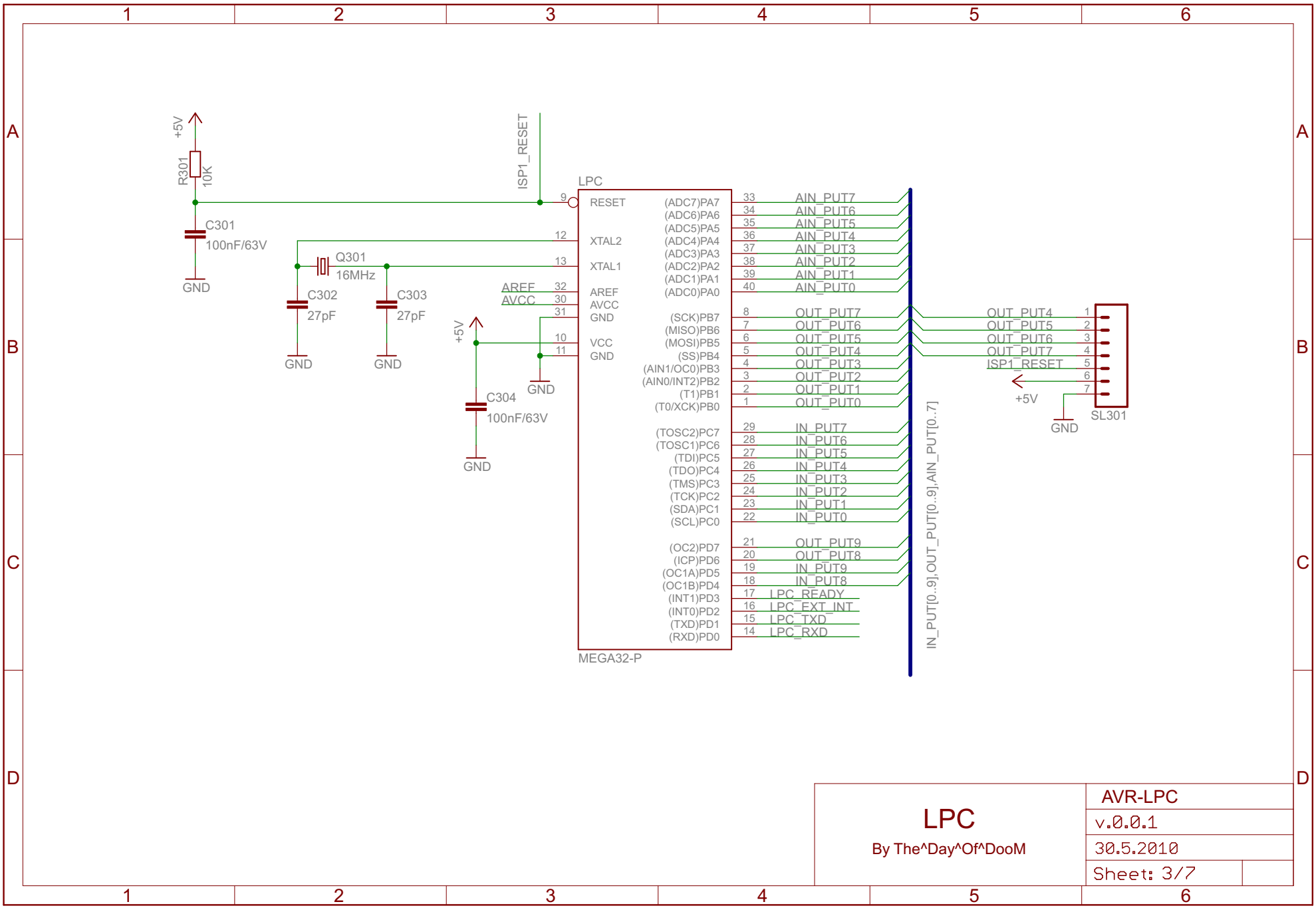
LPC
MEGA32-P
RESET
XTAL2
XTAL1
AREF
AVCC
GND
VCC
(ADC7)PA7
(ADC6)PA6
(ADC5)PA5
(ADC4)PA4
(ADC3)PA3
(ADC2)PA2
(ADC1)PA1
(ADC0)PA0
(SCK)PB7
(MISO)PB6
(MOSI)PB5
(SS)PB4
(AIN1/OC0)PB3
(AIN0/INT2)PB2
(T1)PB1
(T0/XCK)PB0
(TOSC2)PC7
(TOSC1)PC6
(TDI)PC5
(TDO)PC4
(TMS)PC3
(TCK)PC2
(SDA)PC1
(SCL)PC0
(OC2)PD7
(ICP)PD6
(OC1A)PD5
(OC1B)PD4
(INT1)PD3
(INT0)PD2
(TXD)PD1
(RXD)PD0
AIN_PUT7
AIN_PUT6
AIN_PUT5
AIN_PUT4
AIN_PUT3
AIN_PUT2
AIN_PUT1
AIN_PUT0
OUT_PUT7
OUT_PUT6
OUT_PUT5
OUT_PUT4
OUT_PUT3
OUT_PUT2
OUT_PUT1
OUT_PUT0
IN_PUT7
IN_PUT6
IN_PUT5
IN_PUT4
IN_PUT3
IN_PUT2
IN_PUT1
IN_PUT0
OUT_PUT9
OUT_PUT8
IN_PUT9
IN_PUT8
LPC_READY
LPC_EXT_INT
LPC_TXD
LPC_RXD
IN_PUT[0..9],OUT_PUT[0..9],AIN_PUT[0..7]
ISP1_RESET
+5V
R301
10K
C301
100nF/63V
Q301
16MHz
C302
27pF
C303
27pF
C304
100nF/63V
SL301
OUT_PUT4
OUT_PUT5
OUT_PUT6
OUT_PUT7
ISP1_RESET
+5V
GND
LPC
By The^Day^Of^DooM
AVR-LPC
v.0.0.1
30.5.2010
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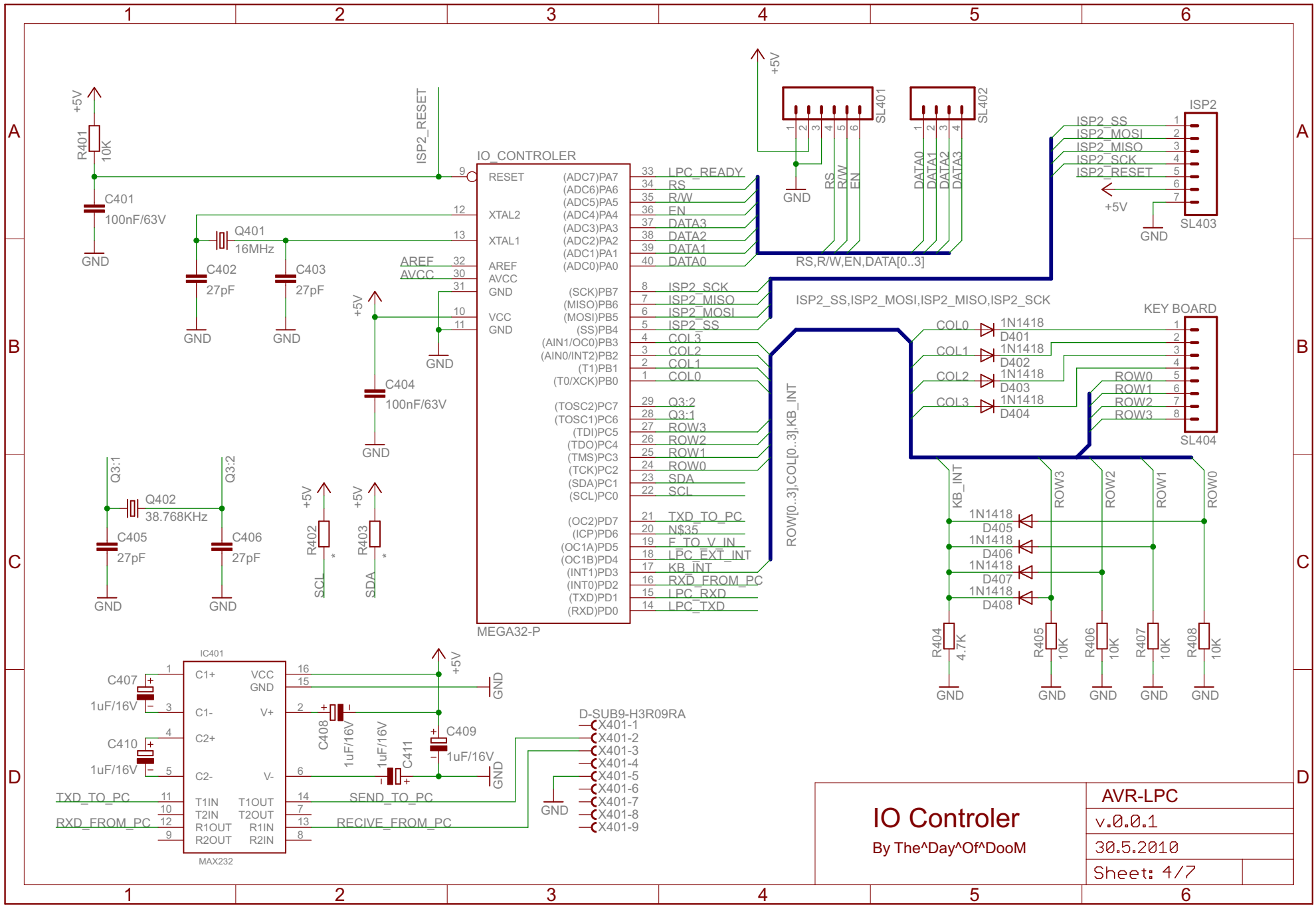
IO_CONTROLER
MEGA32-P
RESET
XTAL2
XTAL1
AREF
AVCC
GND
VCC
(ADC7)PA7
(ADC6)PA6
(ADC5)PA5
(ADC4)PA4
(ADC3)PA3
(ADC2)PA2
(ADC1)PA1
(ADC0)PA0
(SCK)PB7
(MISO)PB6
(MOSI)PB5
(SS)PB4
(AIN1/OC0)PB3
(AIN0/INT2)PB2
(T1)PB1
(T0/XCK)PB0
(TOSC2)PC7
(TOSC1)PC6
(TDI)PC5
(TDO)PC4
(TMS)PC3
(TCK)PC2
(SDA)PC1
(SCL)PC0
(OC2)PD7
(ICP)PD6
(OC1A)PD5
(OC1B)PD4
(INT1)PD3
(INT0)PD2
(TXD)PD1
(RXD)PD0
LPC_READY
RS
R/W
EN
DATA3
DATA2
DATA1
DATA0
Q3:2
Q3:1
ROW3
ROW2
ROW1
ROW0
SDA
SCL
TXD_TO_PC
N$35
F_TO_V_IN
LPC_EXT_INT
KB_INT
RXD_FROM_PC
LPC_RXD
LPC_TXD
ISP2_RESET
R401 10K
C401 100nF/63V
Q401 16MHz
C402 27pF
C403 27pF
C404 100nF/63V
Q402 38.768KHz
C405 27pF
C406 27pF
R402 *
R403 *
SL401
SL402
RS,R/W,EN,DATA[0..3]
ISP2
ISP2_SS
ISP2_MOSI
ISP2_MISO
ISP2_SCK
ISP2_RESET
SL403
ISP2_SS,ISP2_MOSI,ISP2_MISO,ISP2_SCK
KEY BOARD
COL0
COL1
COL2
COL3
1N1418
D401
D402
D403
D404
SL404
ROW[0..3],COL[0..3],KB_INT
D405
D406
D407
D408
R404 4.7K
R405 10K
R406 10K
R407 10K
R408 10K
IC401
MAX232
C1+
C1-
C2+
C2-
VCC
V+
V-
T1IN
T2IN
R1OUT
R2OUT
T1OUT
T2OUT
R1IN
R2IN
C407 1uF/16V
C408 1uF/16V
C409 1uF/16V
C410 1uF/16V
C411 1uF/16V
SEND_TO_PC
RECIVE_FROM_PC
D-SUB9-H3R09RA
X401-1
X401-2
X401-3
X401-4
X401-5
X401-6
X401-7
X401-8
X401-9
IO Controler
By The^Day^Of^DooM
AVR-LPC
v.0.0.1
30.5.2010
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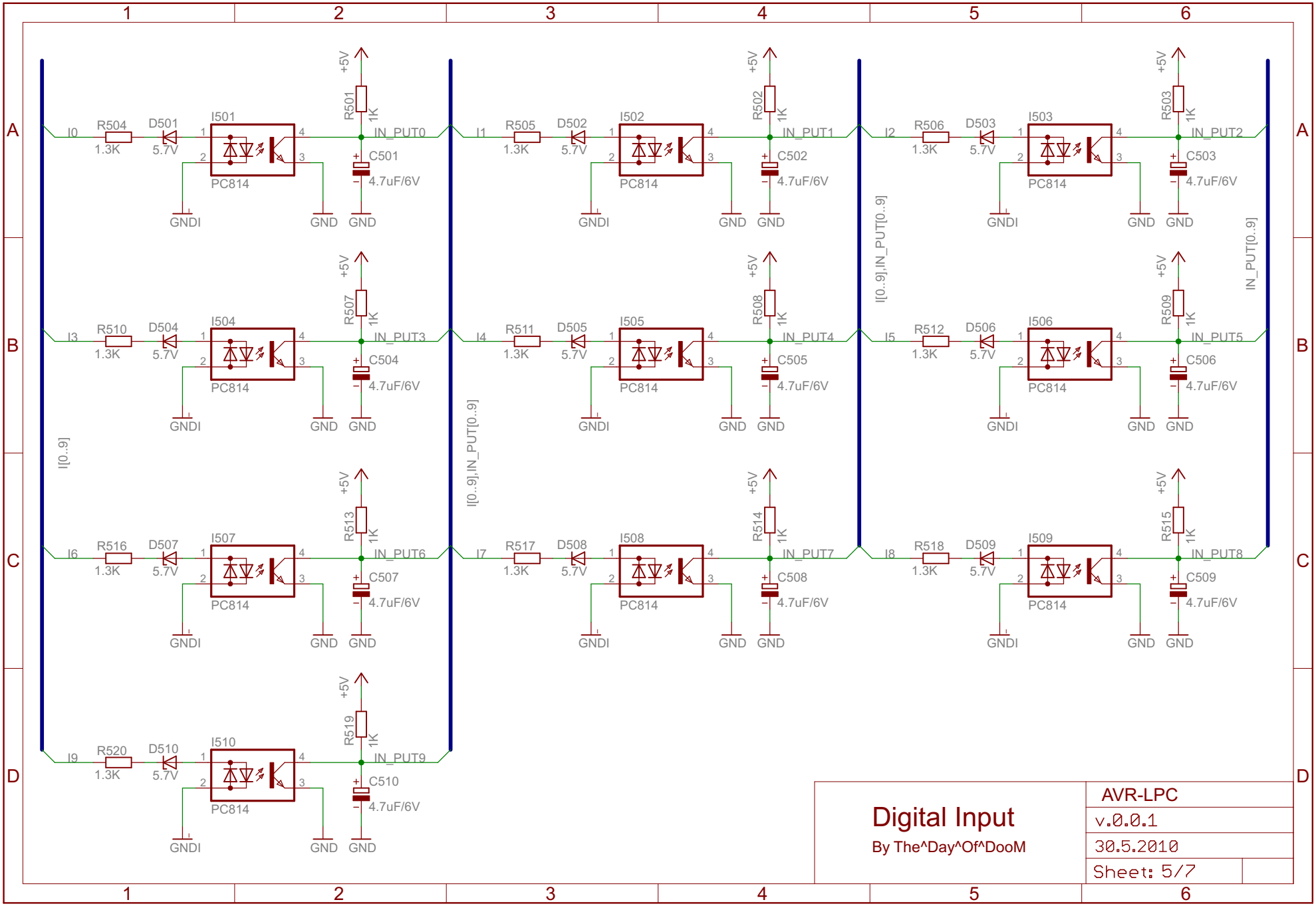
1
2
3
4
5
6
A
B
C
D
I0
I1
I2
I3
I4
I5
I6
I7
I8
I9
R504
R505
R506
R510
R511
R512
R516
R517
R518
R520
1.3K
D501
D502
D503
D504
D505
D506
D507
D508
D509
D510
5.7V
I501
I502
I503
I504
I505
I506
I507
I508
I509
I510
PC814
R501
R502
R503
R507
R508
R509
R513
R514
R515
R519
1K
+5V
IN_PUT0
IN_PUT1
IN_PUT2
IN_PUT3
IN_PUT4
IN_PUT5
IN_PUT6
IN_PUT7
IN_PUT8
IN_PUT9
C501
C502
C503
C504
C505
C506
C507
C508
C509
C510
4.7uF/6V
GNDI
GND
I[0..9]
I[0..9],IN_PUT[0..9]
IN_PUT[0..9]
Digital Input
By The^Day^Of^DooM
AVR-LPC
v.0.0.1
30.5.2010
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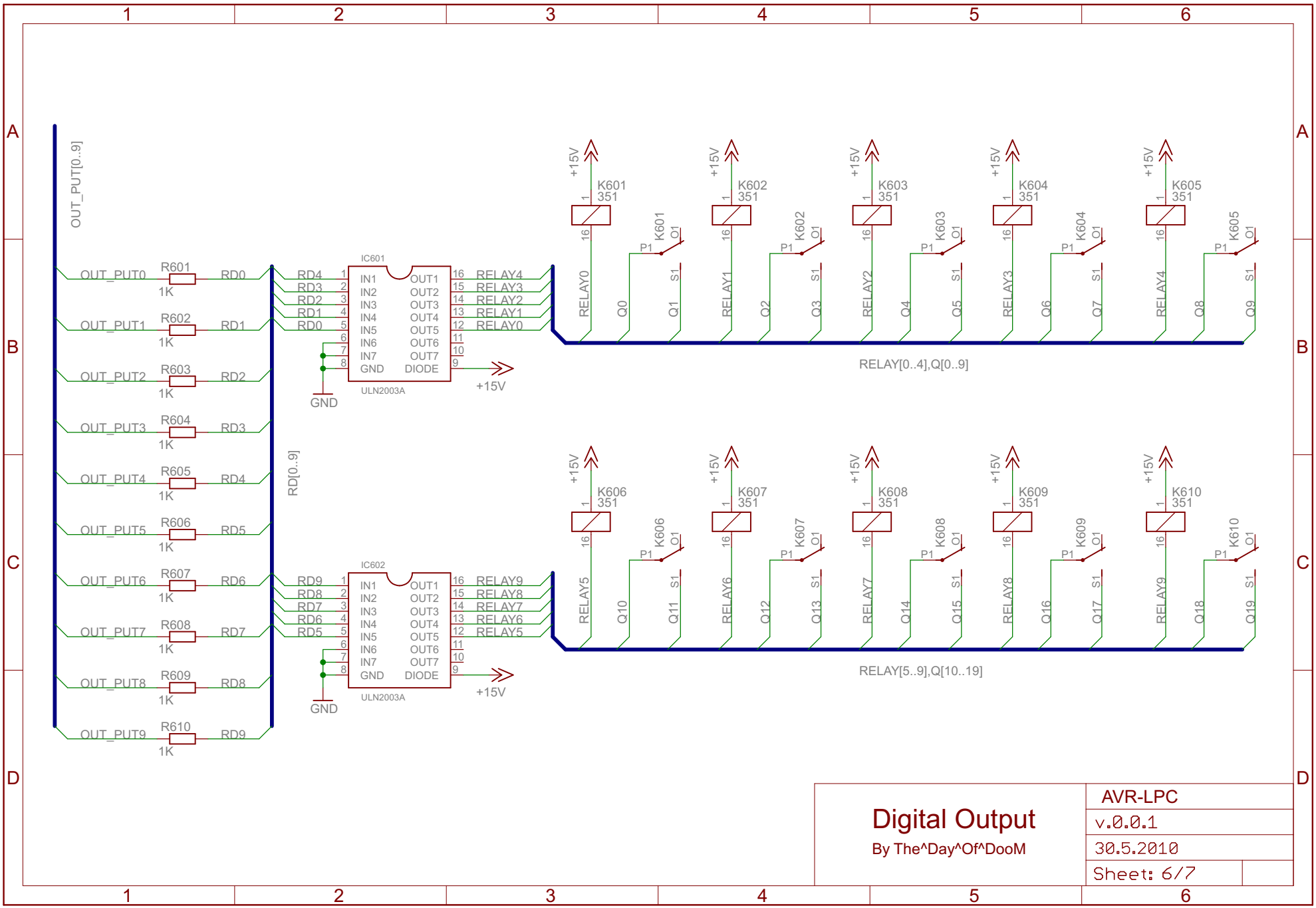
OUT_PUT[0..9]
OUT_PUT0
OUT_PUT1
OUT_PUT2
OUT_PUT3
OUT_PUT4
OUT_PUT5
OUT_PUT6
OUT_PUT7
OUT_PUT8
OUT_PUT9
R601
R602
R603
R604
R605
R606
R607
R608
R609
R610
1K
RD0
RD1
RD2
RD3
RD4
RD5
RD6
RD7
RD8
RD9
RD[0..9]
IC601
IC602
IN1
IN2
IN3
IN4
IN5
IN6
IN7
GND
OUT1
OUT2
OUT3
OUT4
OUT5
OUT6
OUT7
DIODE
ULN2003A
RELAY0
RELAY1
RELAY2
RELAY3
RELAY4
RELAY5
RELAY6
RELAY7
RELAY8
RELAY9
+15V
K601
K602
K603
K604
K605
K606
K607
K608
K609
K610
351
P1
S1
Q0
Q1
Q2
Q3
Q4
Q5
Q6
Q7
Q8
Q9
Q10
Q11
Q12
Q13
Q14
Q15
Q16
Q17
Q18
Q19
RELAY[0..4],Q[0..9]
RELAY[5..9],Q[10..19]
Digital Output
By The^Day^Of^DooM
AVR-LPC
v.0.0.1
30.5.2010
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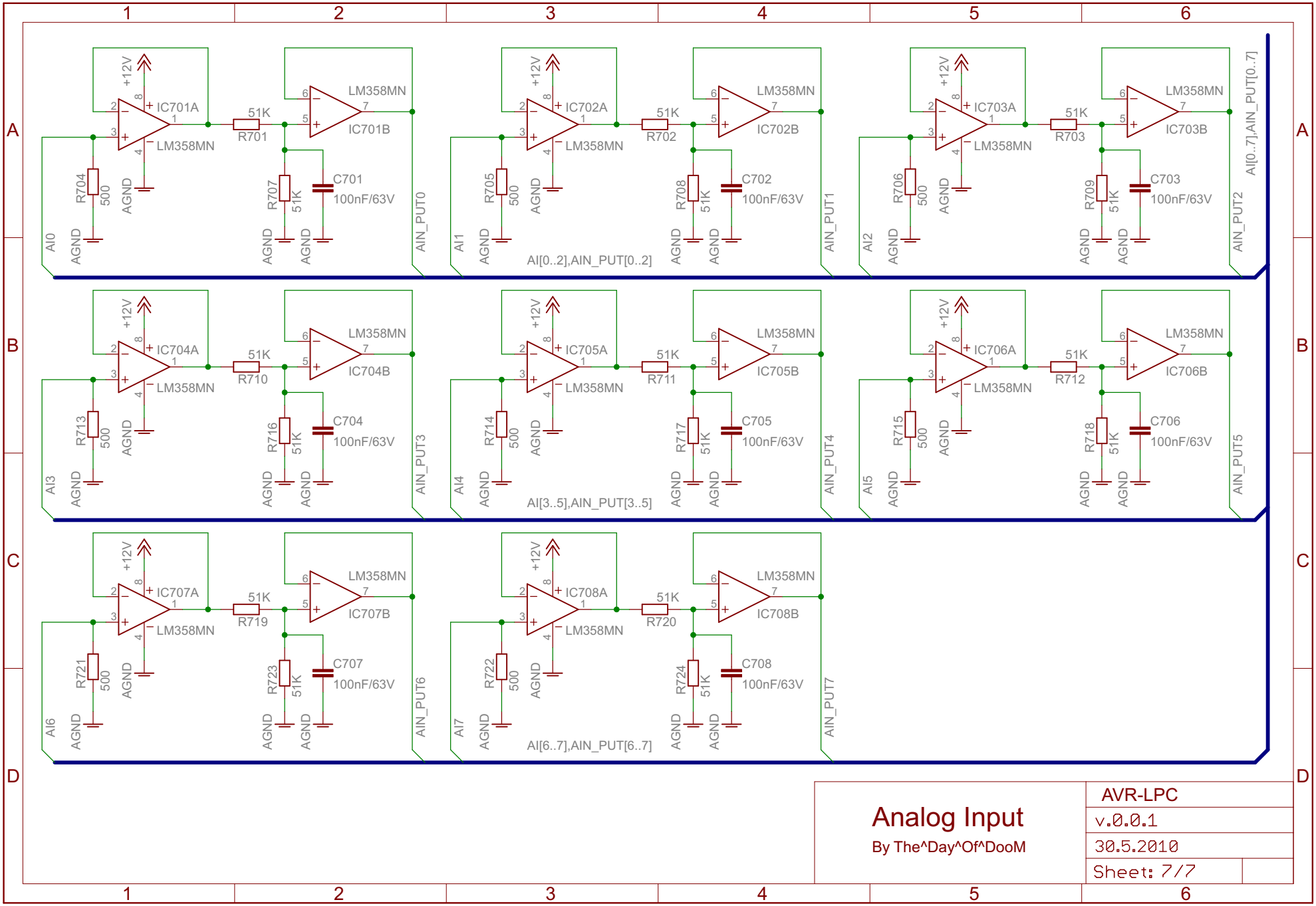
1
2
3
4
5
6
A
B
C
D
+12V
IC701A
IC701B
IC702A
IC702B
IC703A
IC703B
IC704A
IC704B
IC705A
IC705B
IC706A
IC706B
IC707A
IC707B
IC708A
IC708B
LM358MN
R701
R702
R703
R704
R705
R706
R707
R708
R709
R710
R711
R712
R713
R714
R715
R716
R717
R718
R719
R720
R721
R722
R723
R724
51K
500
C701
C702
C703
C704
C705
C706
C707
C708
100nF/63V
AGND
AI0
AI1
AI2
AI3
AI4
AI5
AI6
AI7
AIN_PUT0
AIN_PUT1
AIN_PUT2
AIN_PUT3
AIN_PUT4
AIN_PUT5
AIN_PUT6
AIN_PUT7
AI[0..2],AIN_PUT[0..2]
AI[0..7],AIN_PUT[0..7]
AI[3..5],AIN_PUT[3..5]
AI[6..7],AIN_PUT[6..7]
Analog Input
By The^Day^Of^DooM
AVR-LPC
v.0.0.1
30.5.2010
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