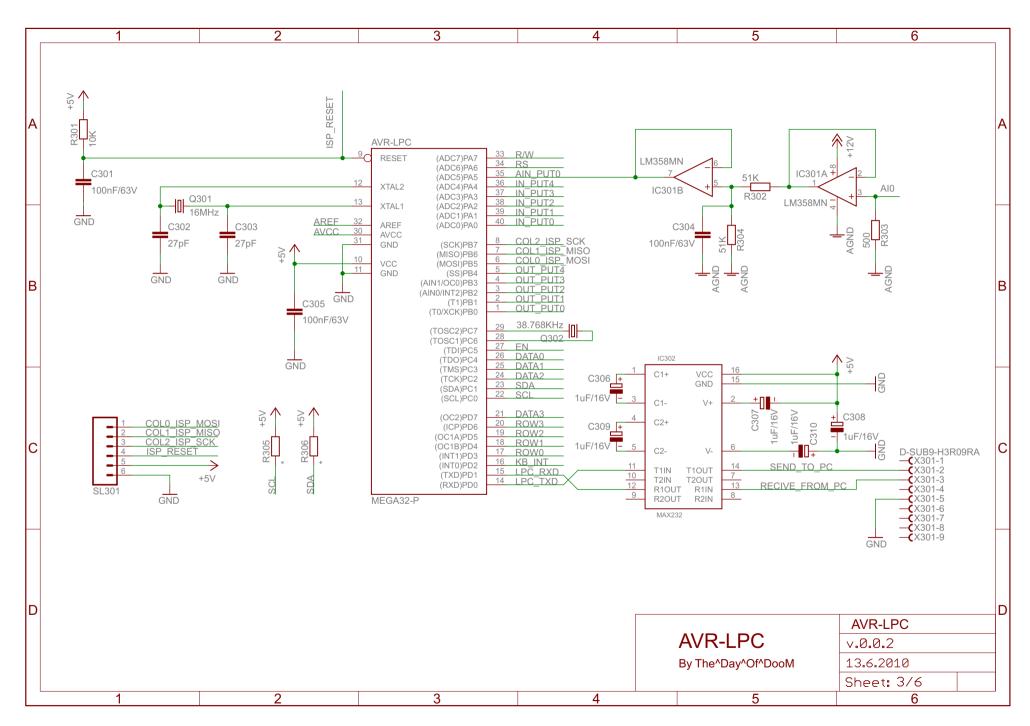
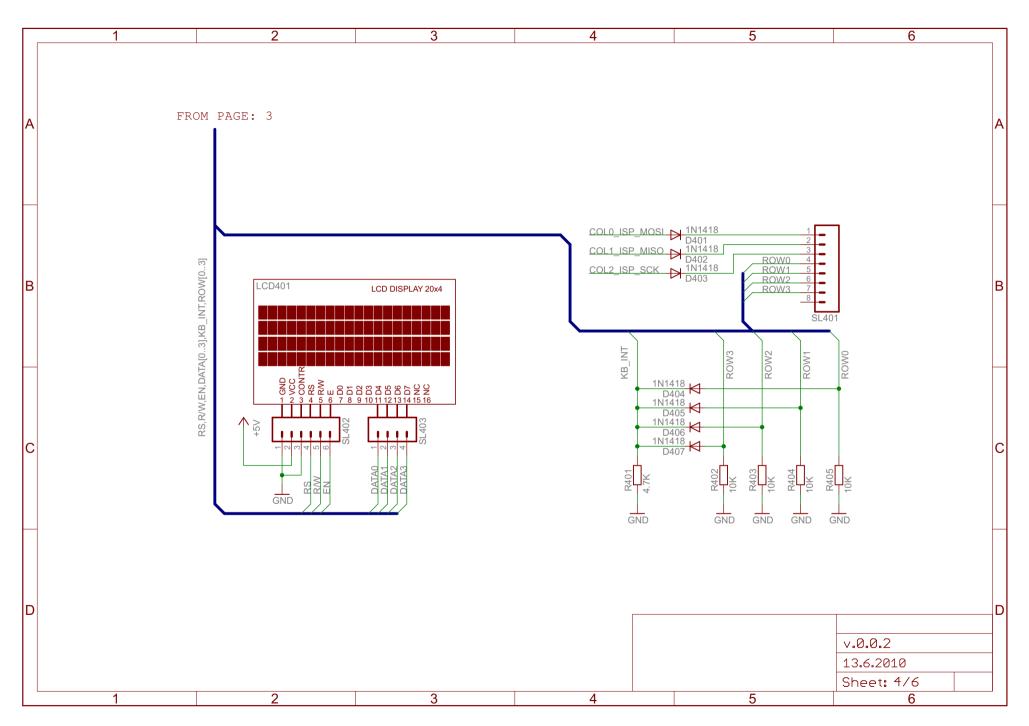


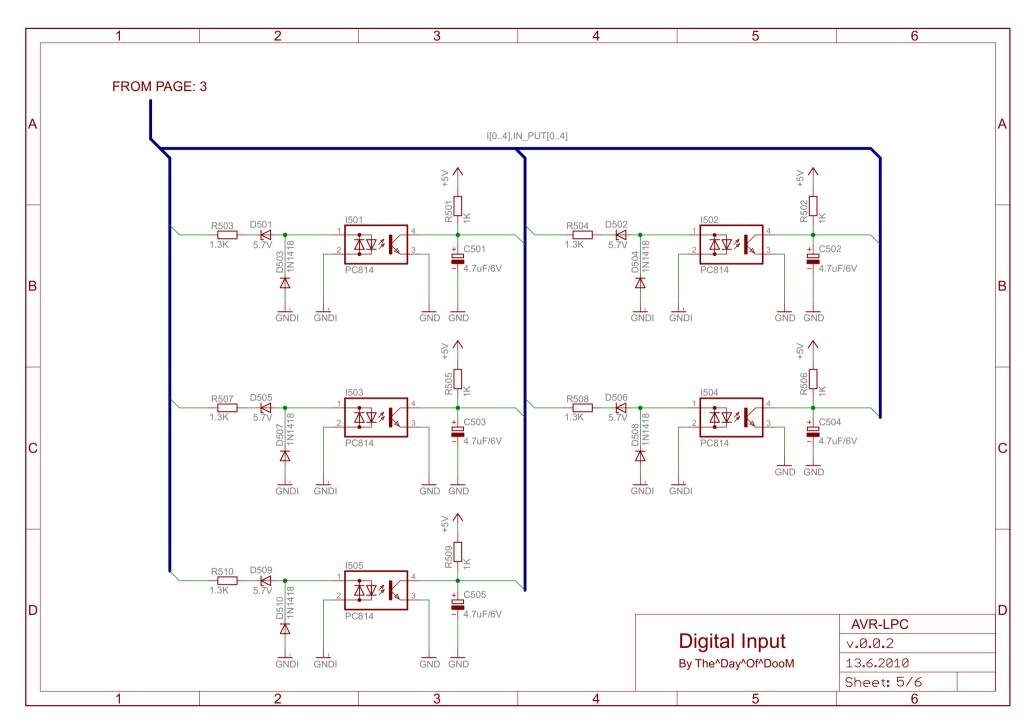
13.6.2010 r. 18:49:25 D:\Developers\Elektronika\MCU\ATMEL\AVR\AVR-LPC\Scheme and Board\v.0.0.2\v.0.0.2.sch (Sheet: 2/6)



13.6.2010 r. 18:49:25 D:\Developers\Elektronika\MCU\ATMEL\AVR\AVR-LPC\Scheme and Board\v.0.0.2\v.0.0.2.sch (Sheet: 3/6)



13.6.2010 r. 18:49:25 D:\Developers\Elektronika\MCU\ATMEL\AVR\AVR-LPC\Scheme and Board\v.0.0.2\v.0.0.2.sch (Sheet: 4/6)



13.6.2010 r. 18:49:25 D:\Developers\Elektronika\MCU\ATMEL\AVR\AVR-LPC\Scheme and Board\v.0.0.2\v.0.0.2.sch (Sheet: 5/6)

