```
`timescale 1ns / 1ps
// Company:
// Engineer:
// Create Date: 12/02/2020 05:58:51 PM
// Design Name:
// Module Name: Full Subtractor
// Project Name:
// Target Devices:
// Tool Versions:
// Description:
//
// Dependencies:
// Revision:
// Revision 0.01 - File Created
// Additional Comments:
// TEST PASSED
module Full Subtractor(
   input x, y, Bin,
   output Diff, Bout
   );
   assign Diff = (\sim x\&\sim y\&Bin) | (\sim x\&y\&\sim Bin) | (x\&\sim y\&\sim Bin) | (x\&y\&Bin);
   assign Bout = (\sim x \& \sim y \& Bin) | (\sim x \& y \& \sim Bin) | (\sim x \& y \& Bin) | (x \& y \& Bin);
```

endmodule