			15		11	10		8	7		4	3		0
			Op code (5 bit)			Operand 1 (3 bit)			Operand 2 (4 bit)			Operand 3 (4 bit)		

R type (register type)	r1	r2	r3	
	r1	r2	-	
I type (Immediate type)	r1	val2	val3	
	-	val2	val3	
RI type	r1	r2	val3	

r1
000: gr[0]
001: gr[1]
010: gr[2]
011: gr[3]
100: gr[4]
101: gr[5]
110: gr[6]
111: gr[7]

r2
x000: gr[0]
x001: gr[1]
x010: gr[2]
x011: gr[3]
x100: gr[4]
x101: gr[5]
x110: gr[6]
x111: gr[7]

r3
x000: gr[0]
x001: gr[1]
x010: gr[2]
x011: gr[3]
x100: gr[4]
x101: gr[5]
x110: gr[6]
x111: gr[7]

val2
Immediate data (4 bit)

val3
Immediate data (4 bit)